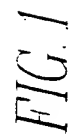


1,367



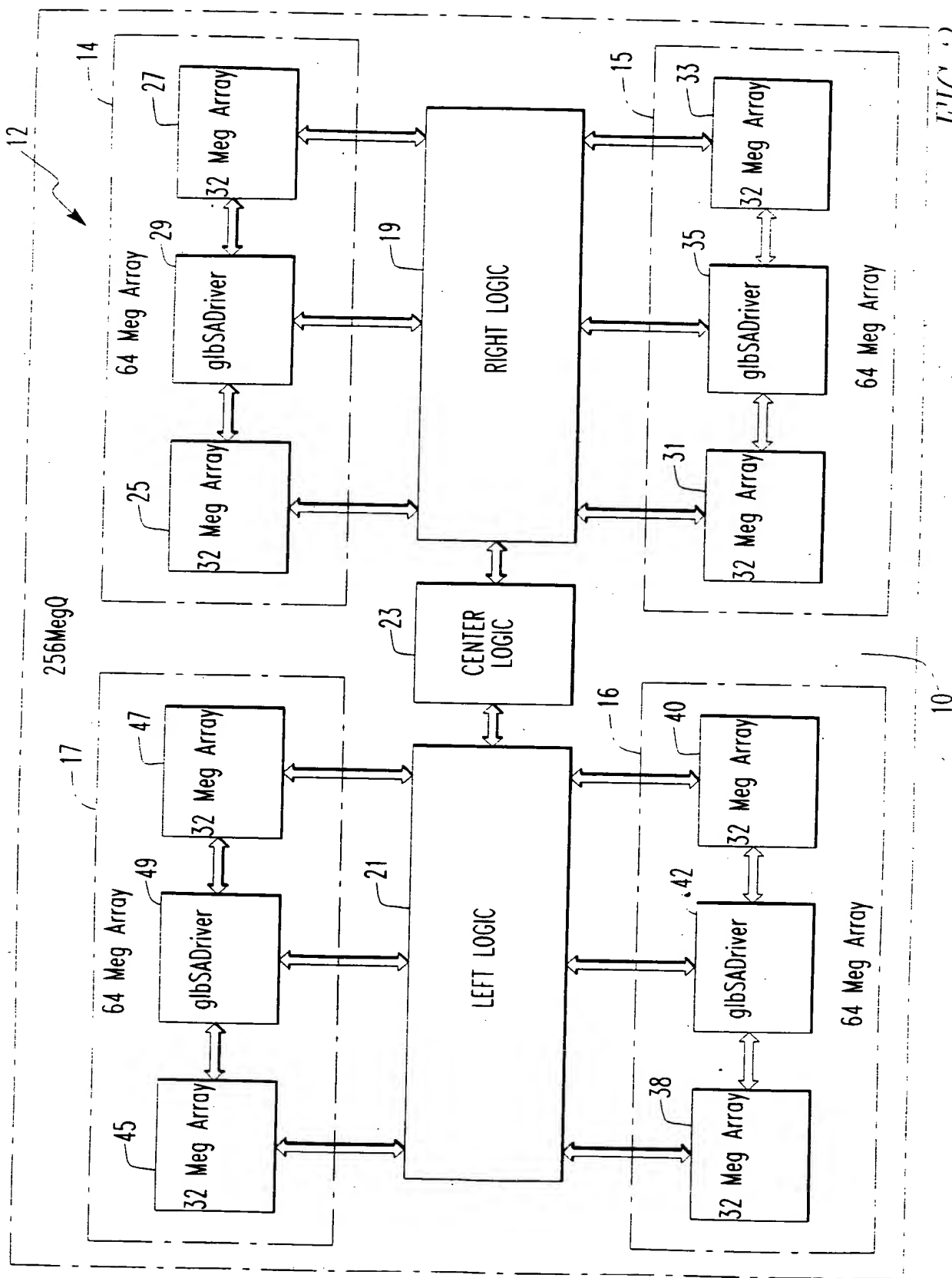
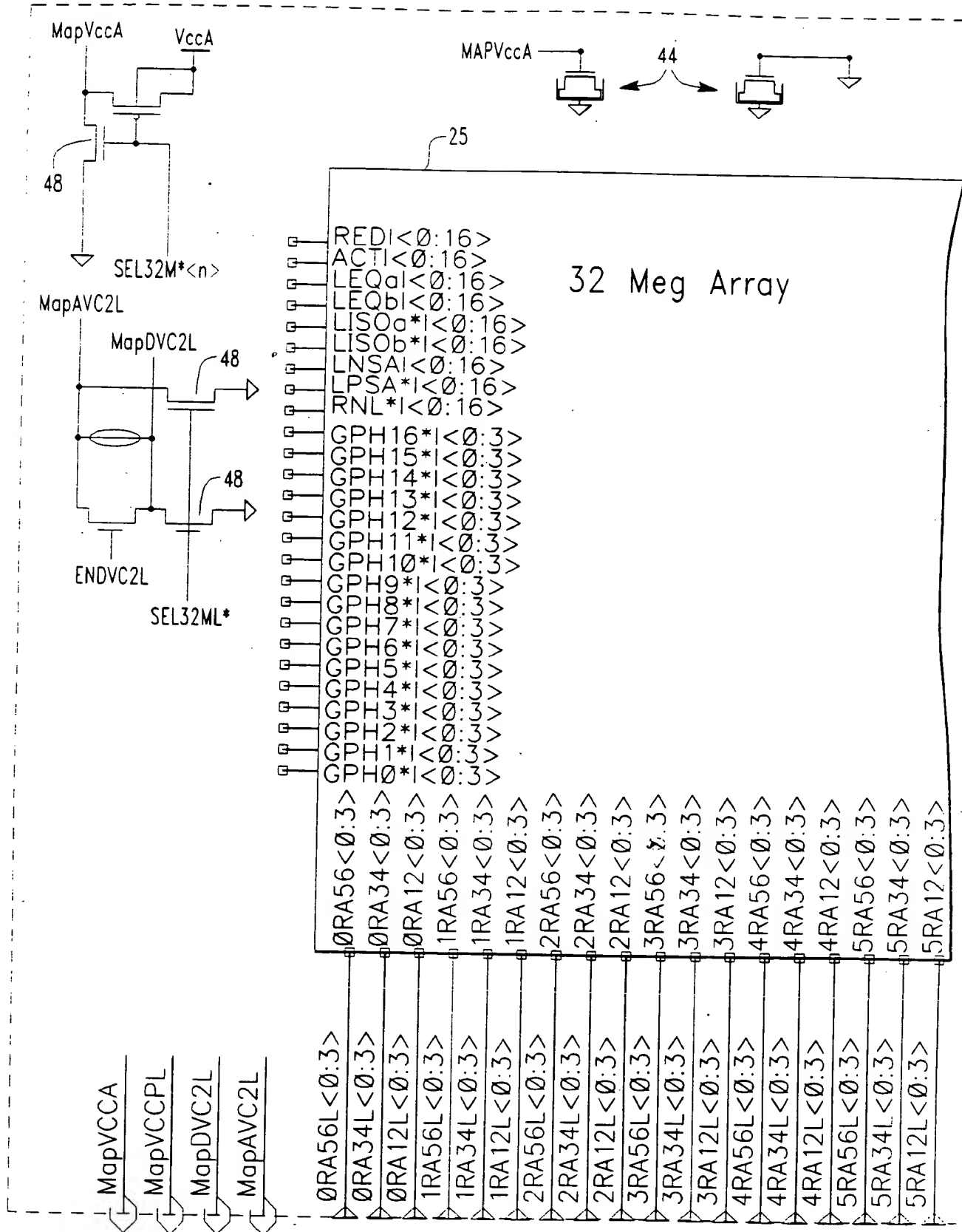
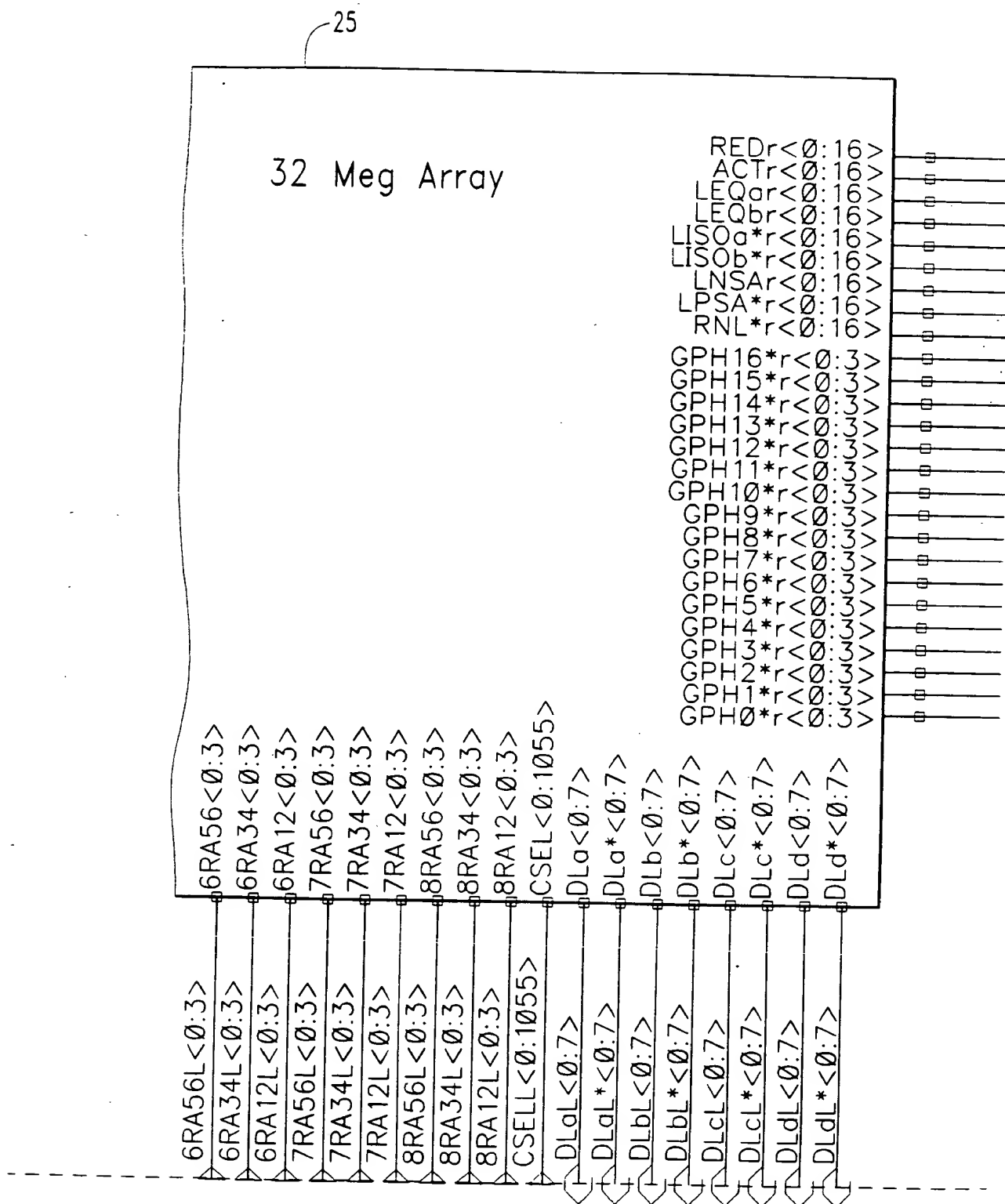


FIG. 2



.....



-29

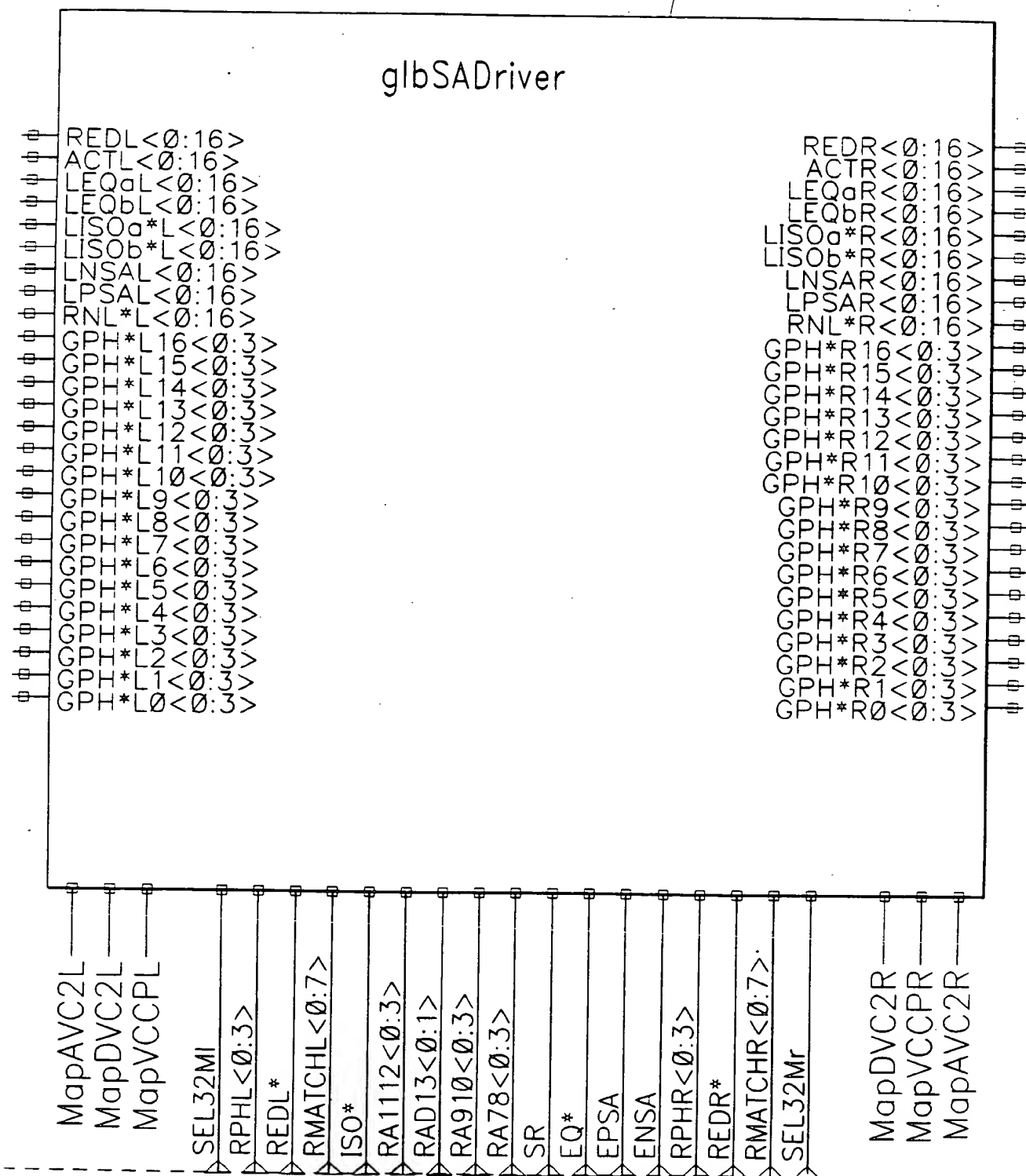
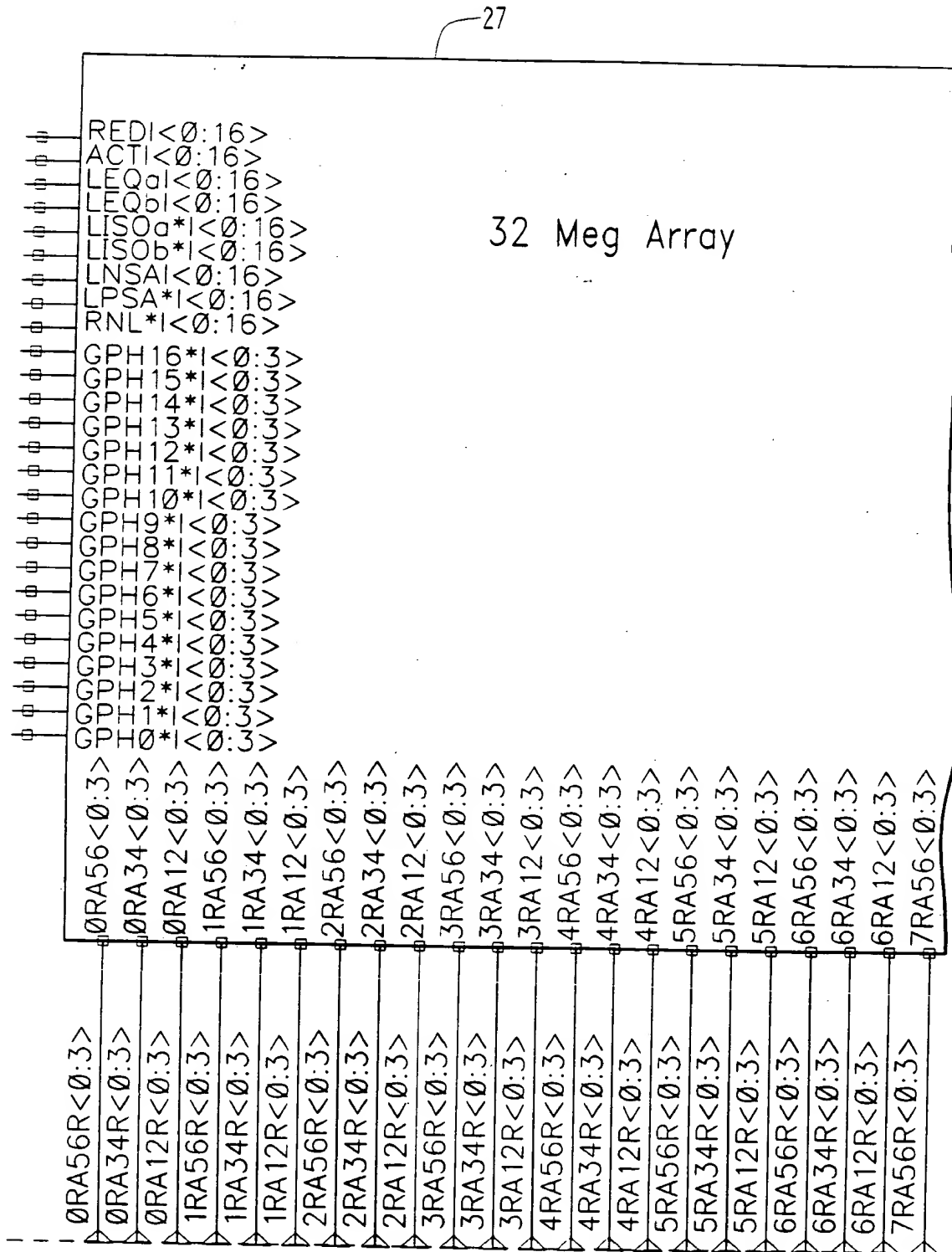
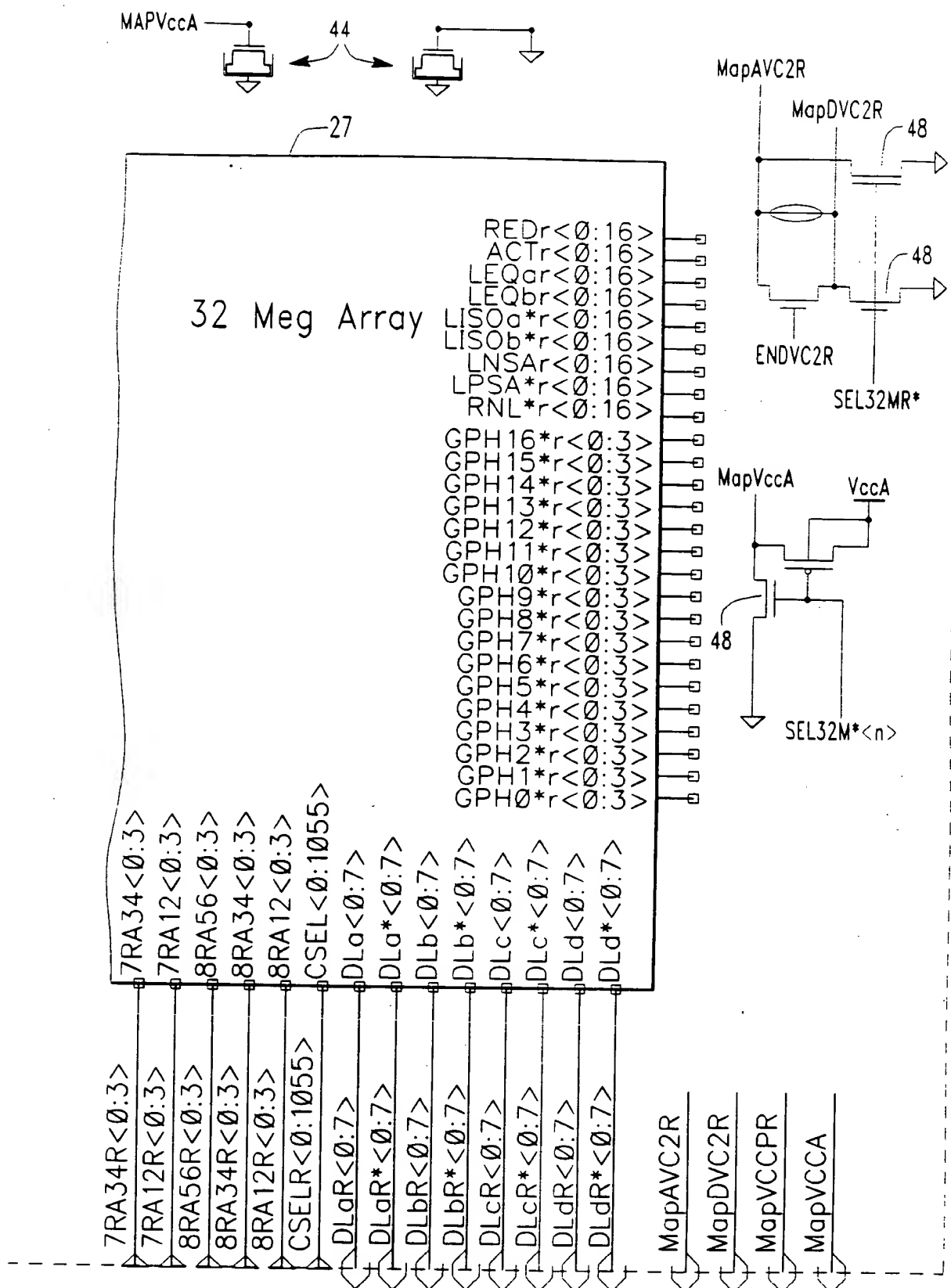


FIG. 3D

6/367

FIG. 3D





32MEG ARRAY

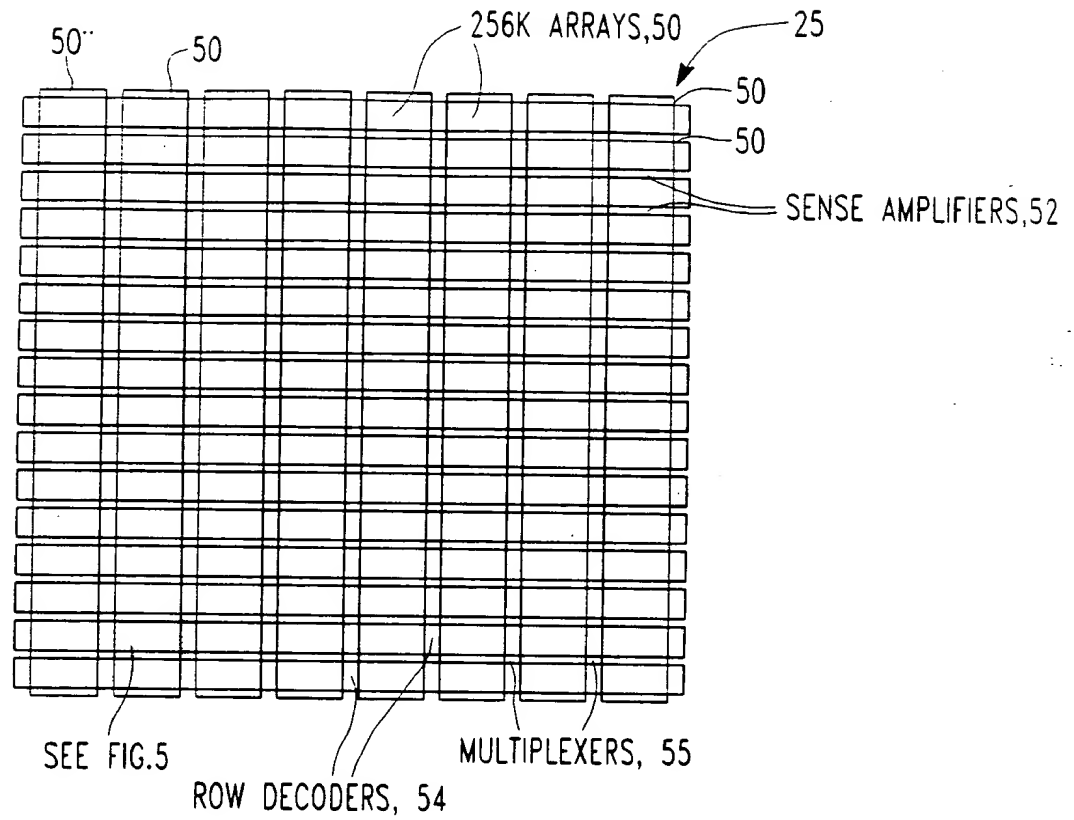


FIG. 4

TOP SECRET 5624E660

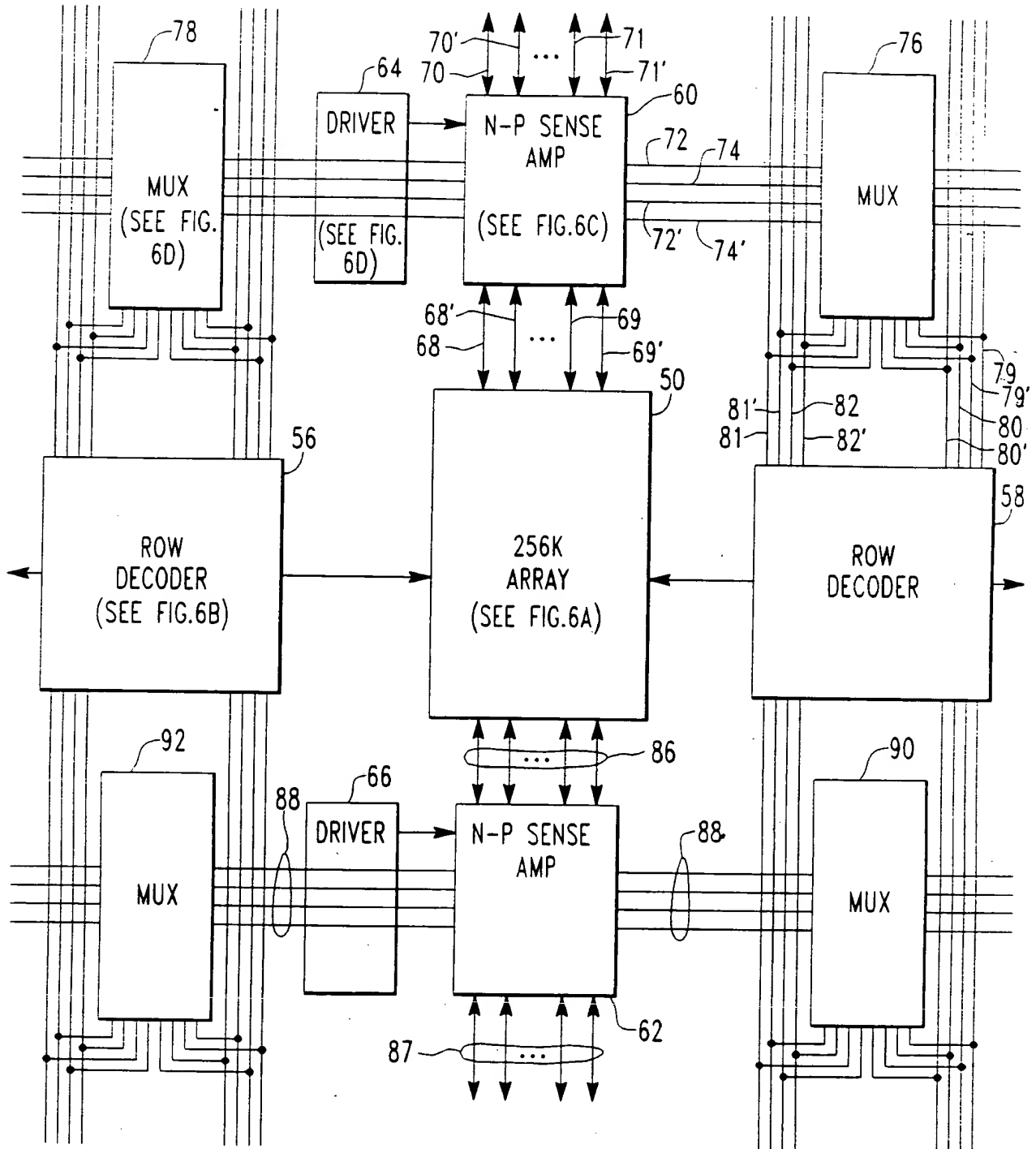


FIG. 5

TOP SECRET 5644E660

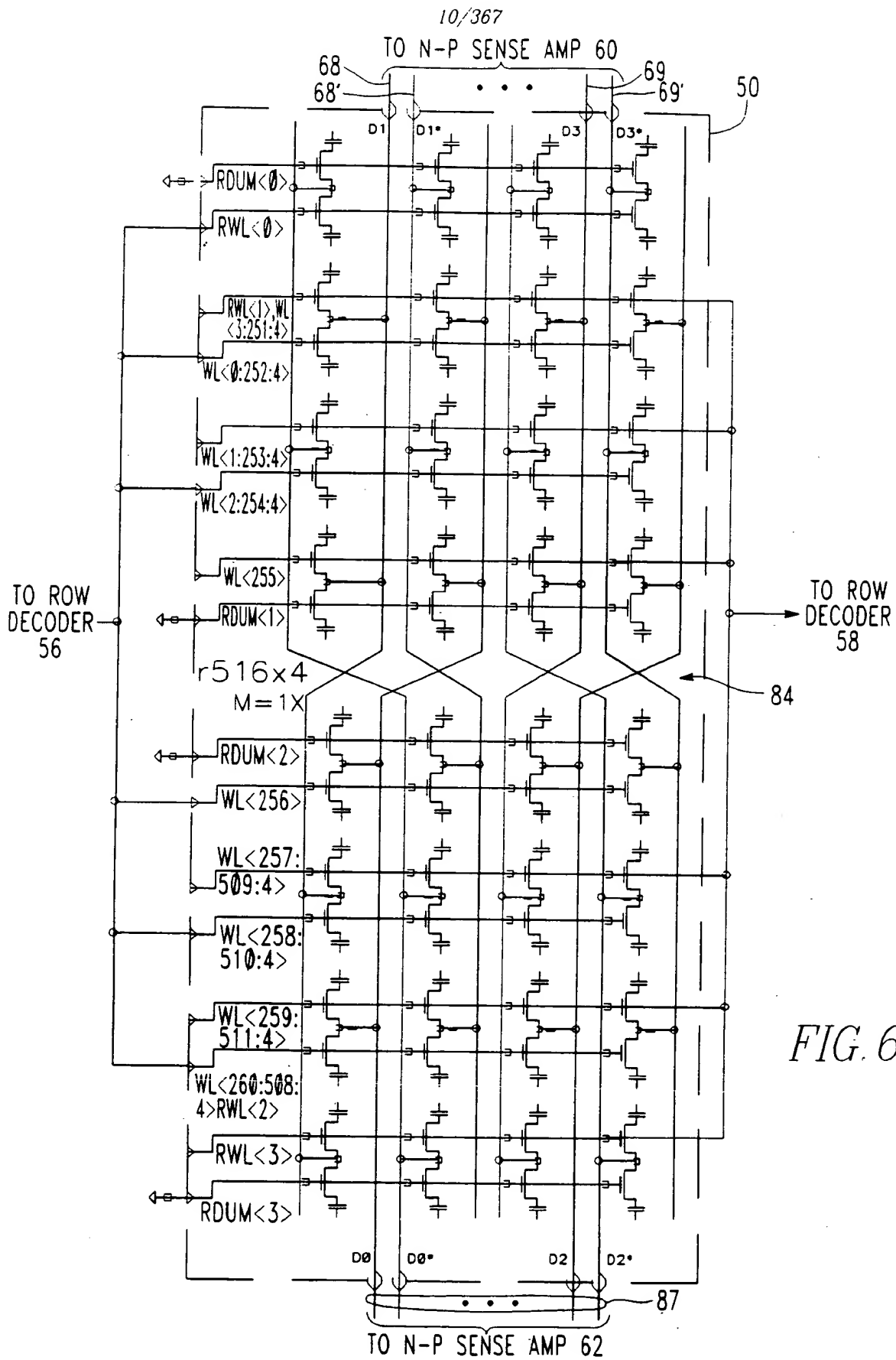


FIG. 6A

FIG. 6B

12/367

Connections of odd/even
columns to IOa and IOb
alternates with odd/even
column select lines:
CA01* D1(even) D2(odd)

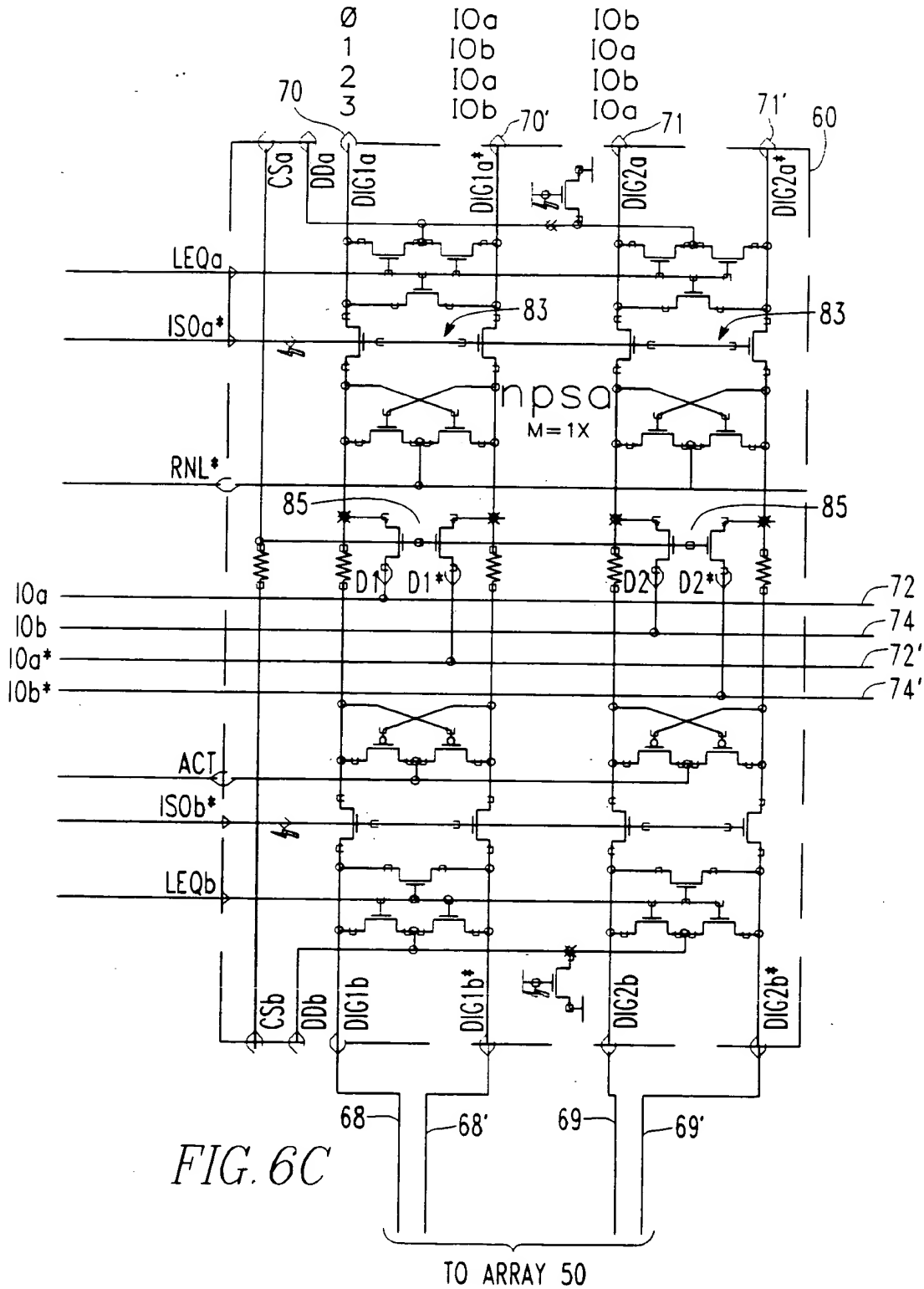


FIG. 6C

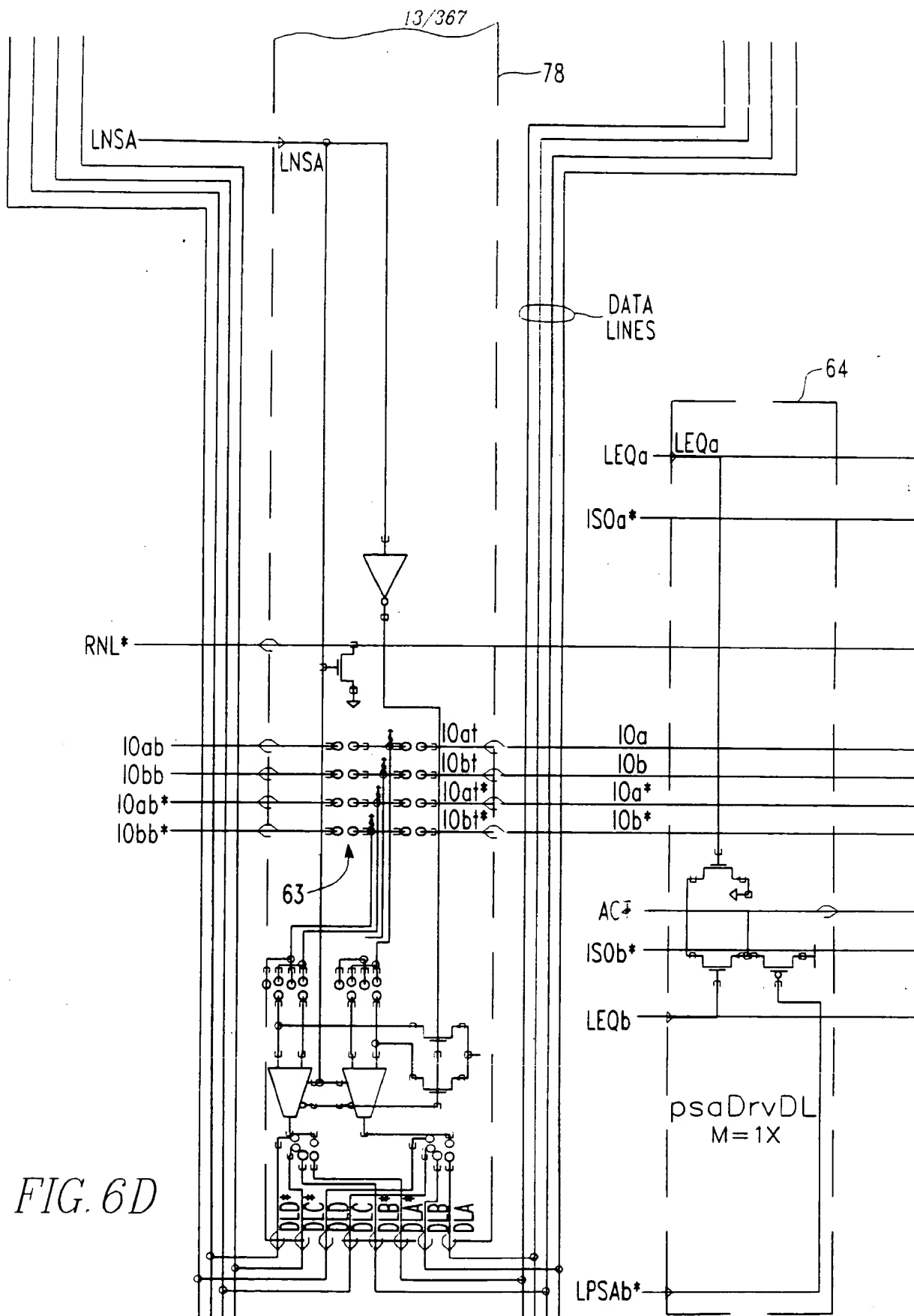


FIG. 6D

25

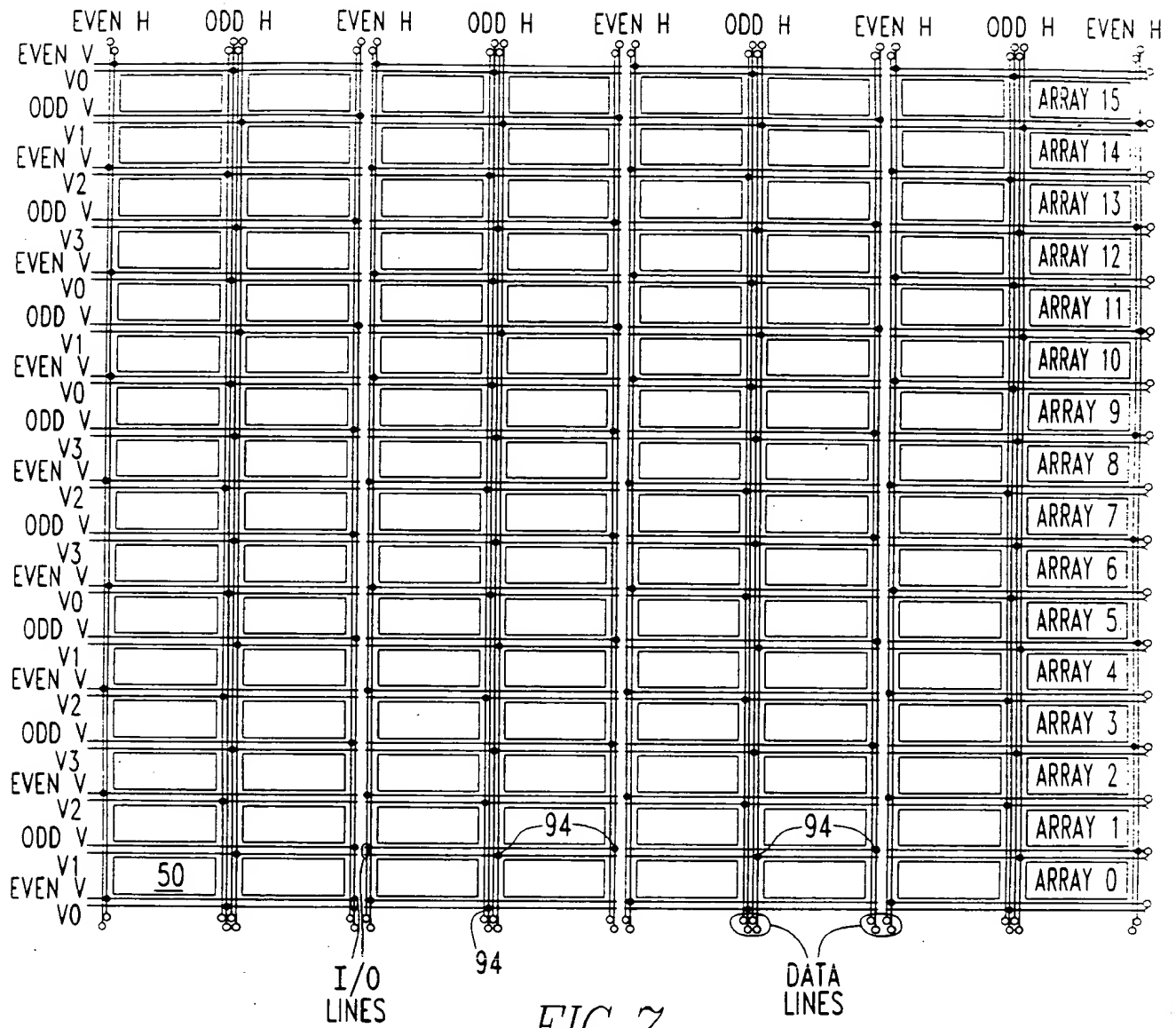


FIG. 7

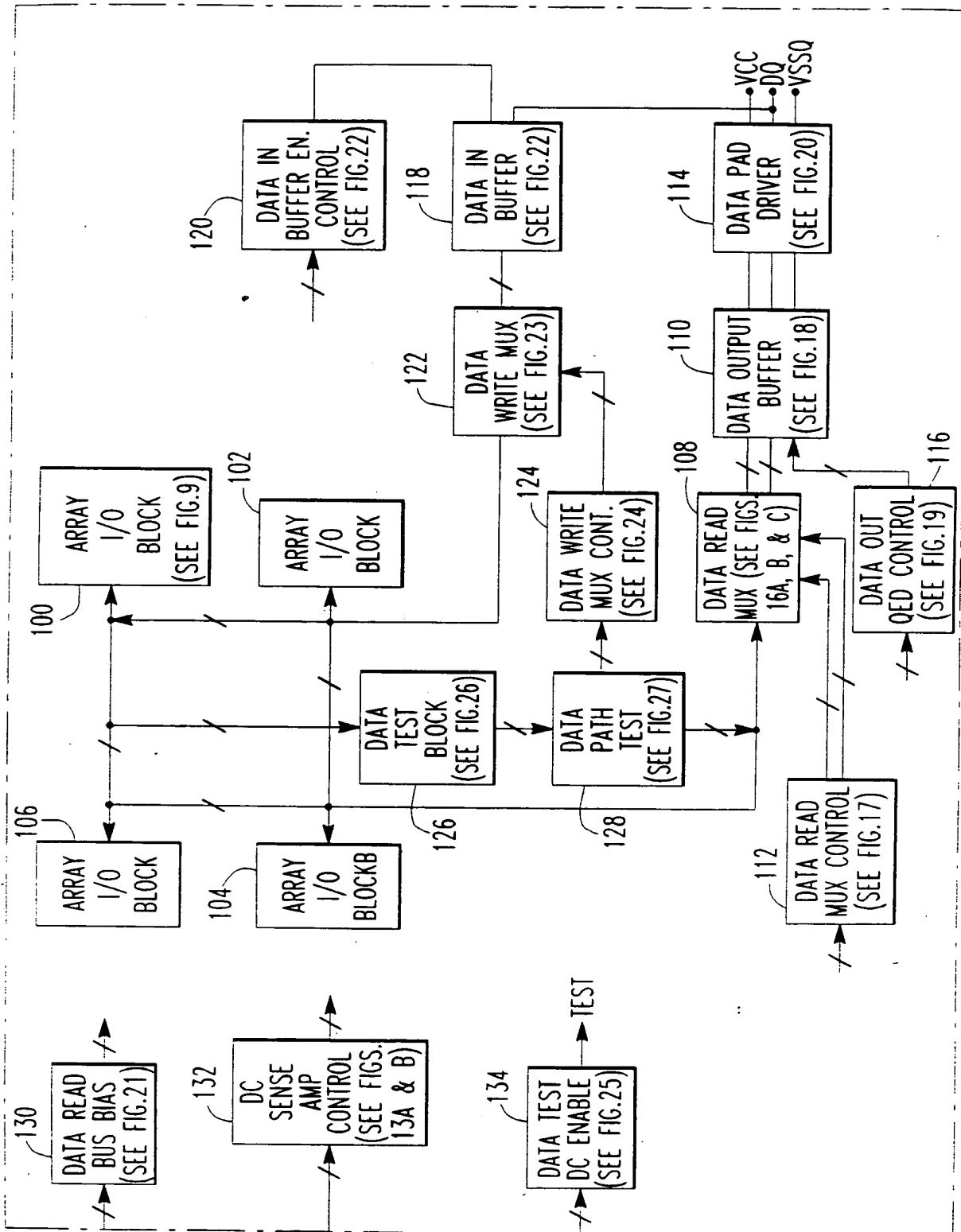


FIG. 8

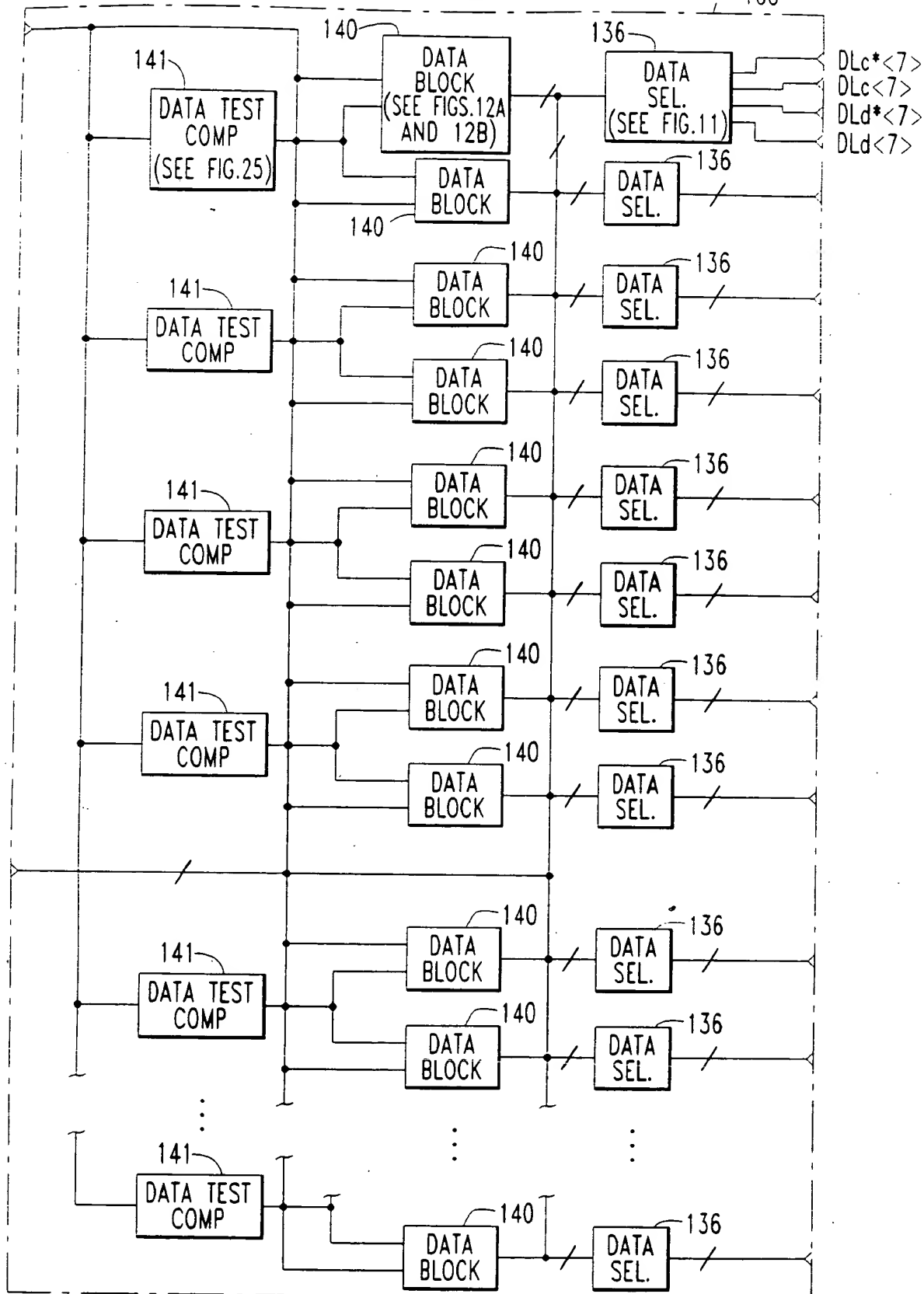


FIG. 9

FIG. 10A1

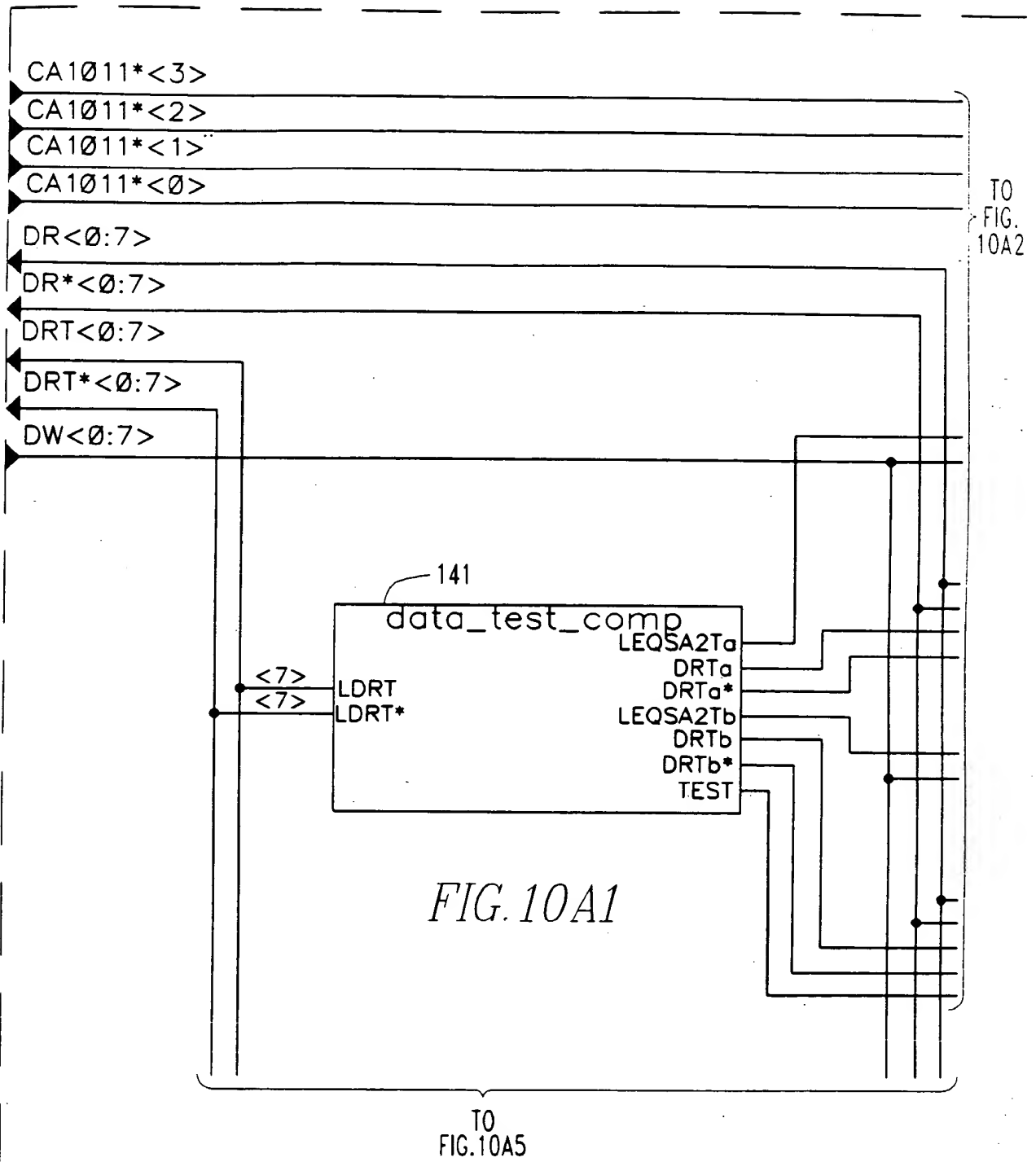
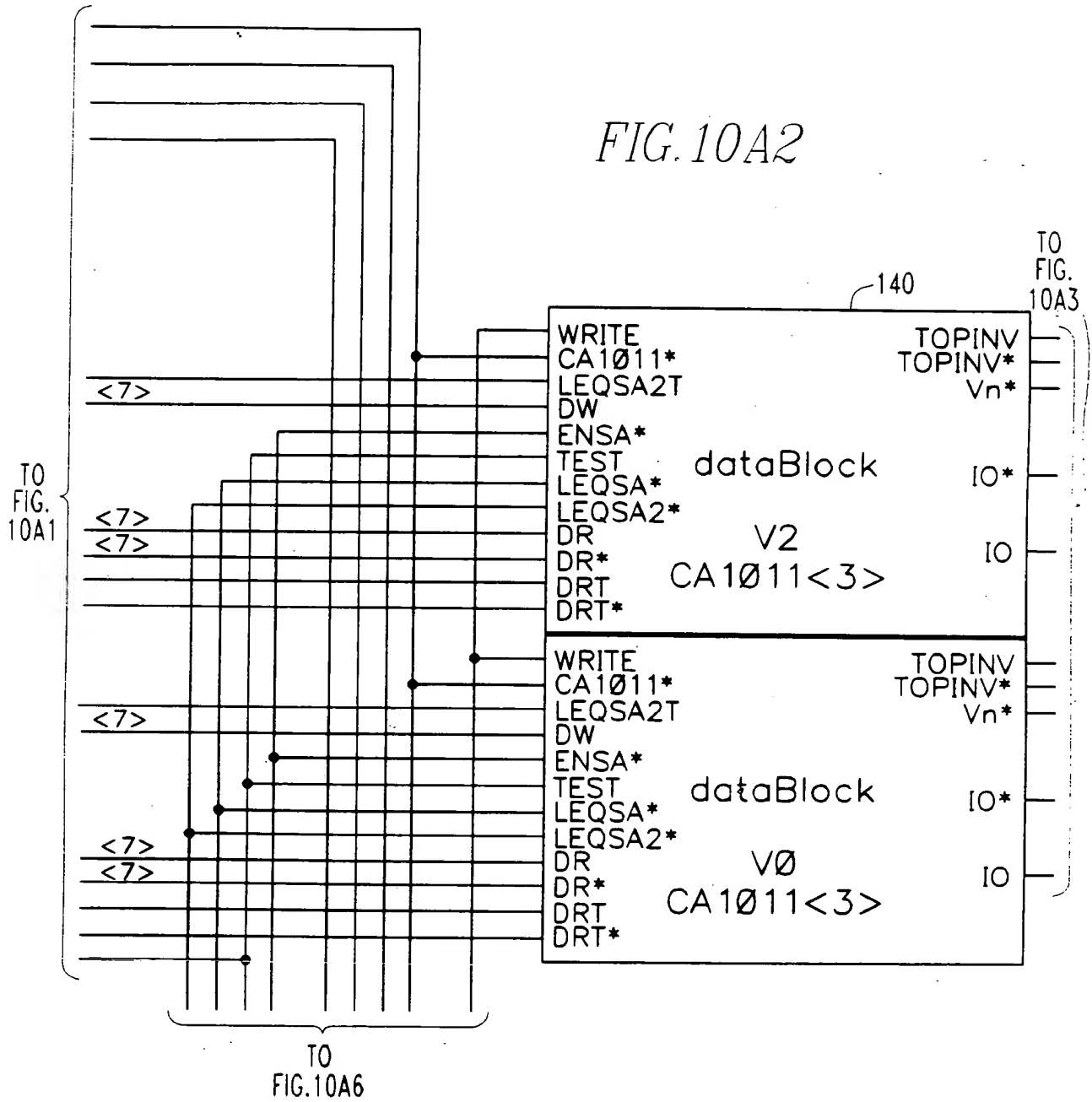
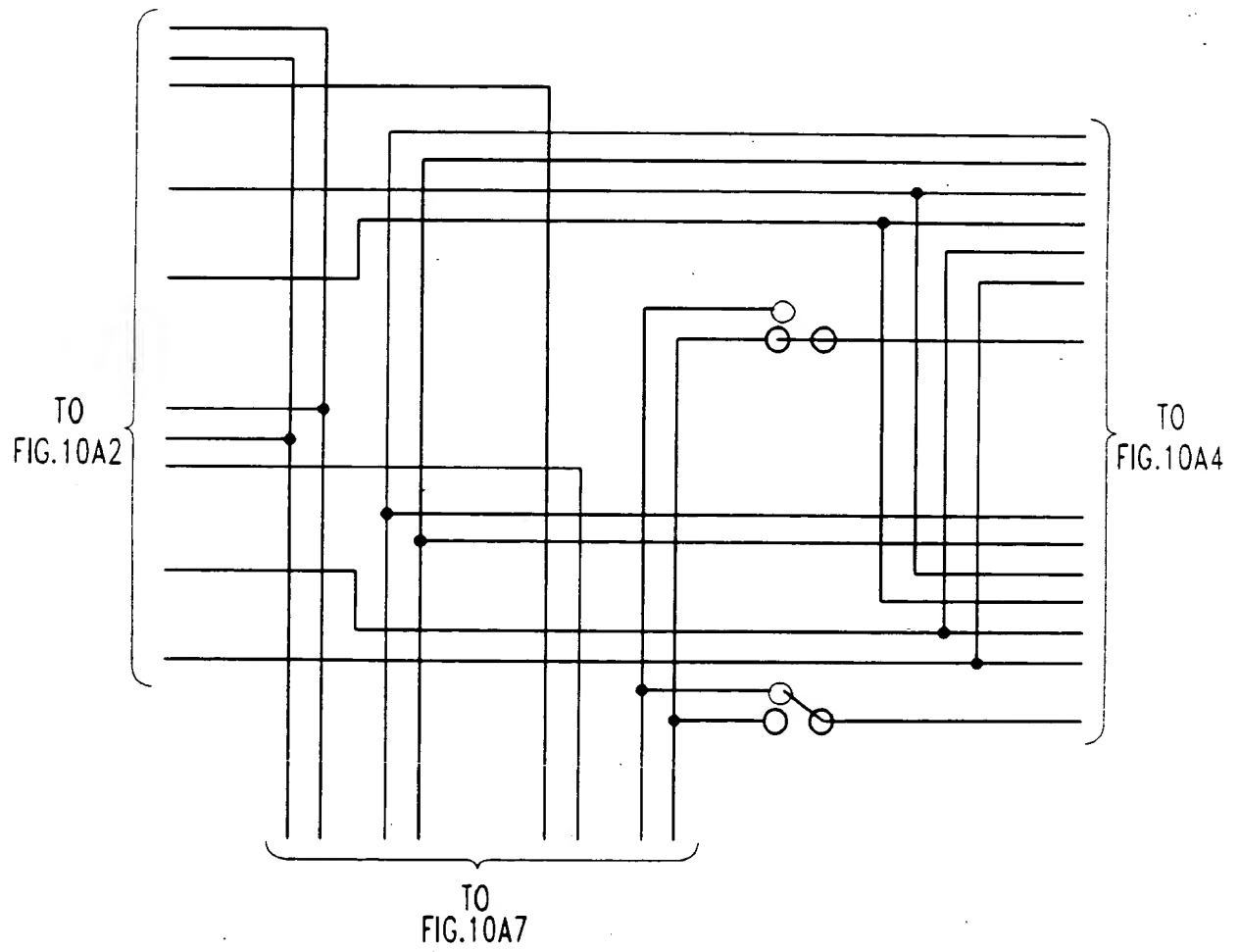


FIG. 10A2



[illegible]

arrayIOBlock

100

FIG. 10A4

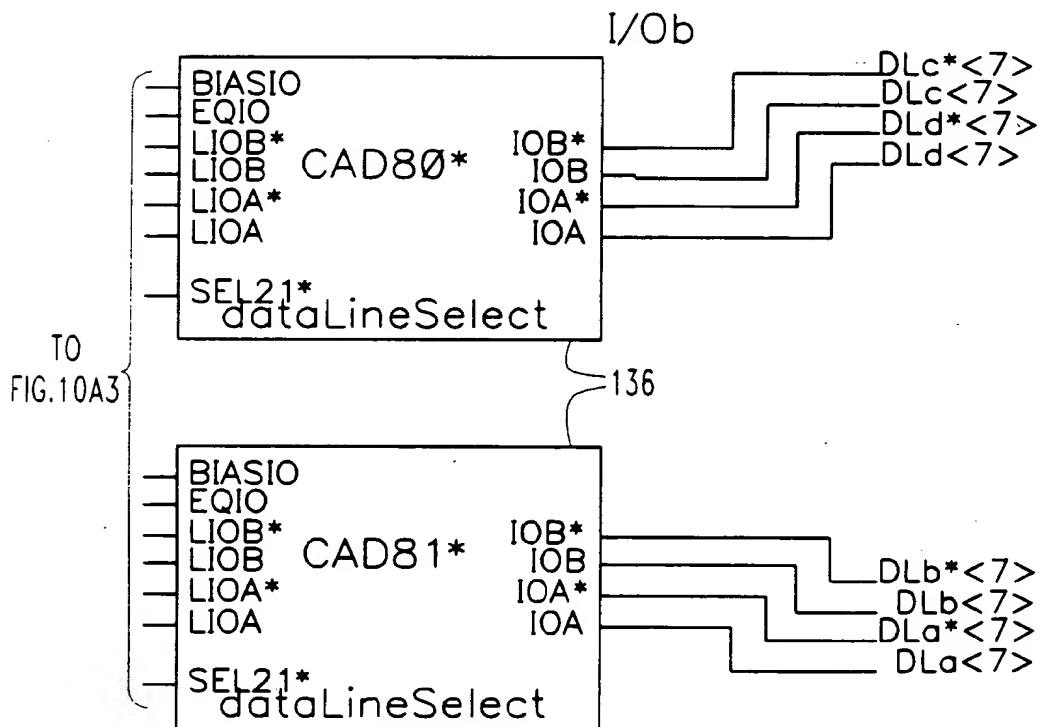


FIG. 10A5

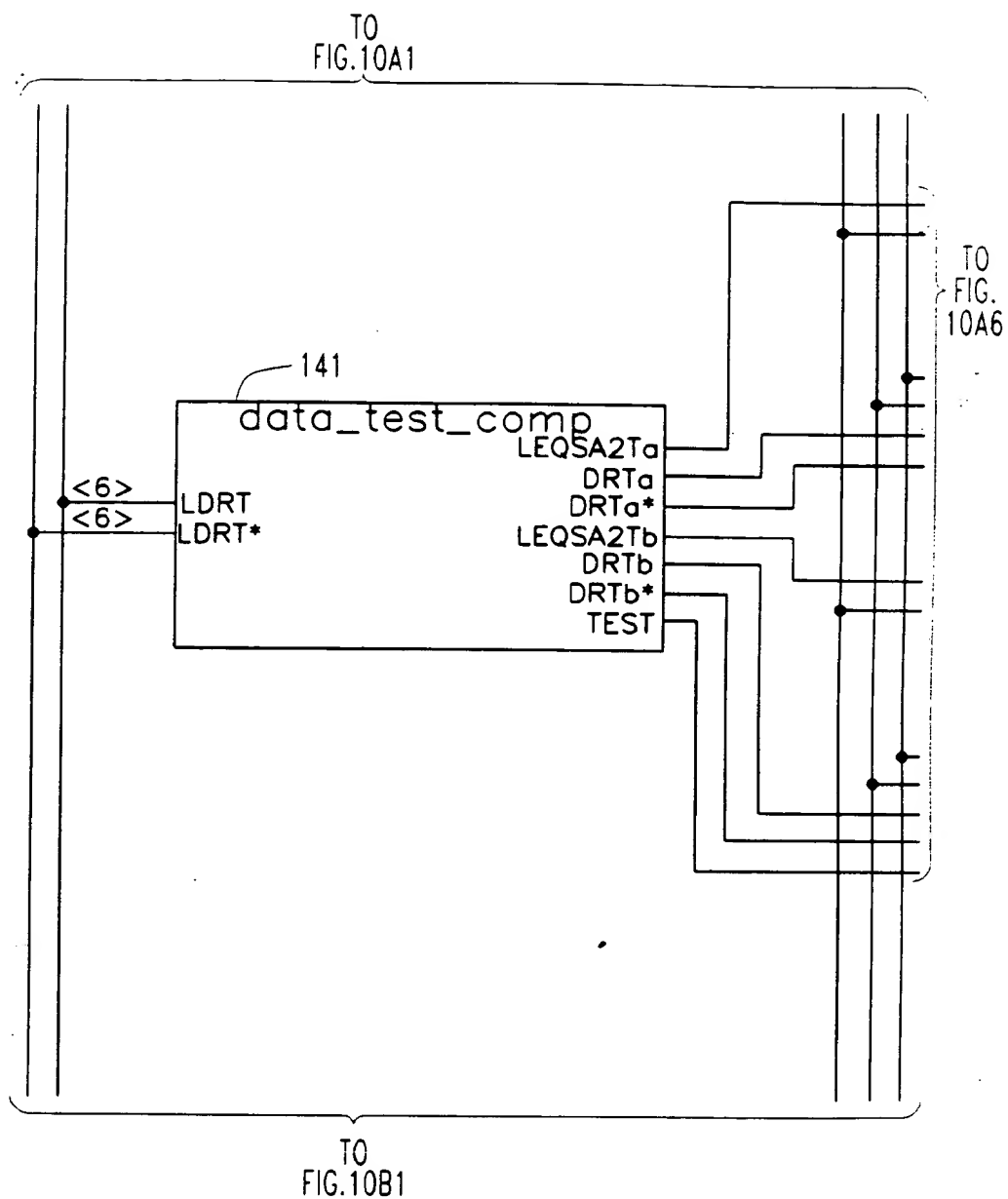


FIG. 10A5

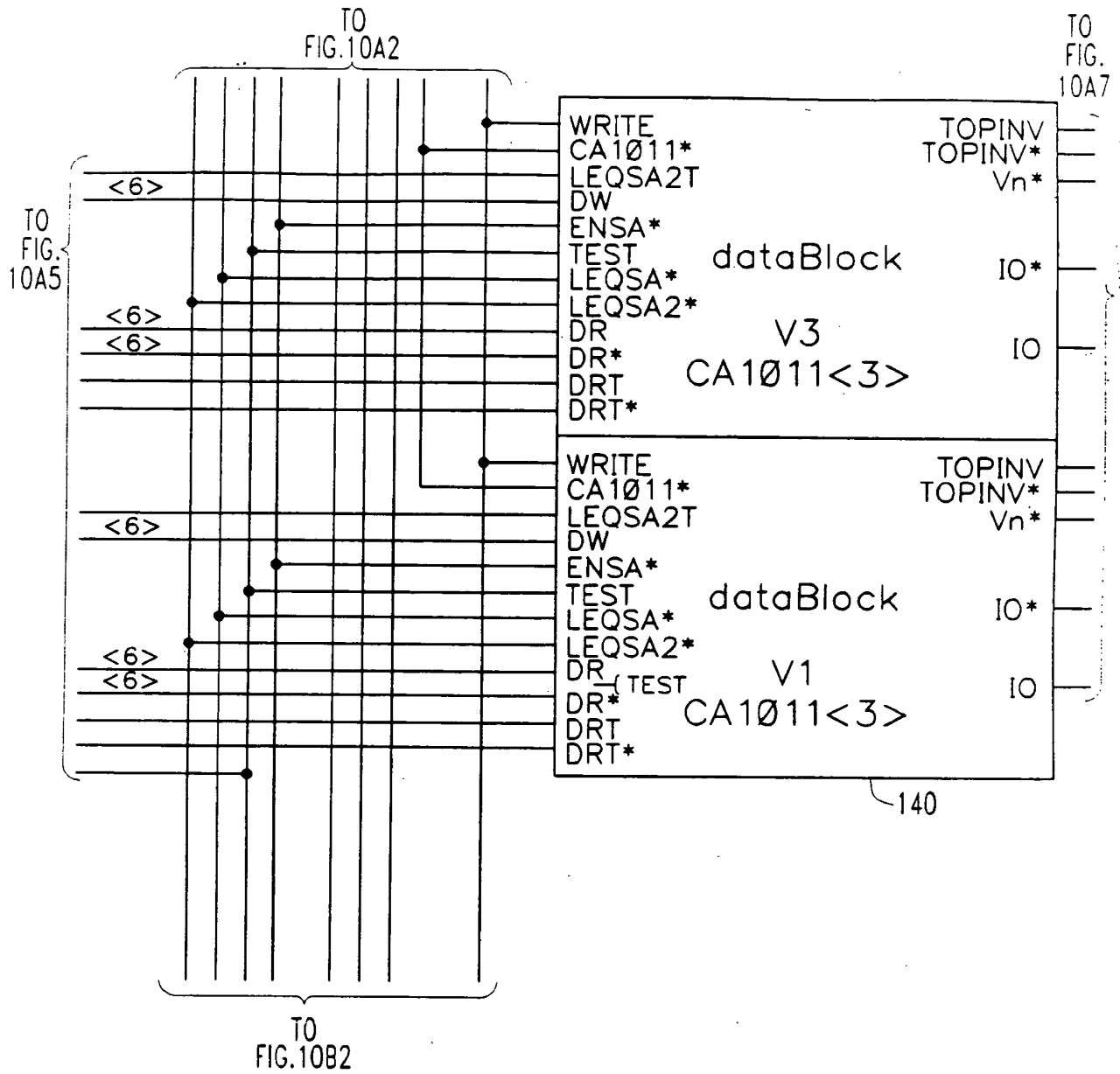


FIG. 10A6

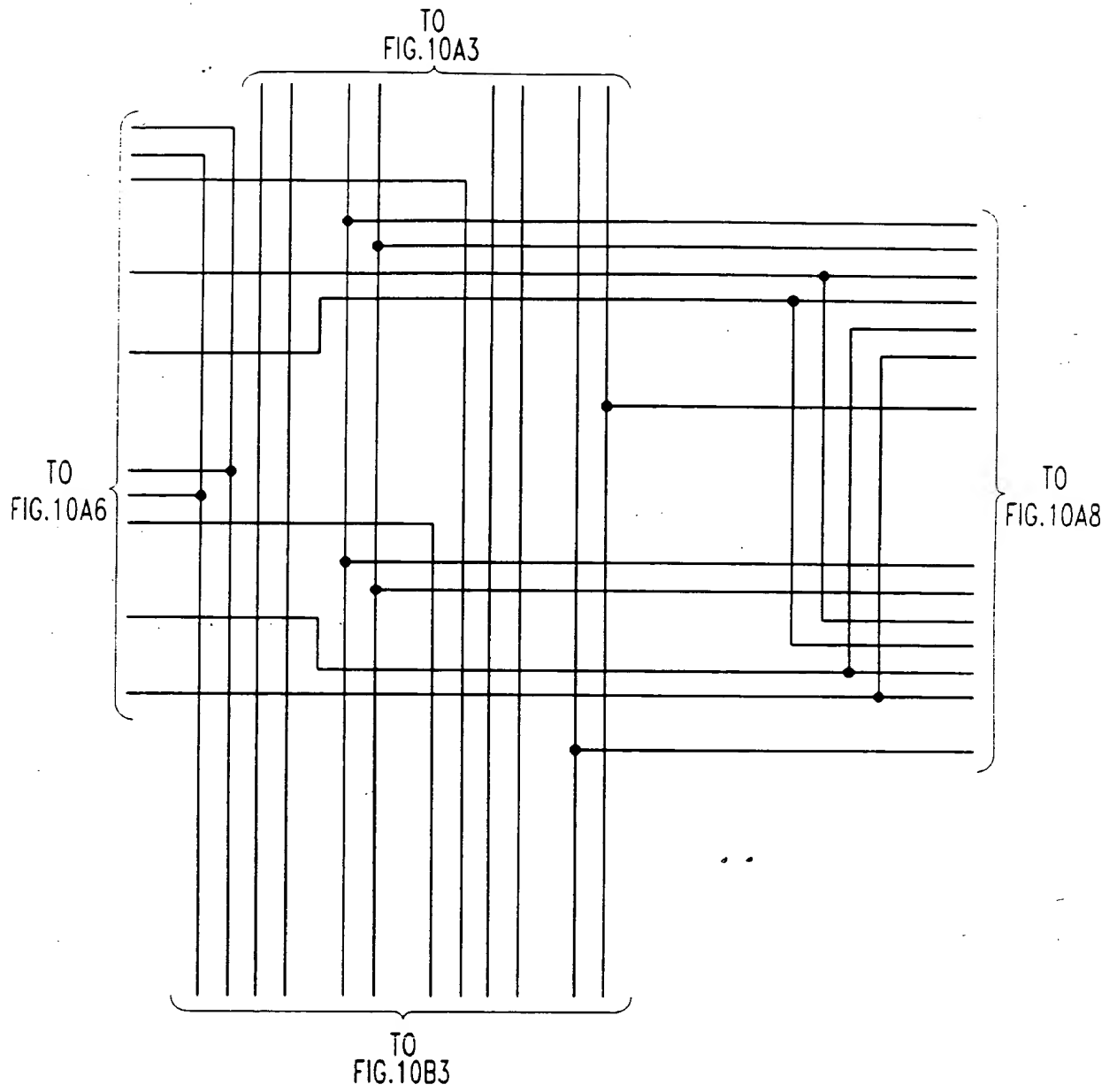


FIG. 10A7

FIG. 10A7

I/Oa

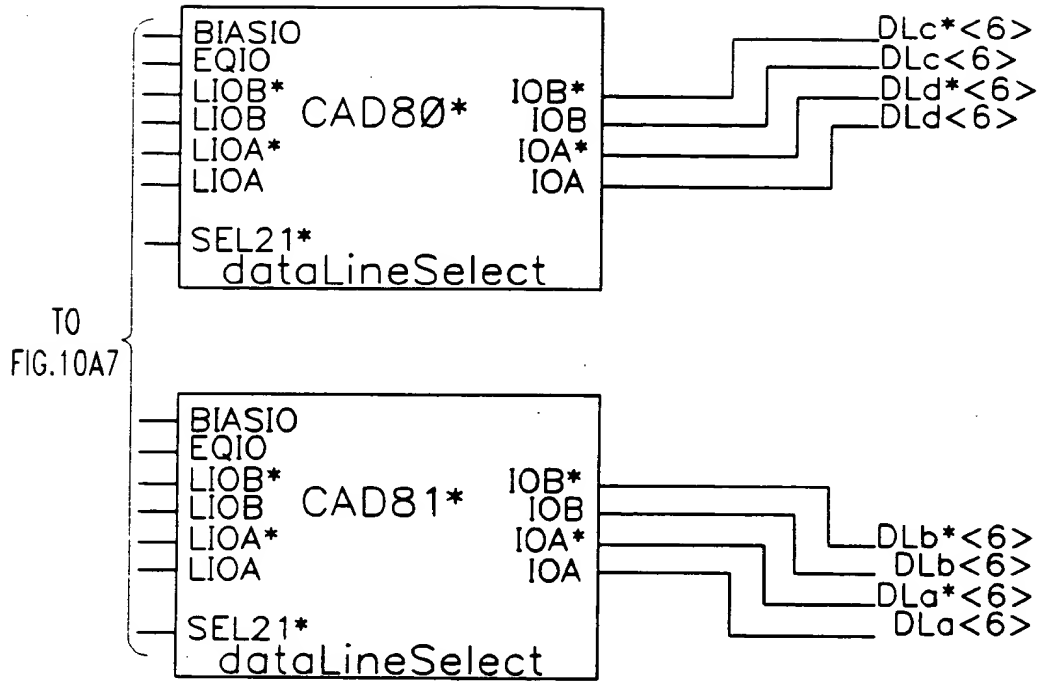


FIG.10A8

092475-0220

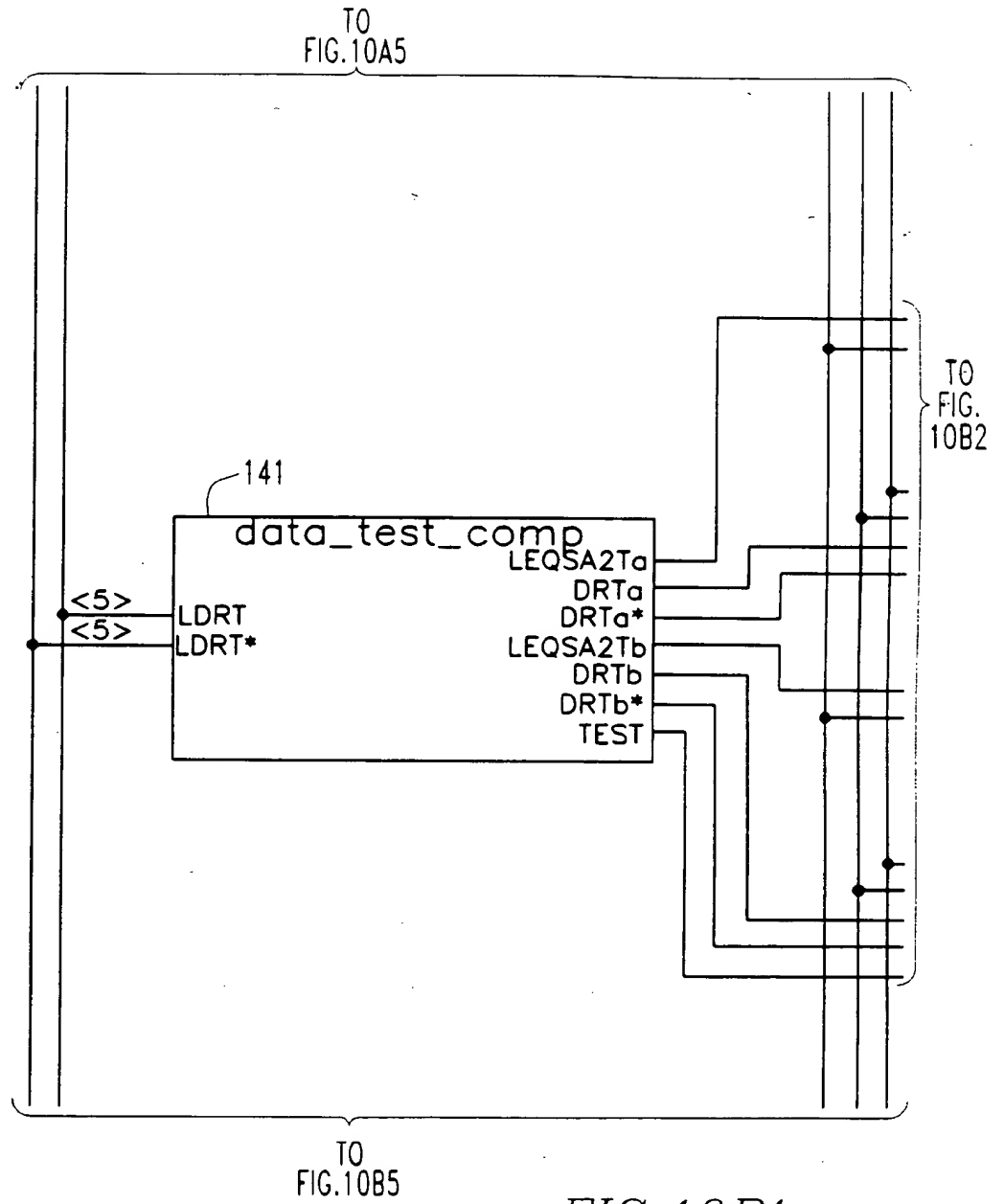


FIG. 10B1

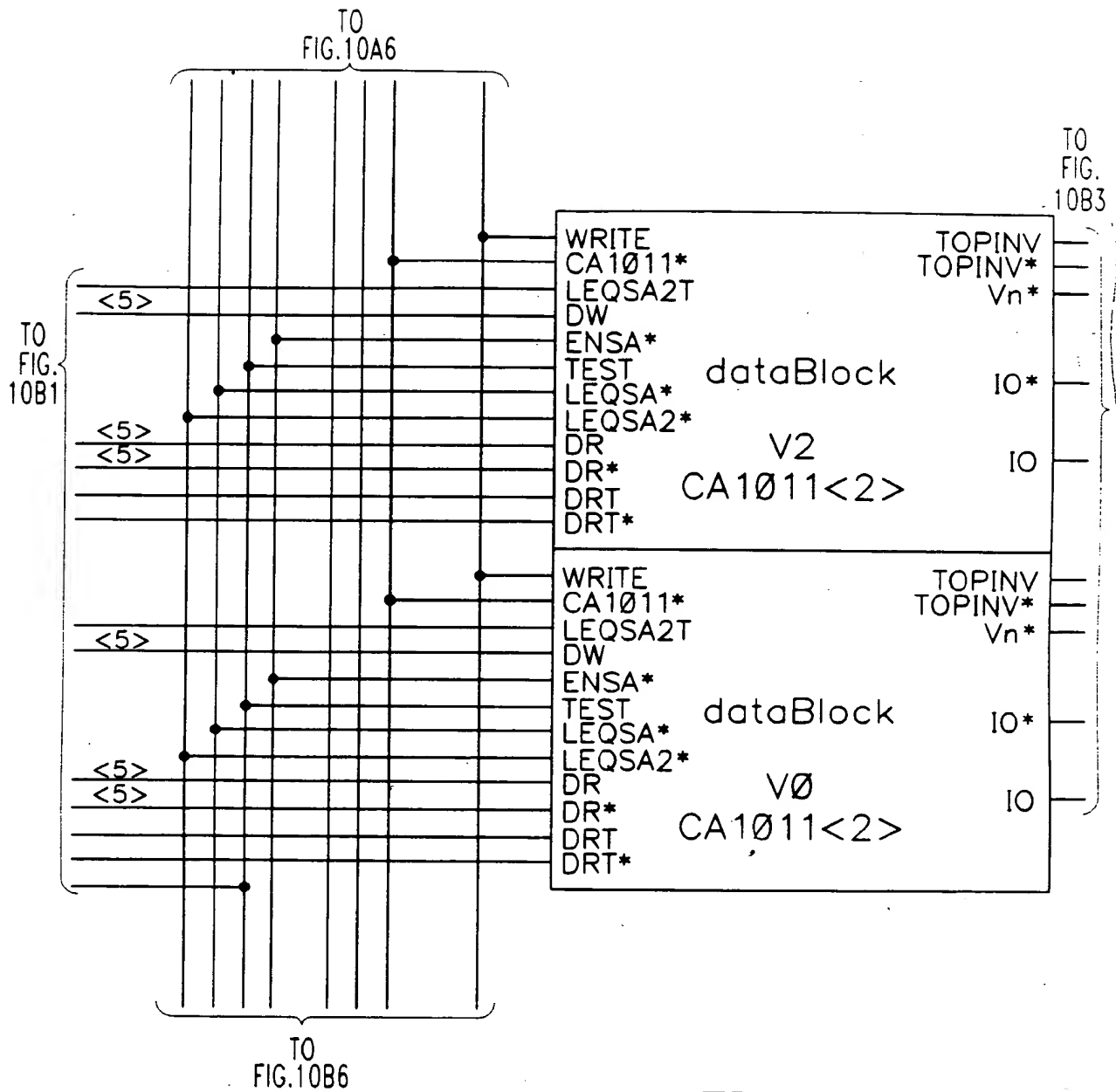
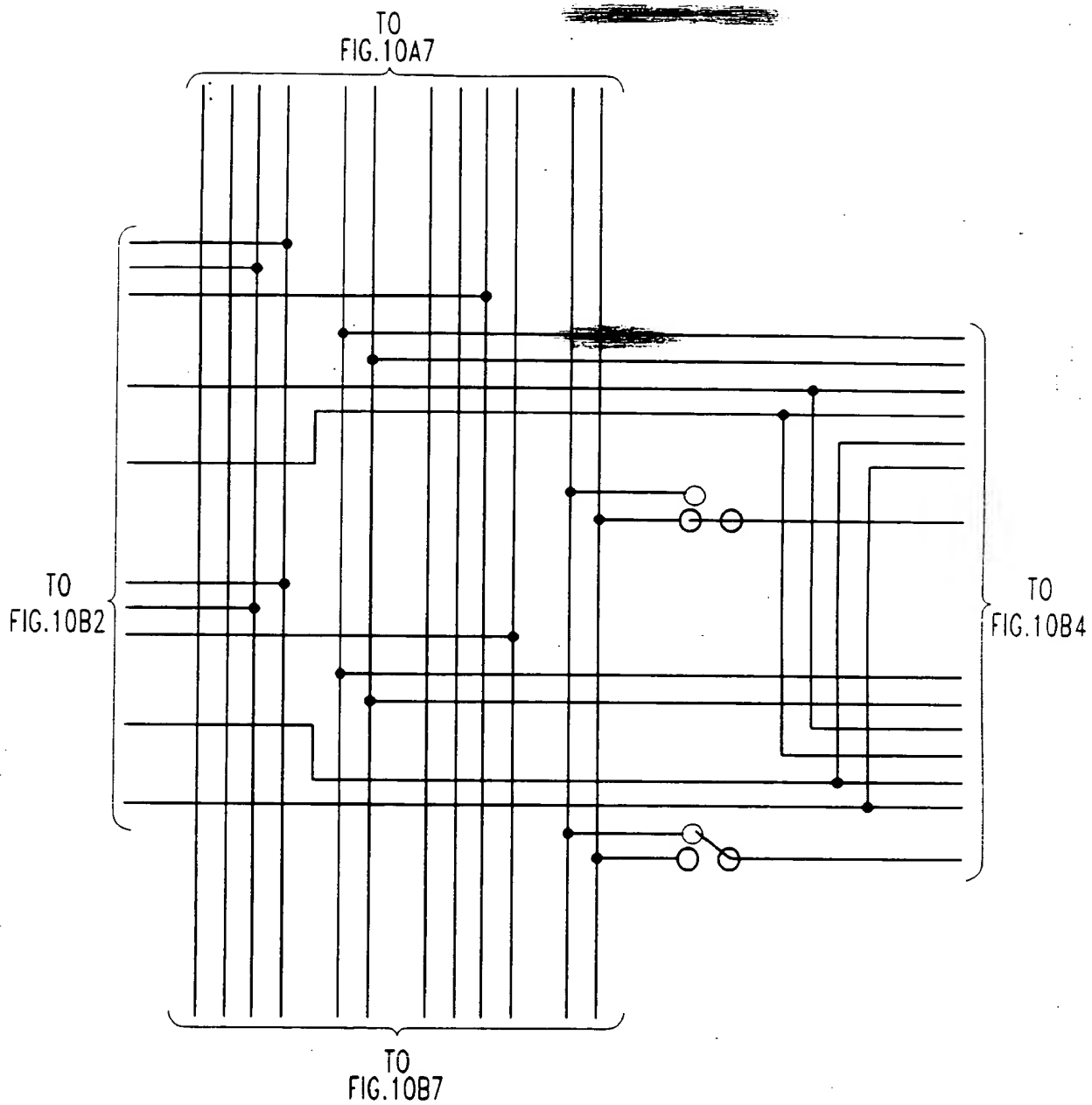


FIG. 10B2



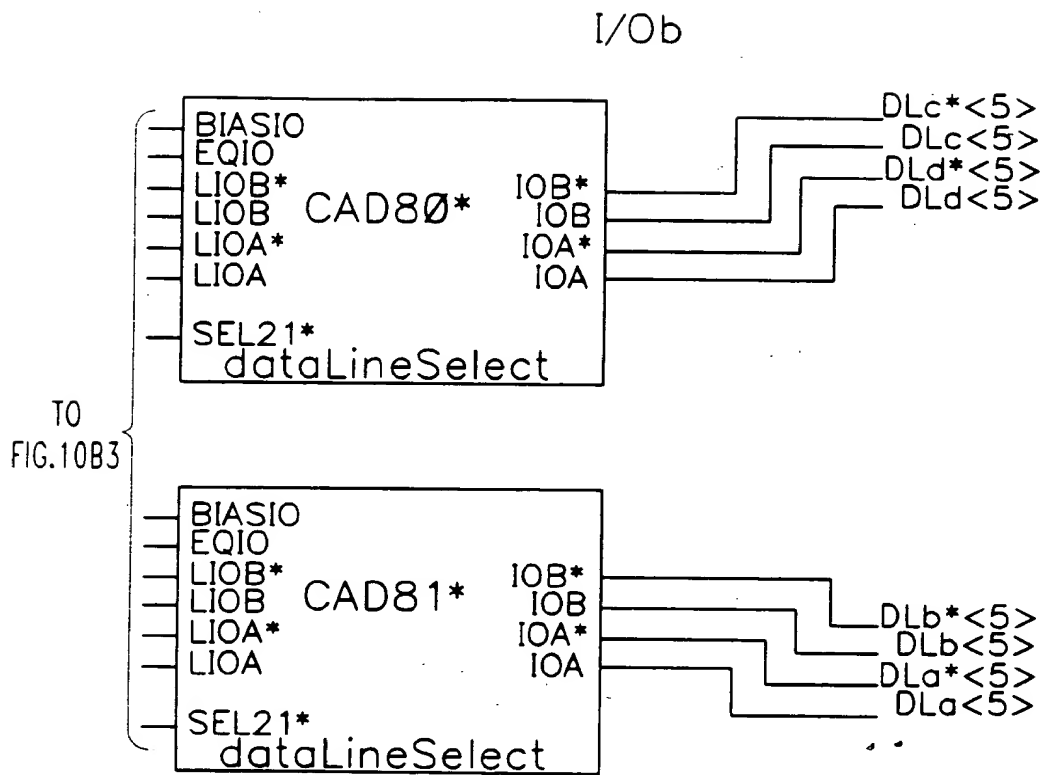


FIG. 10B4

FIG. 10B5

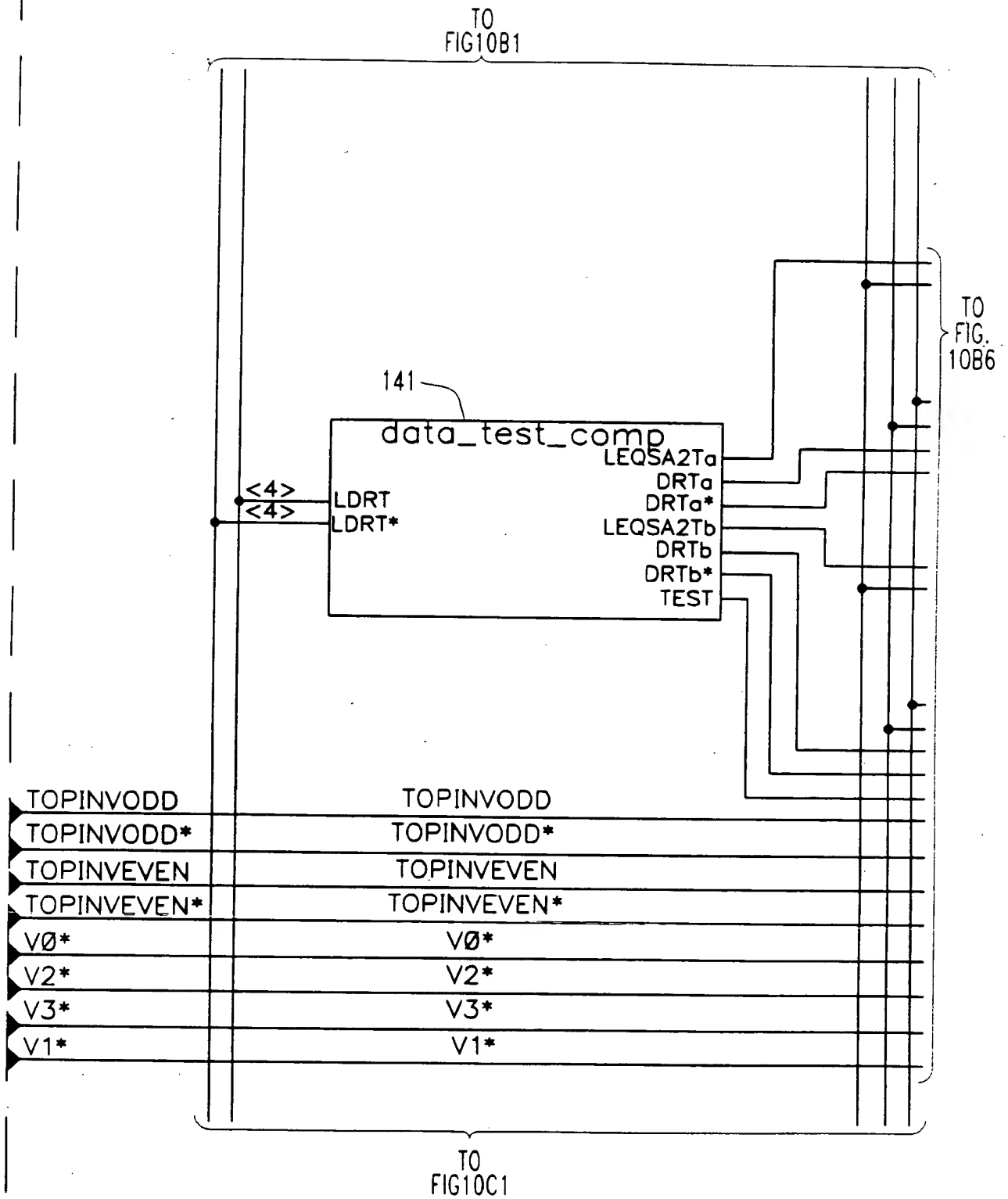


FIG. 10B6

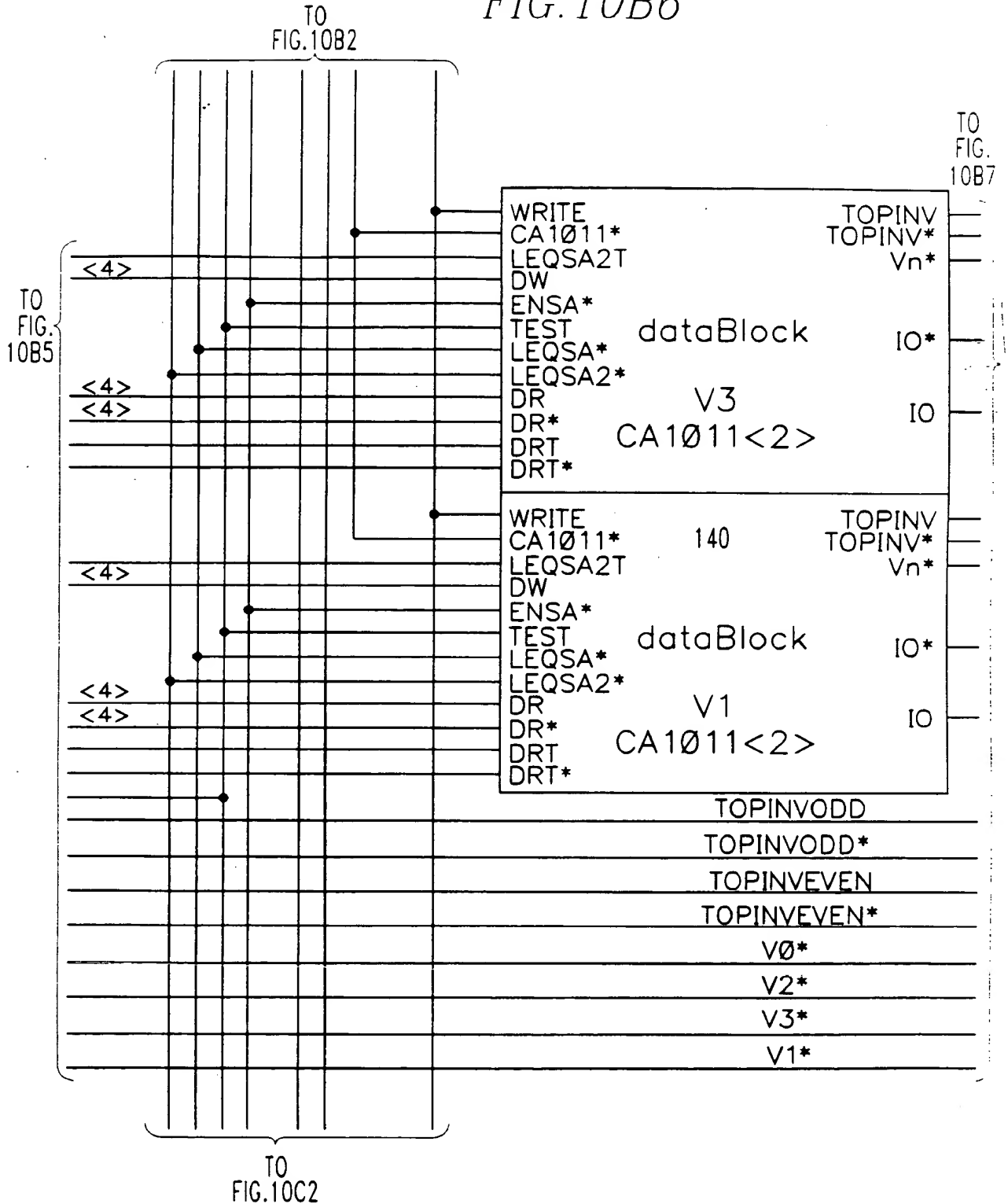
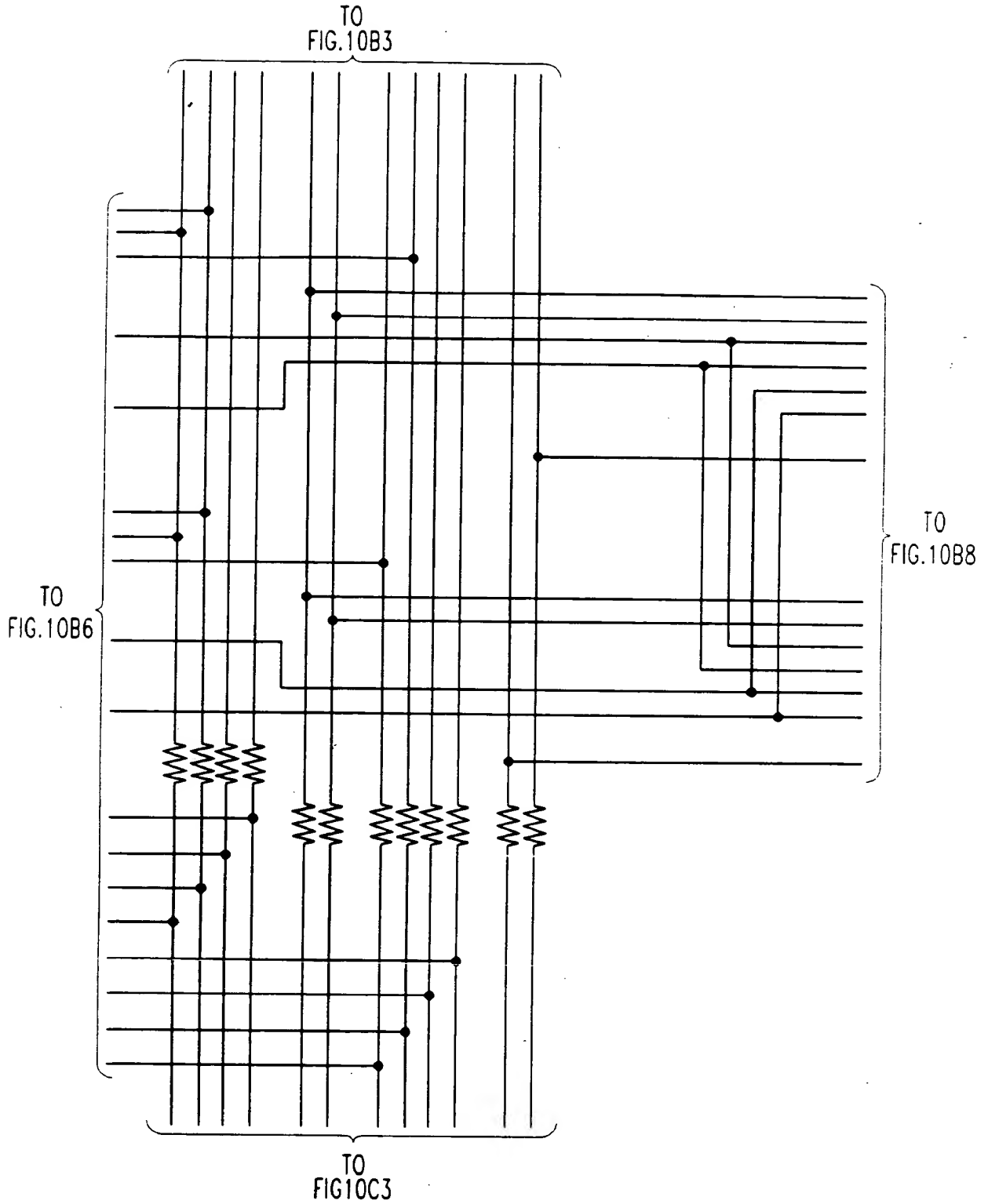
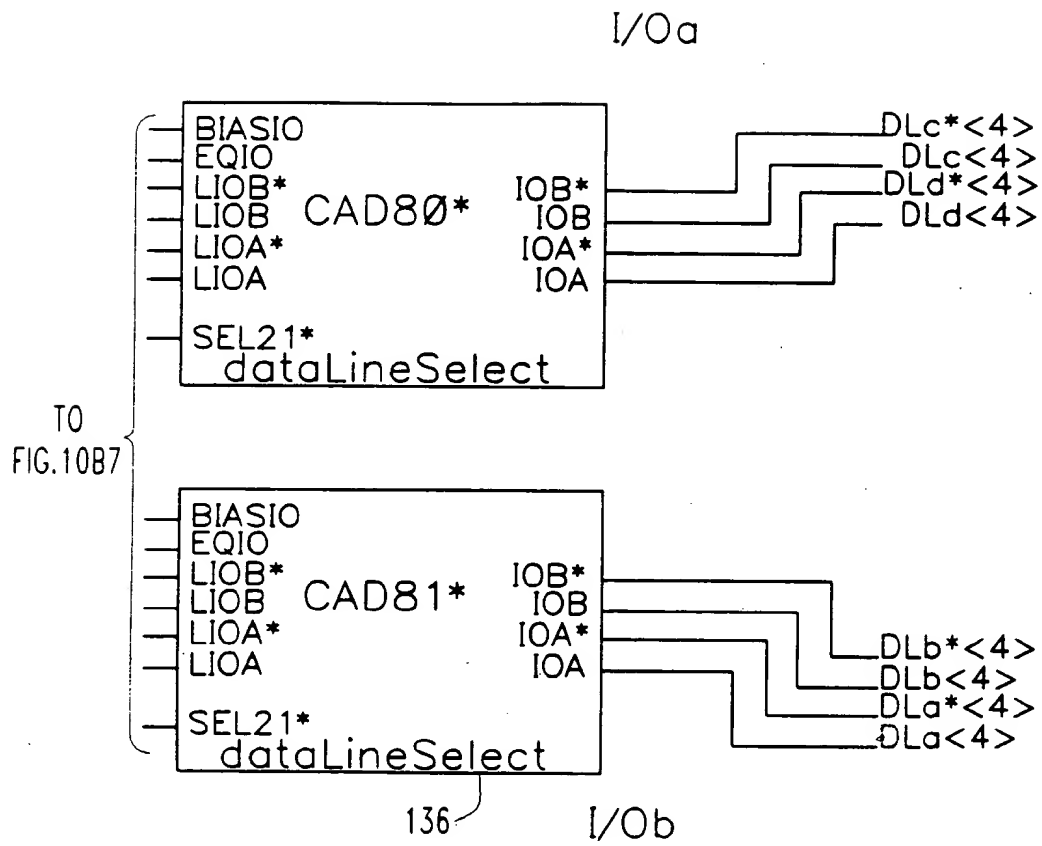


FIG. 10B7

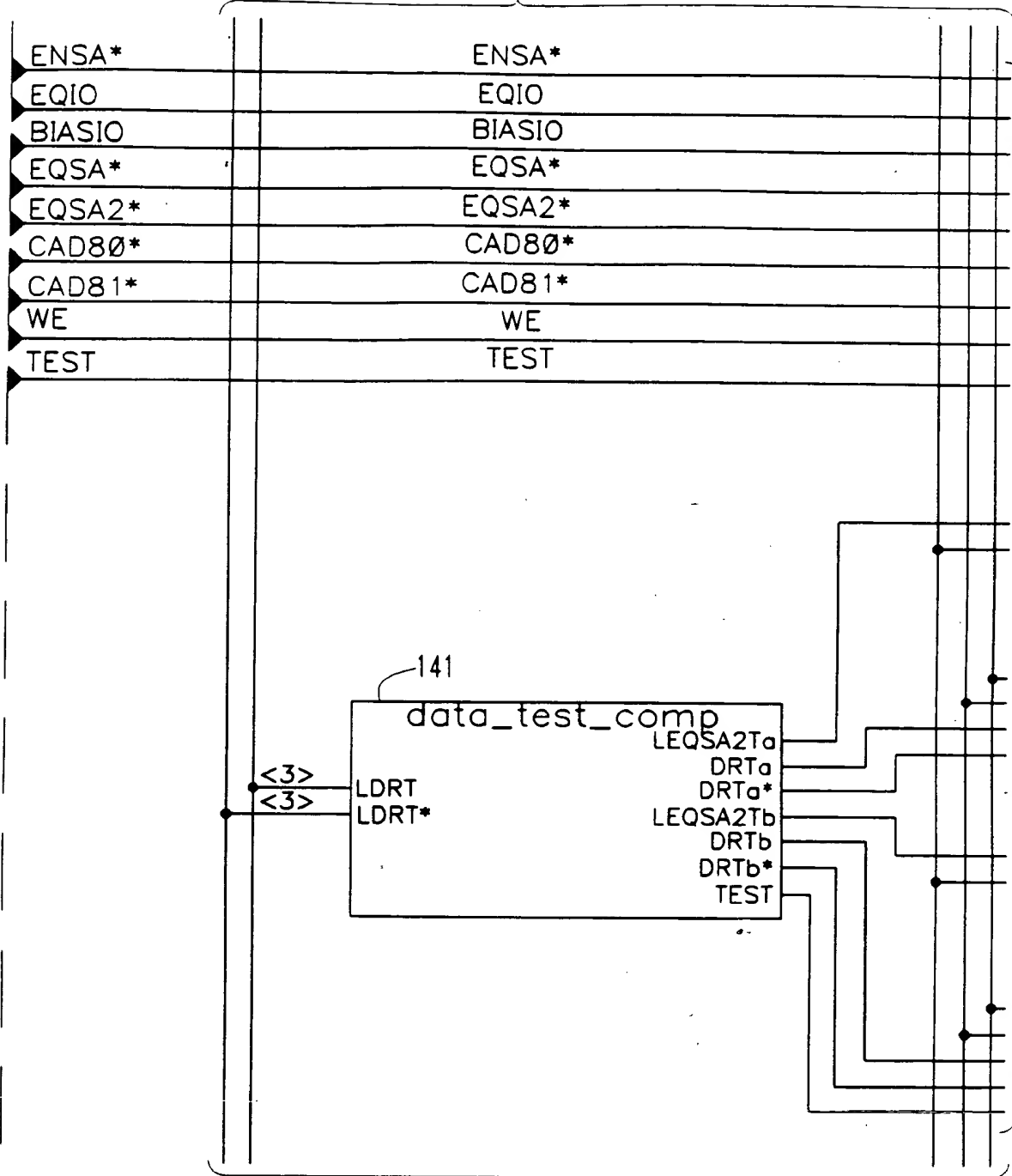


09475-08201 5644E660

FIG. 10B8



TO
FIG.
10C2



TO
FIG. 10C5

FIG. 10C1

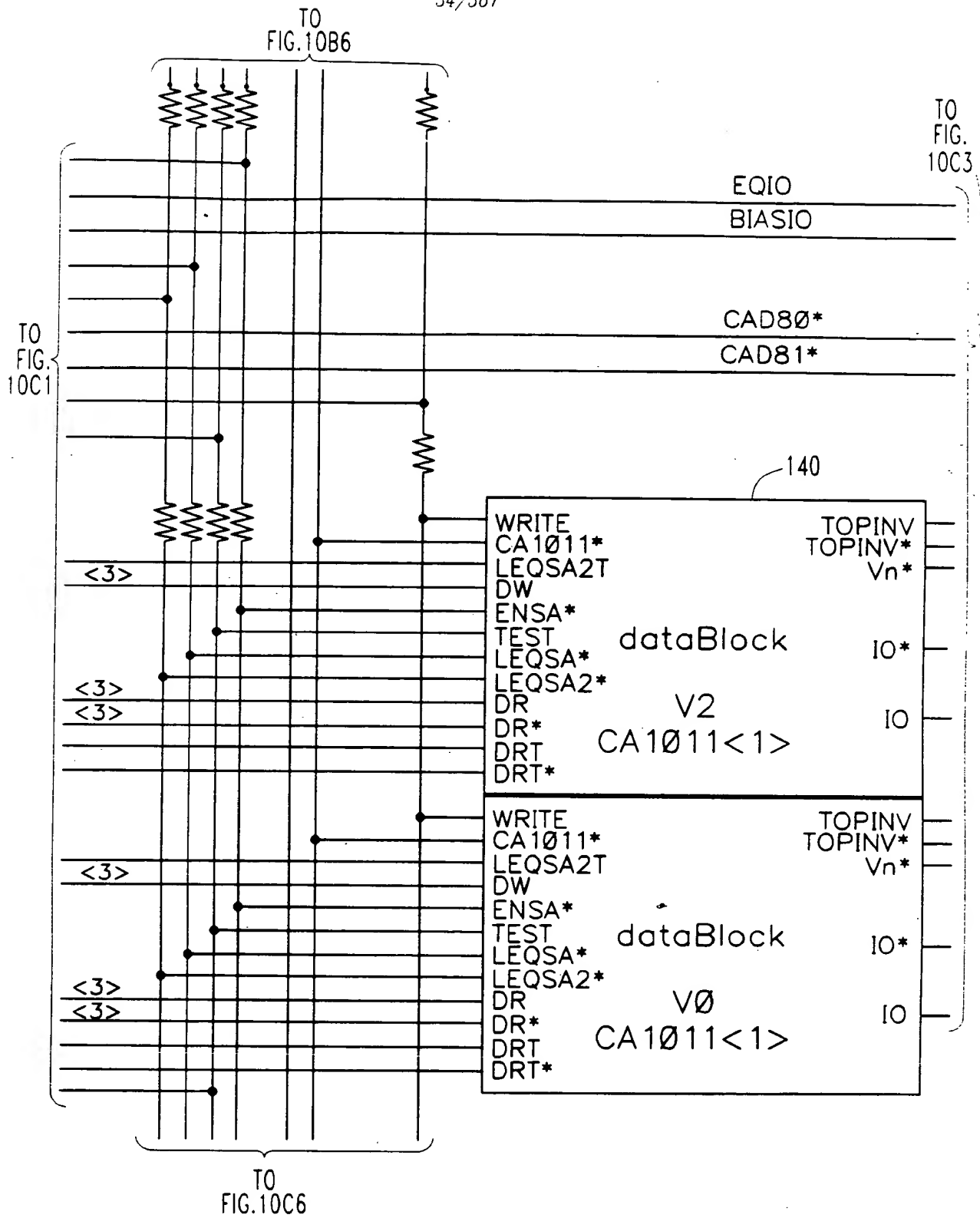


FIG. 10C2

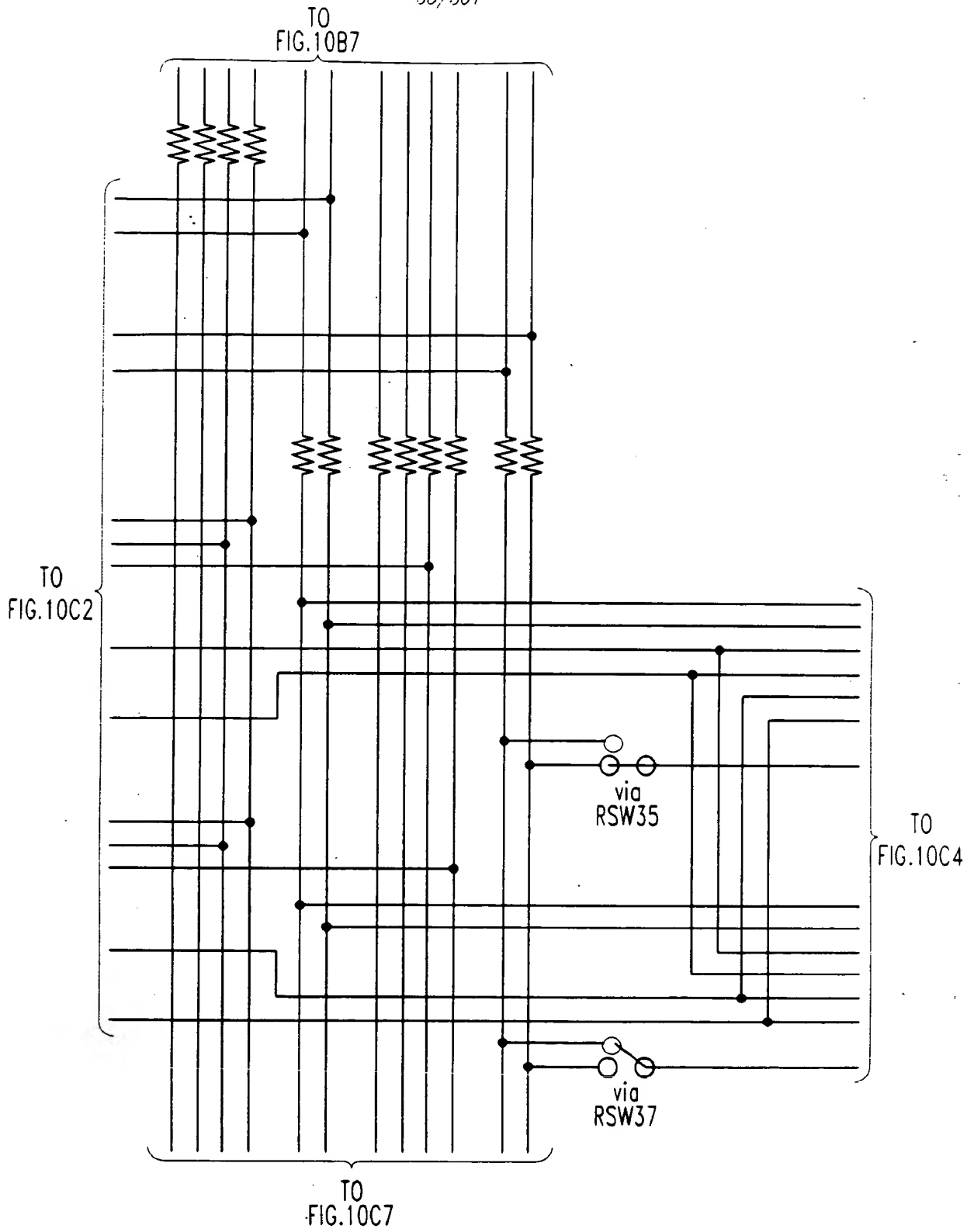
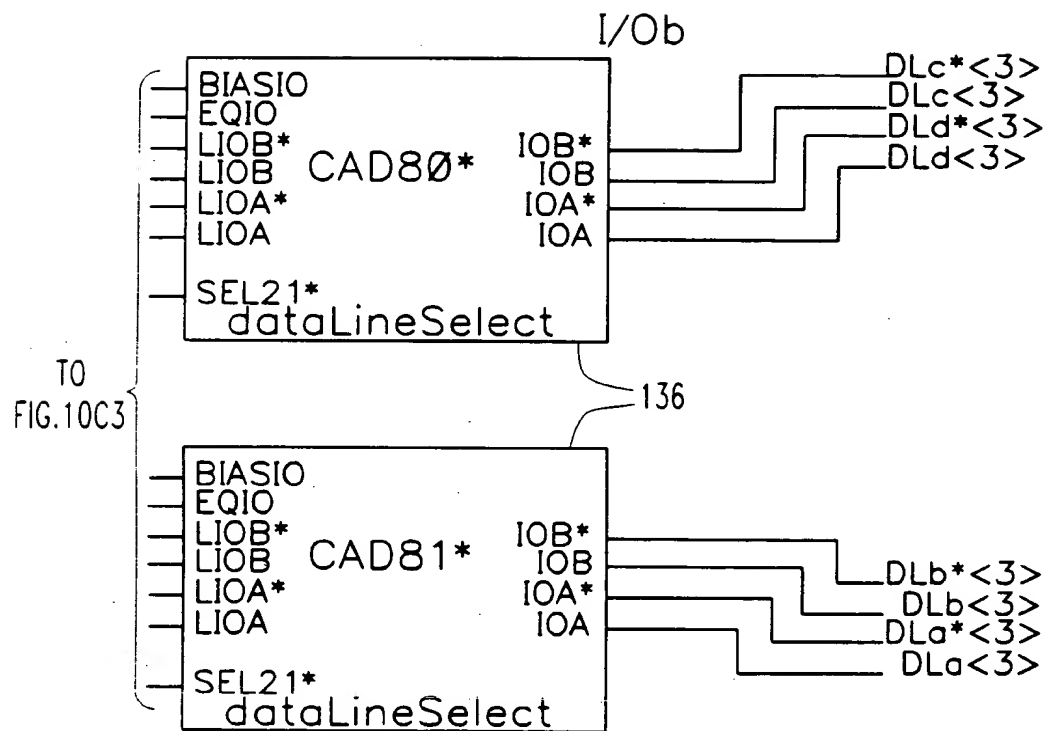
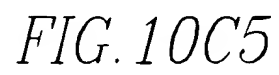


FIG. 10C3

003475-08201
002280-5644E60

FIG. 10C4





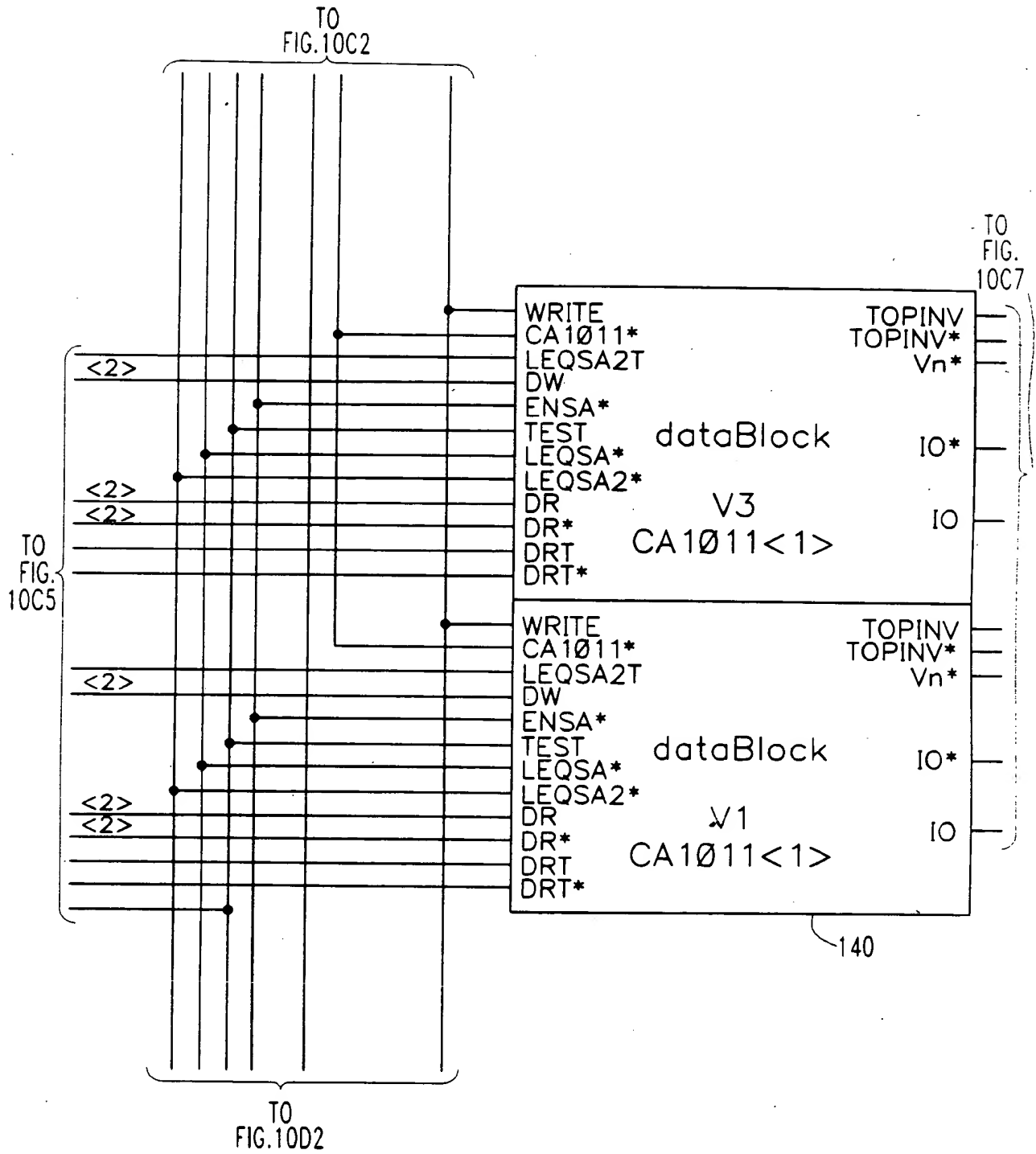
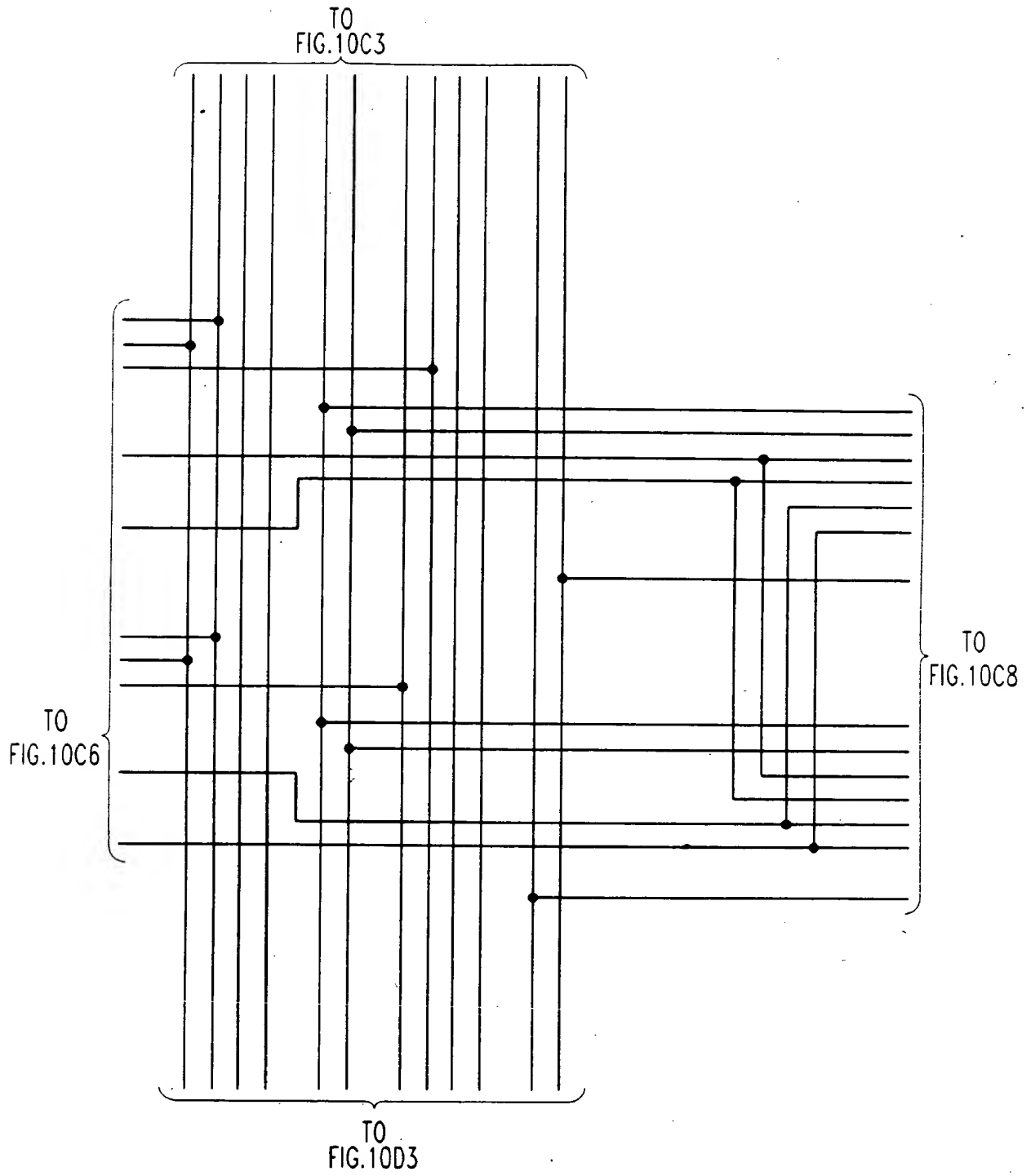


FIG. 10C6

*FIG. 10C7*

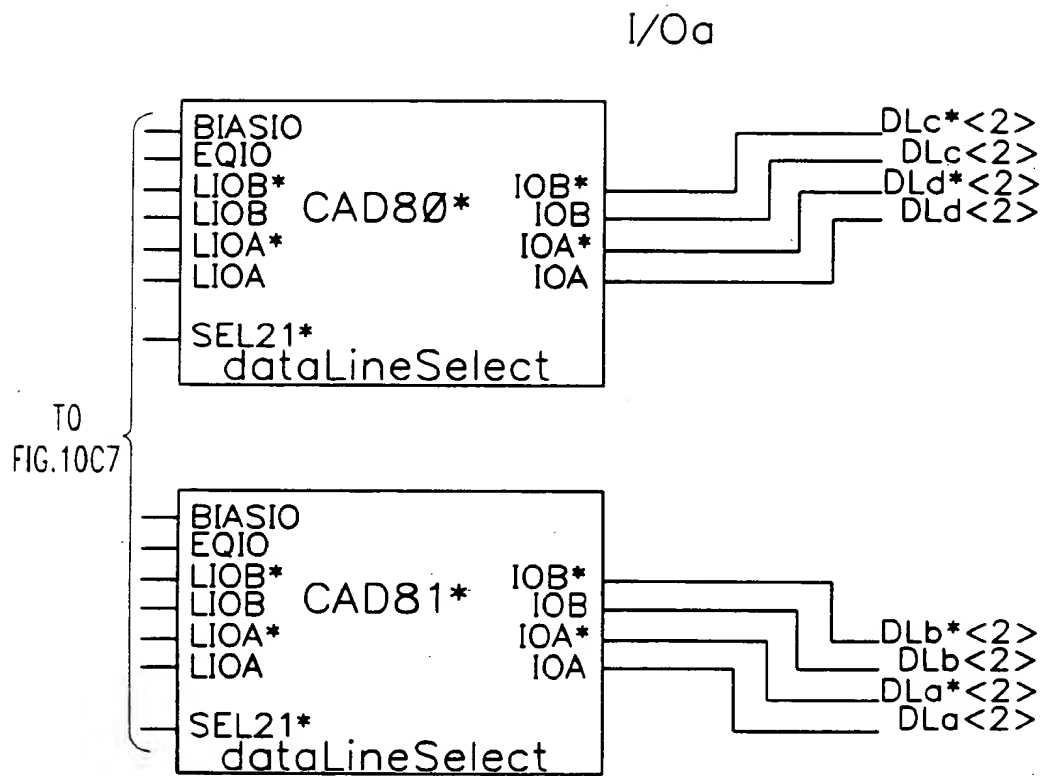


FIG. 10C8

00947-00201

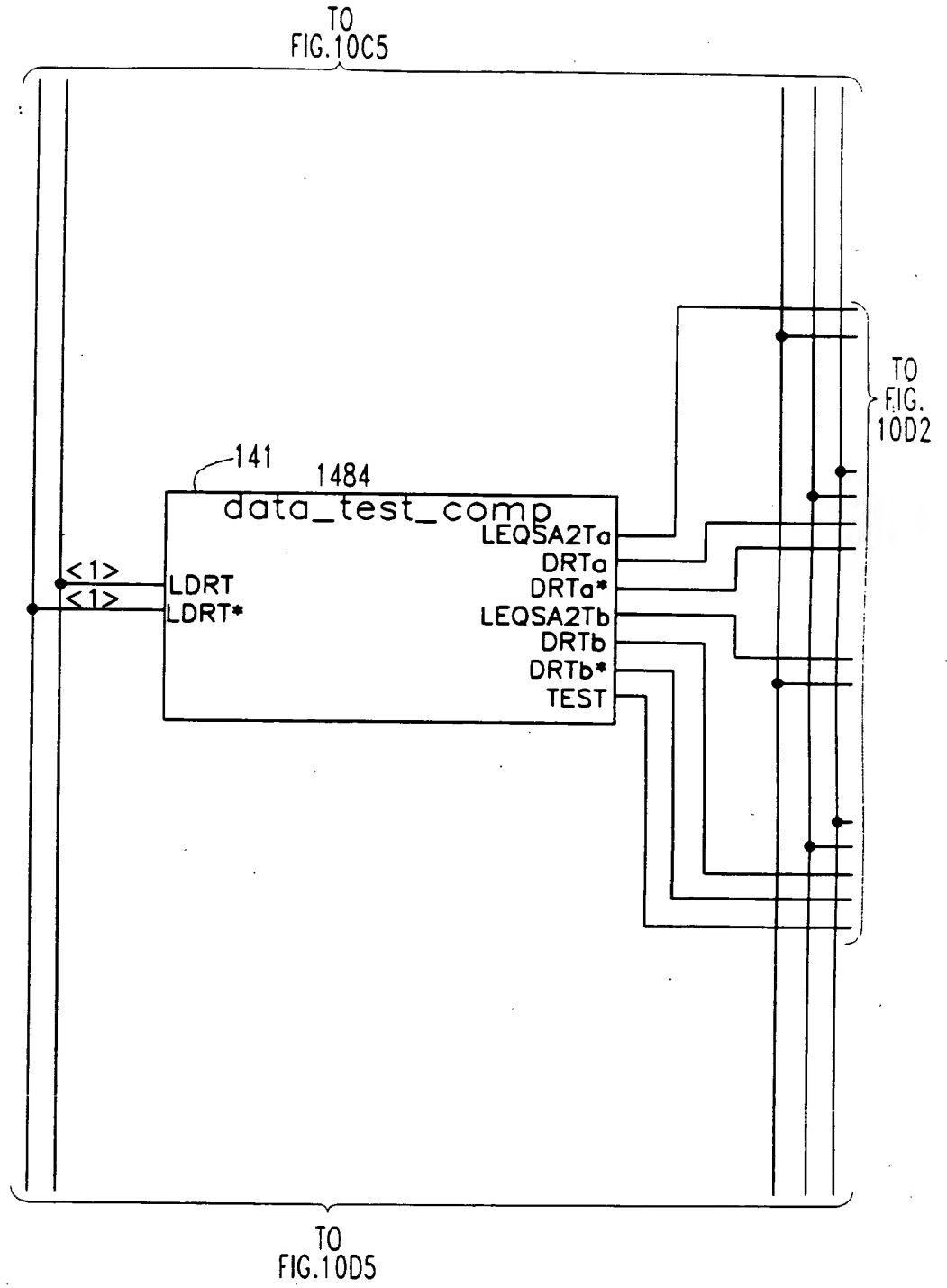


FIG.10D1

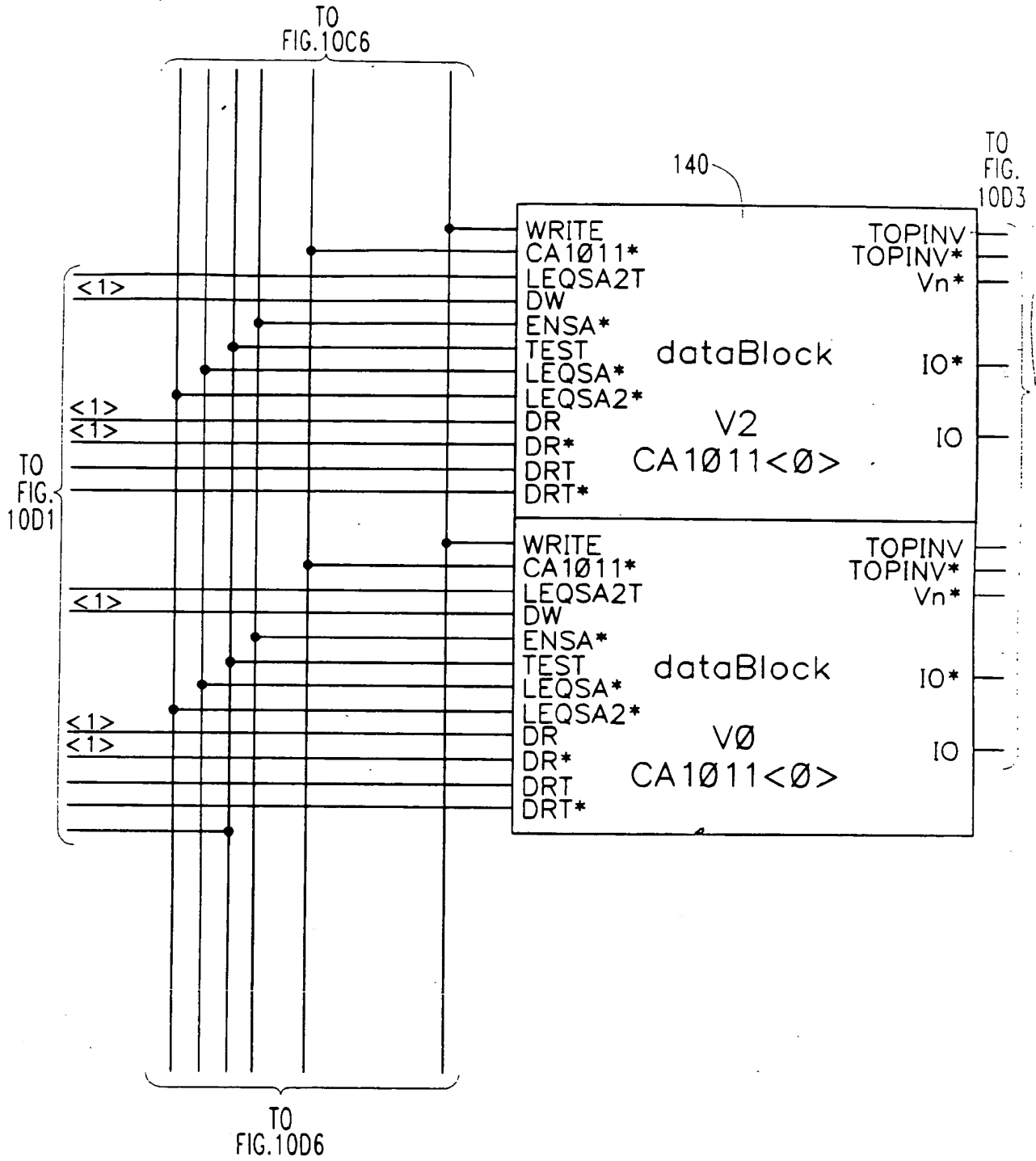


FIG. 10D2

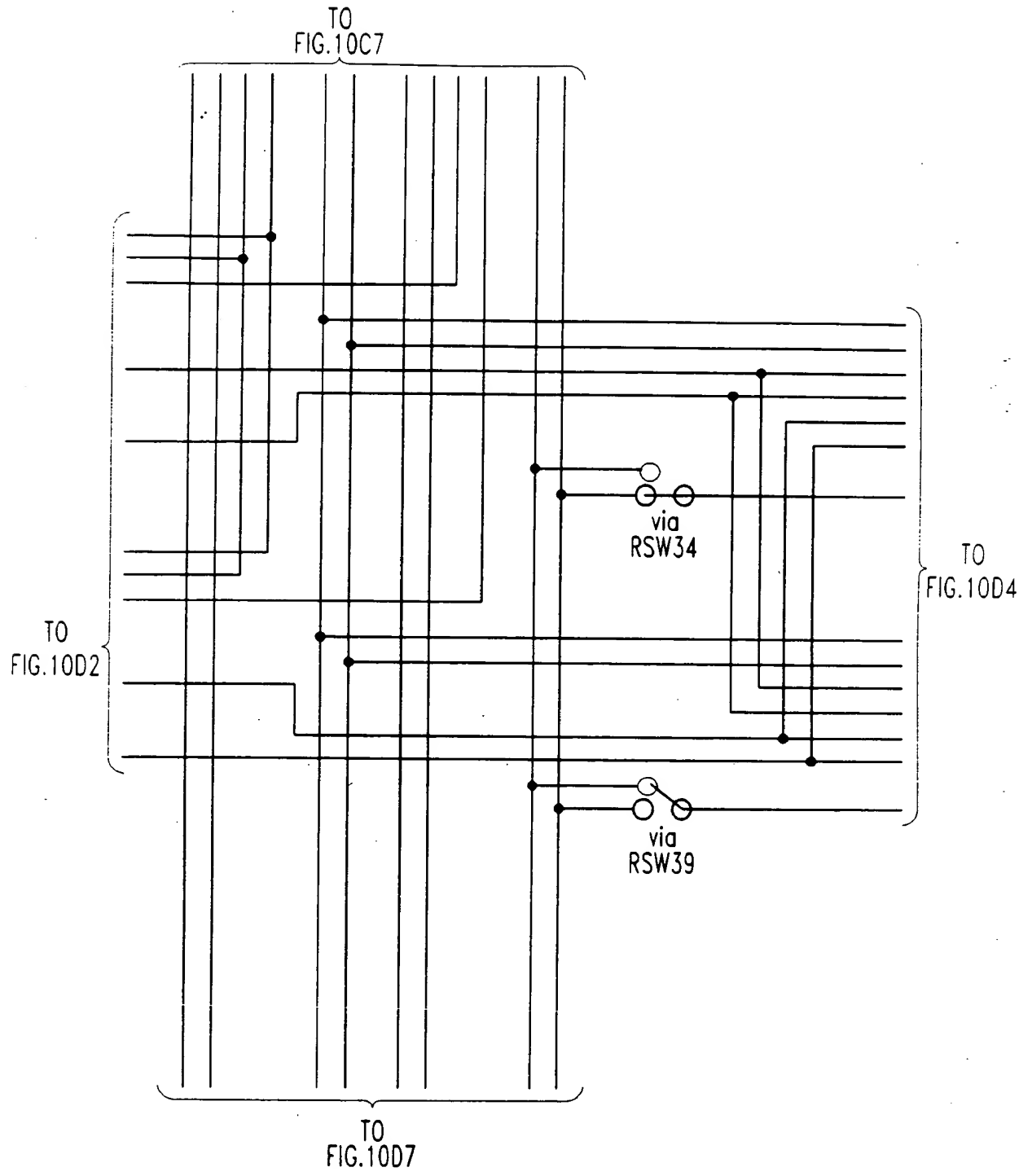


FIG. 10D3

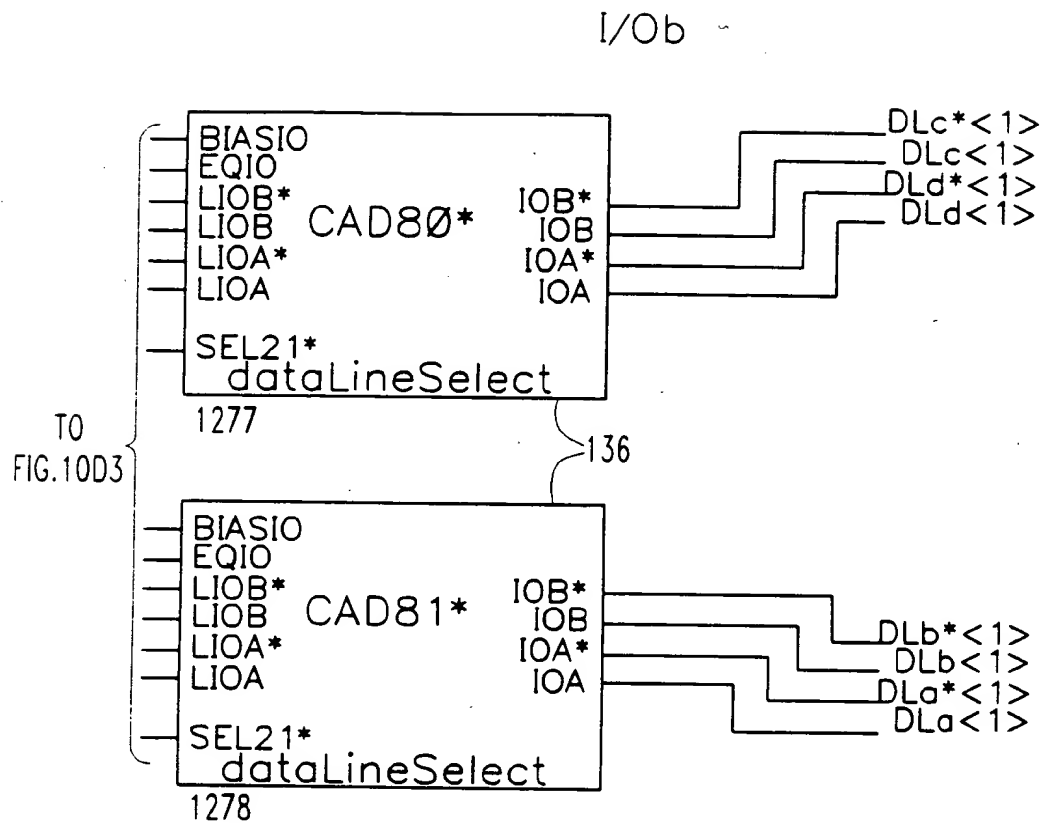


FIG.10D4

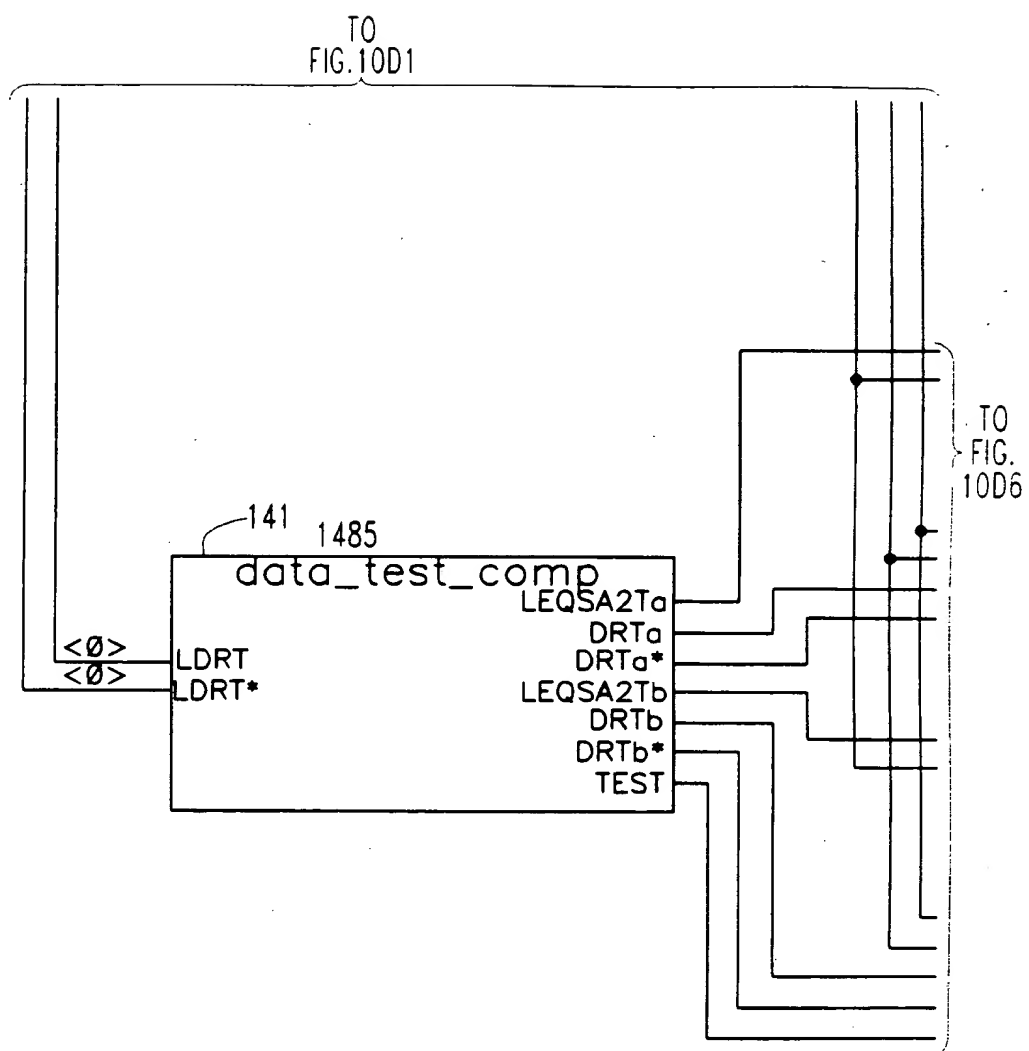


FIG. 10D5

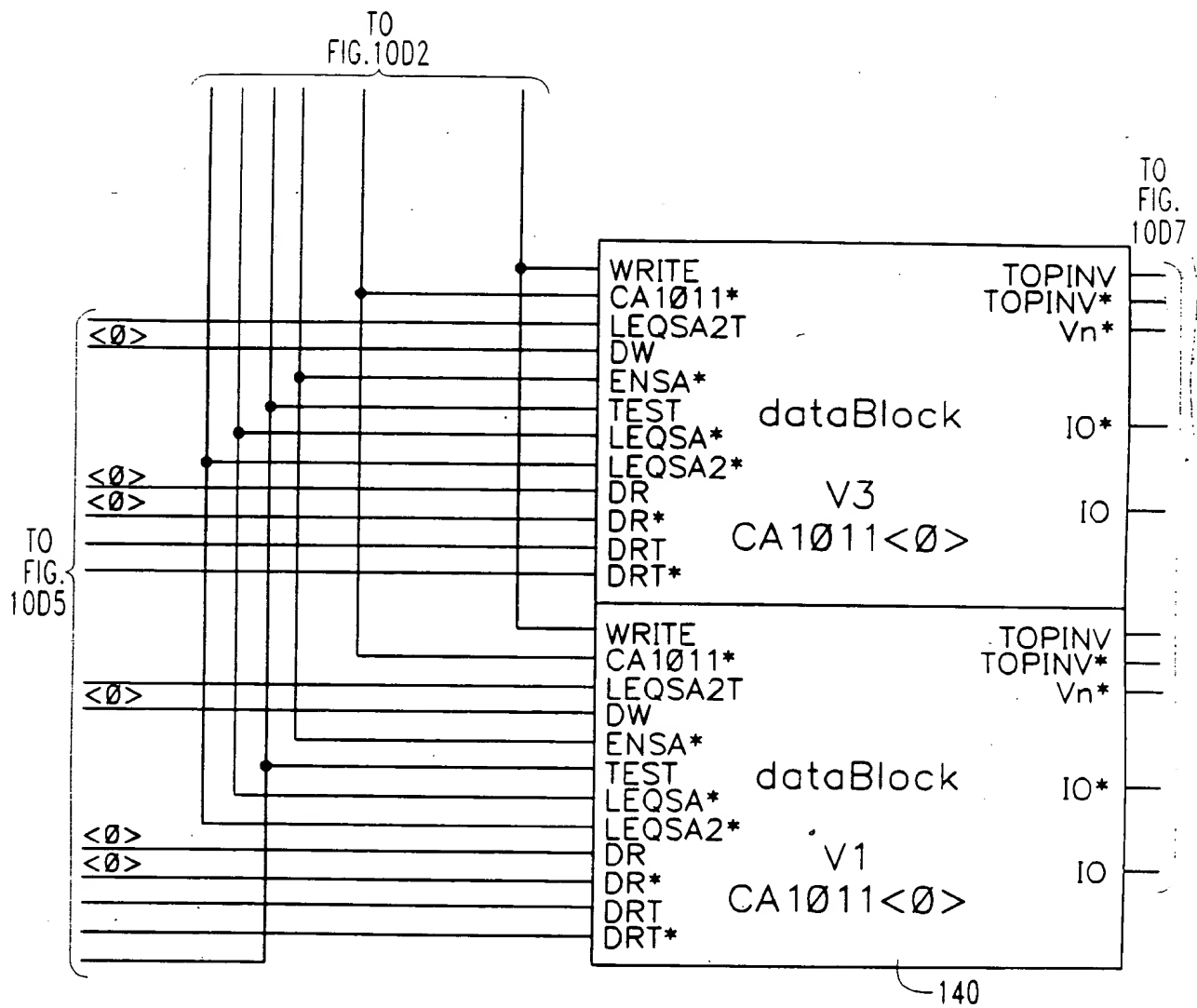


FIG. 10D6

FIG. 10D7

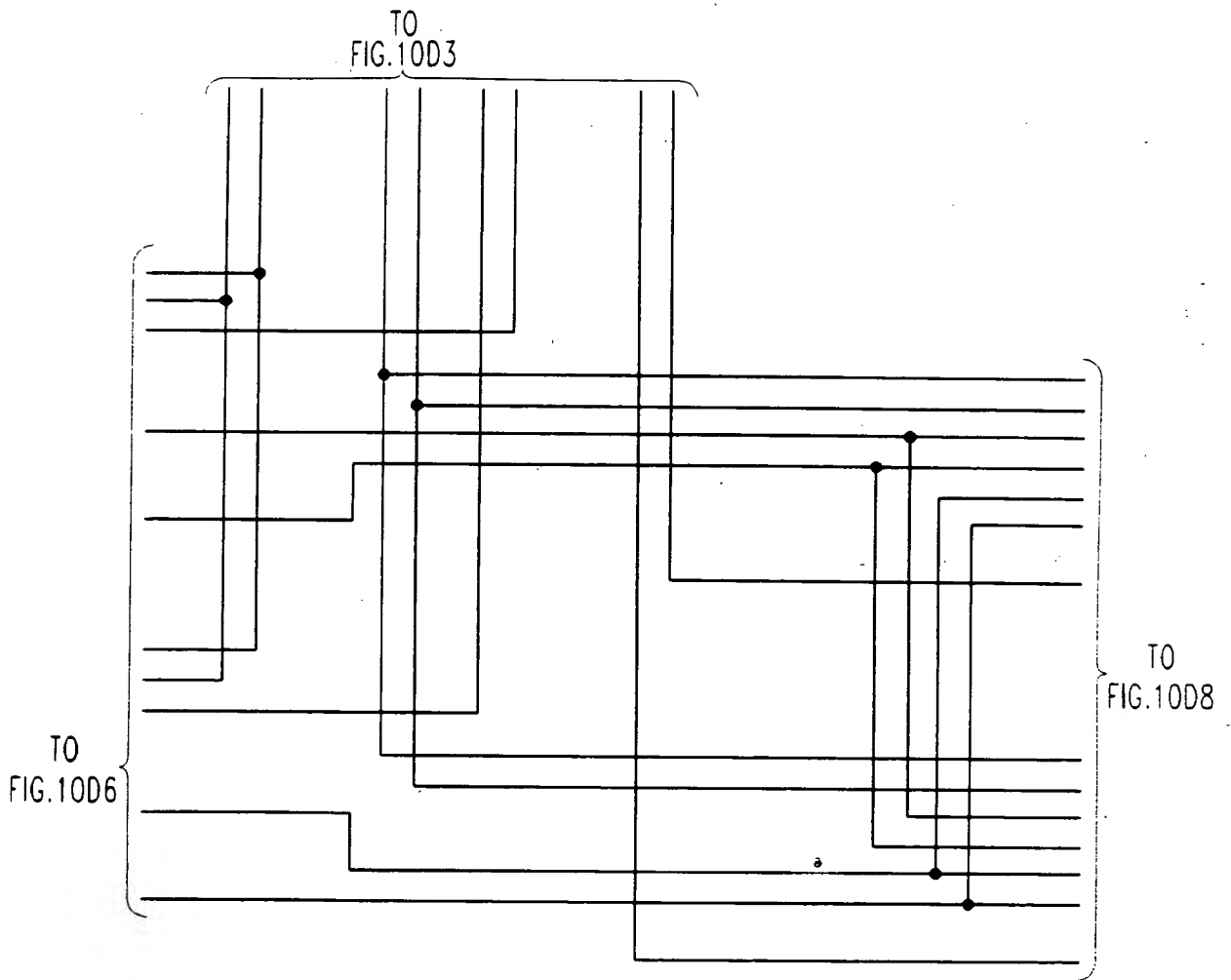


FIG. 10D7

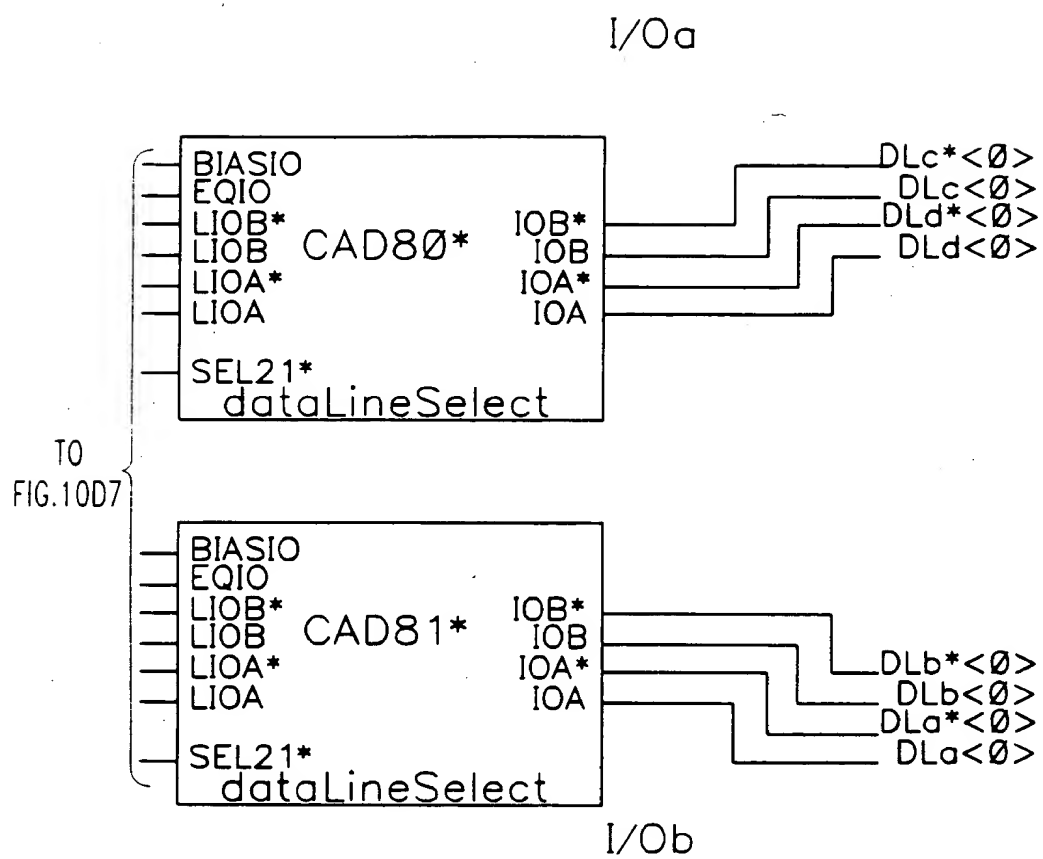
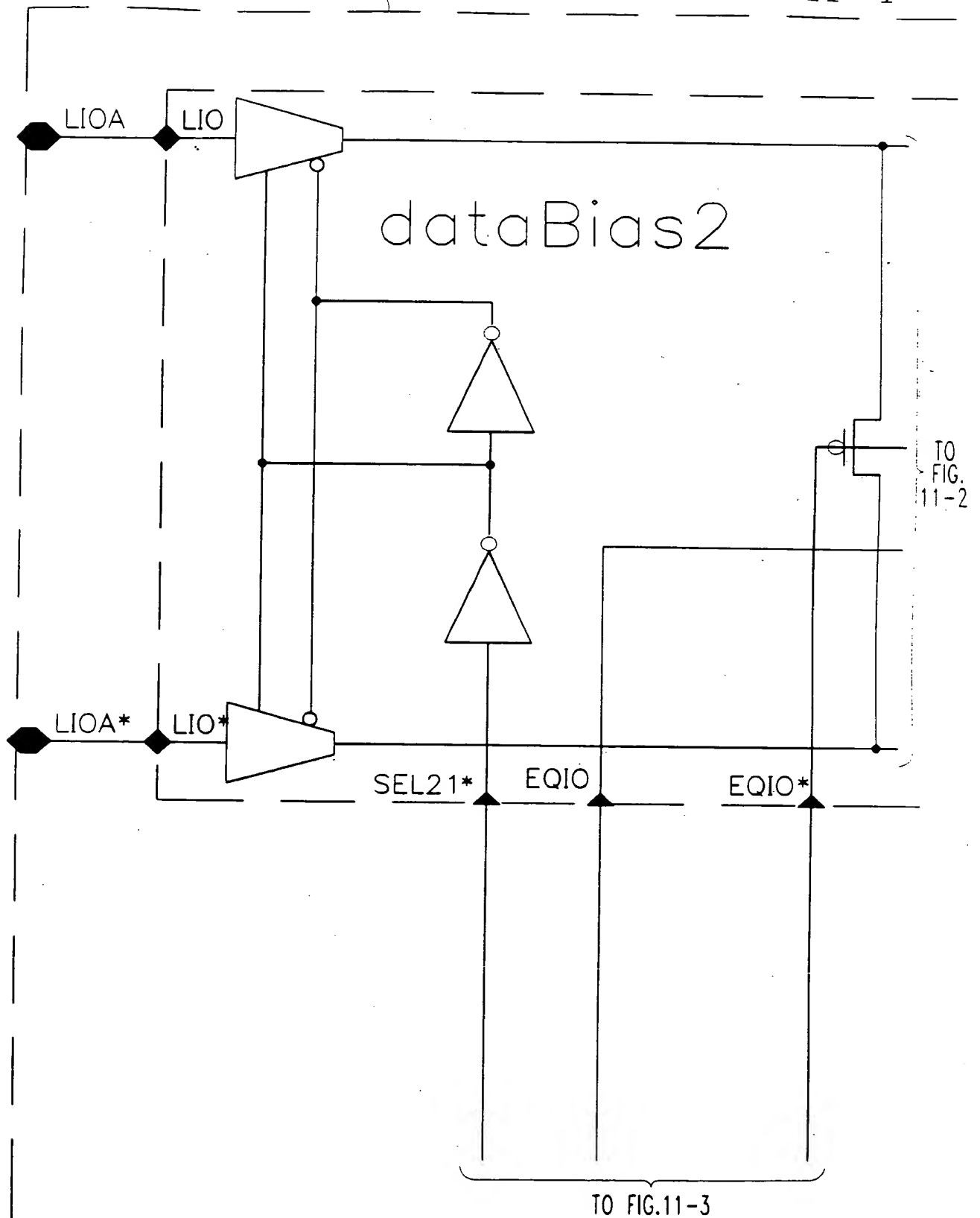


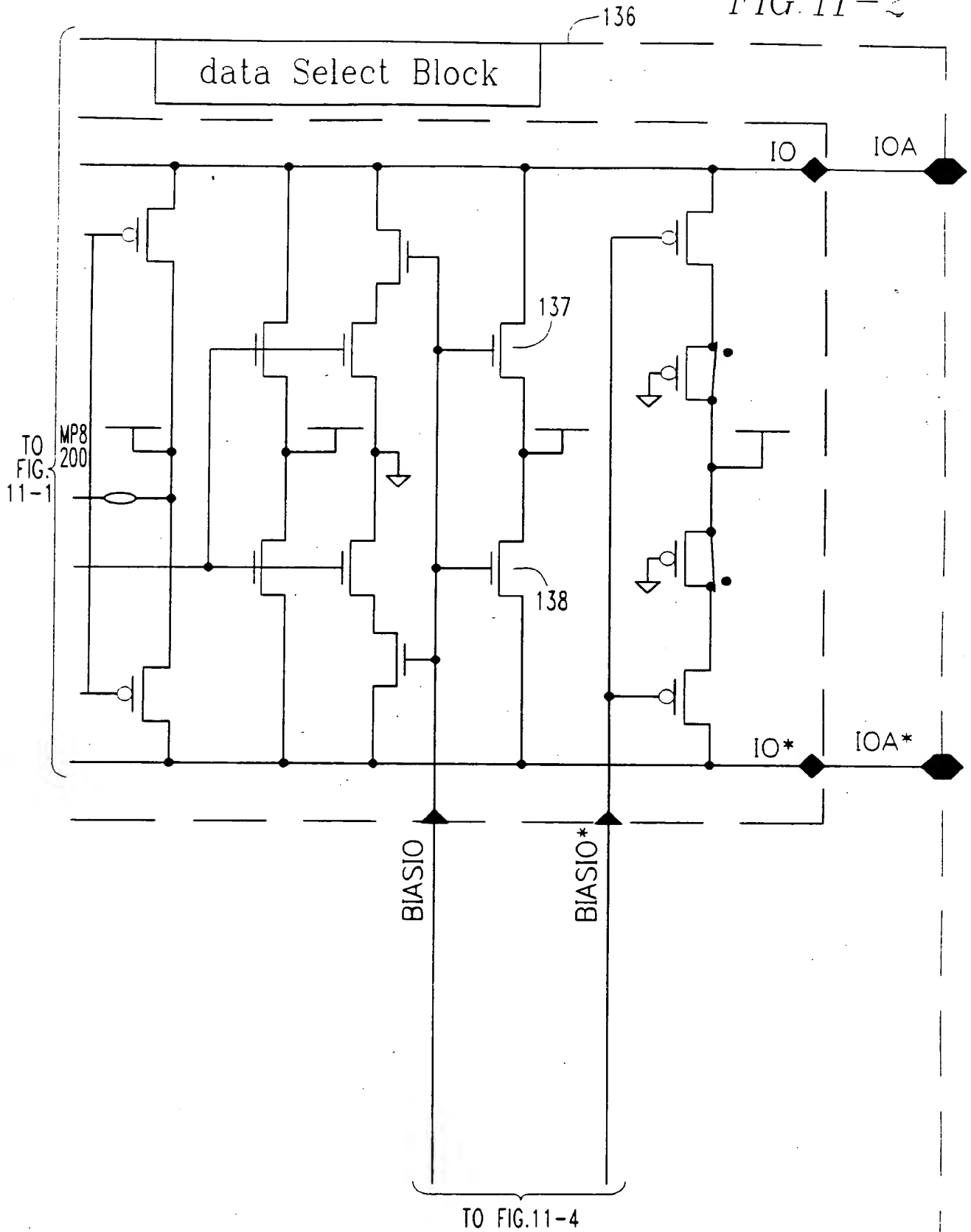
FIG. 10D8

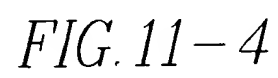
136

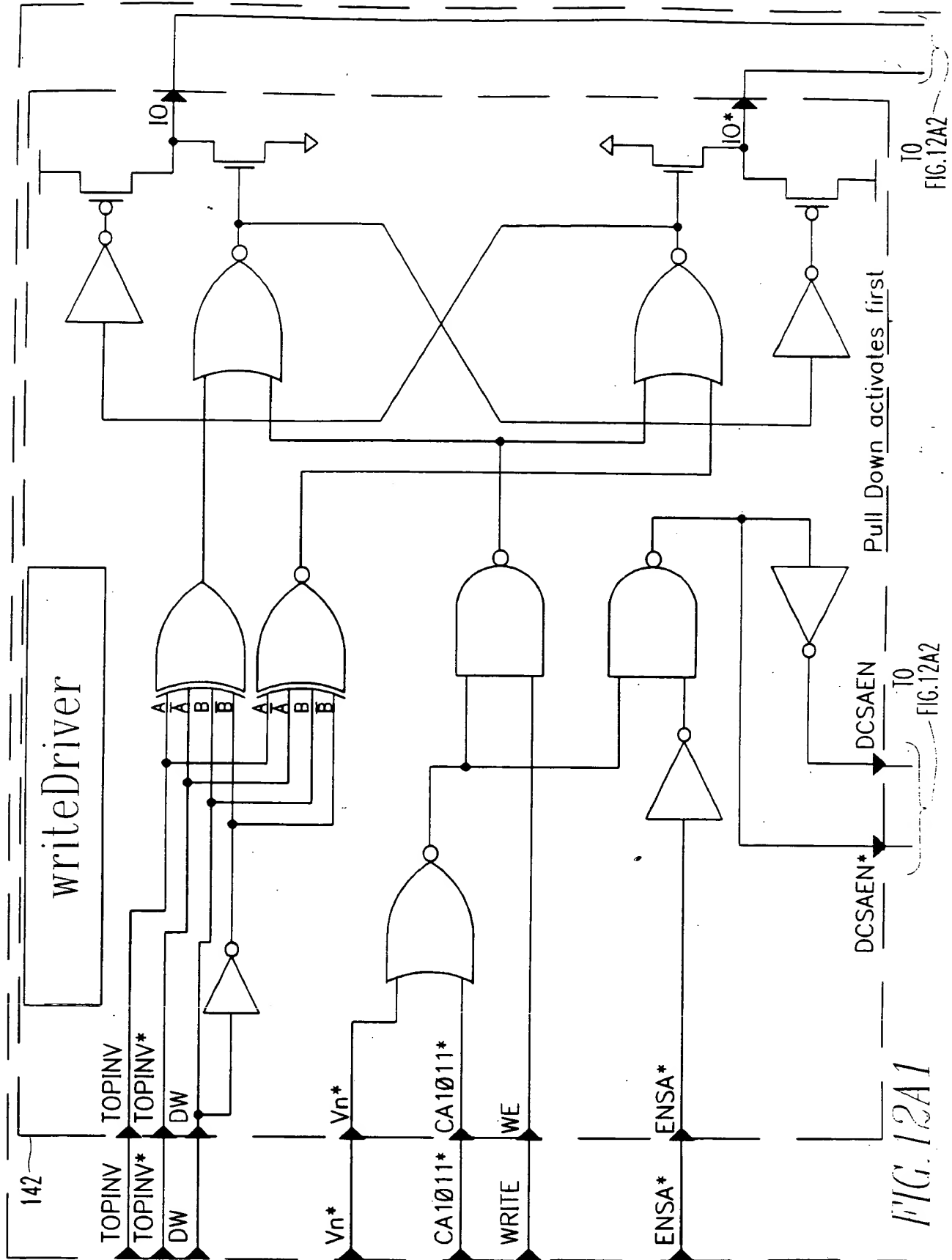


T02233 56242660

FIG. 11-2







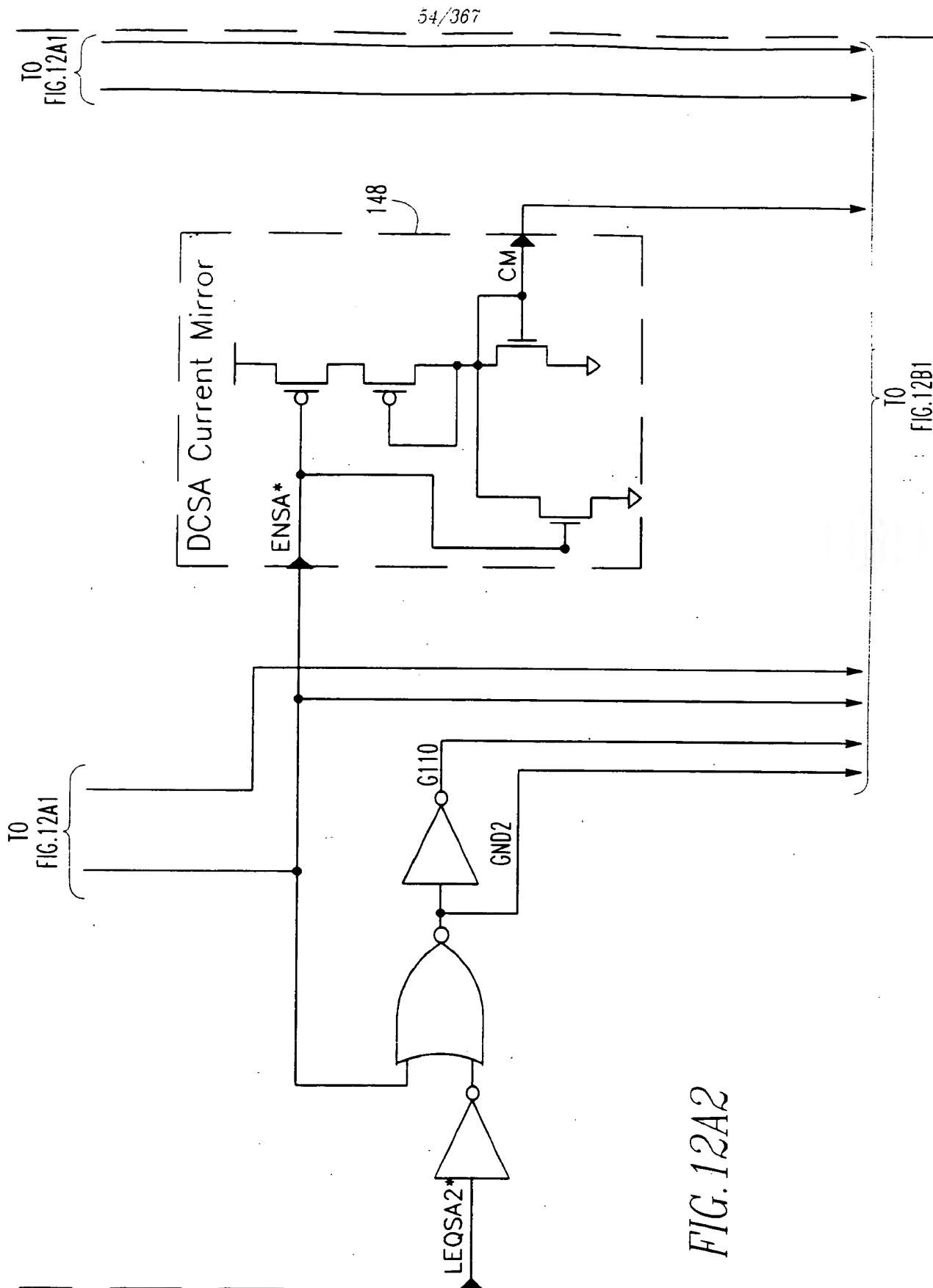
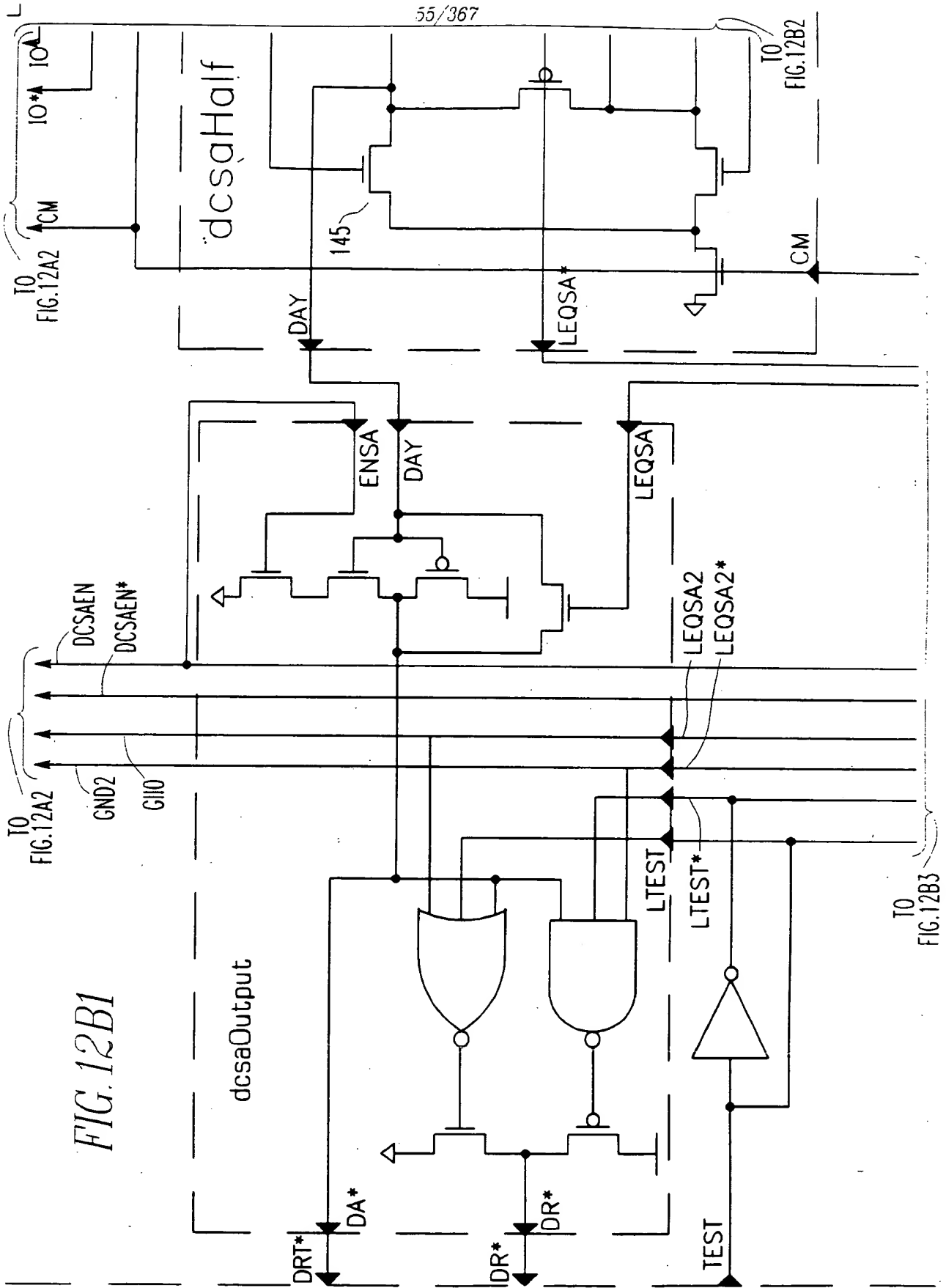


FIG. 12A2



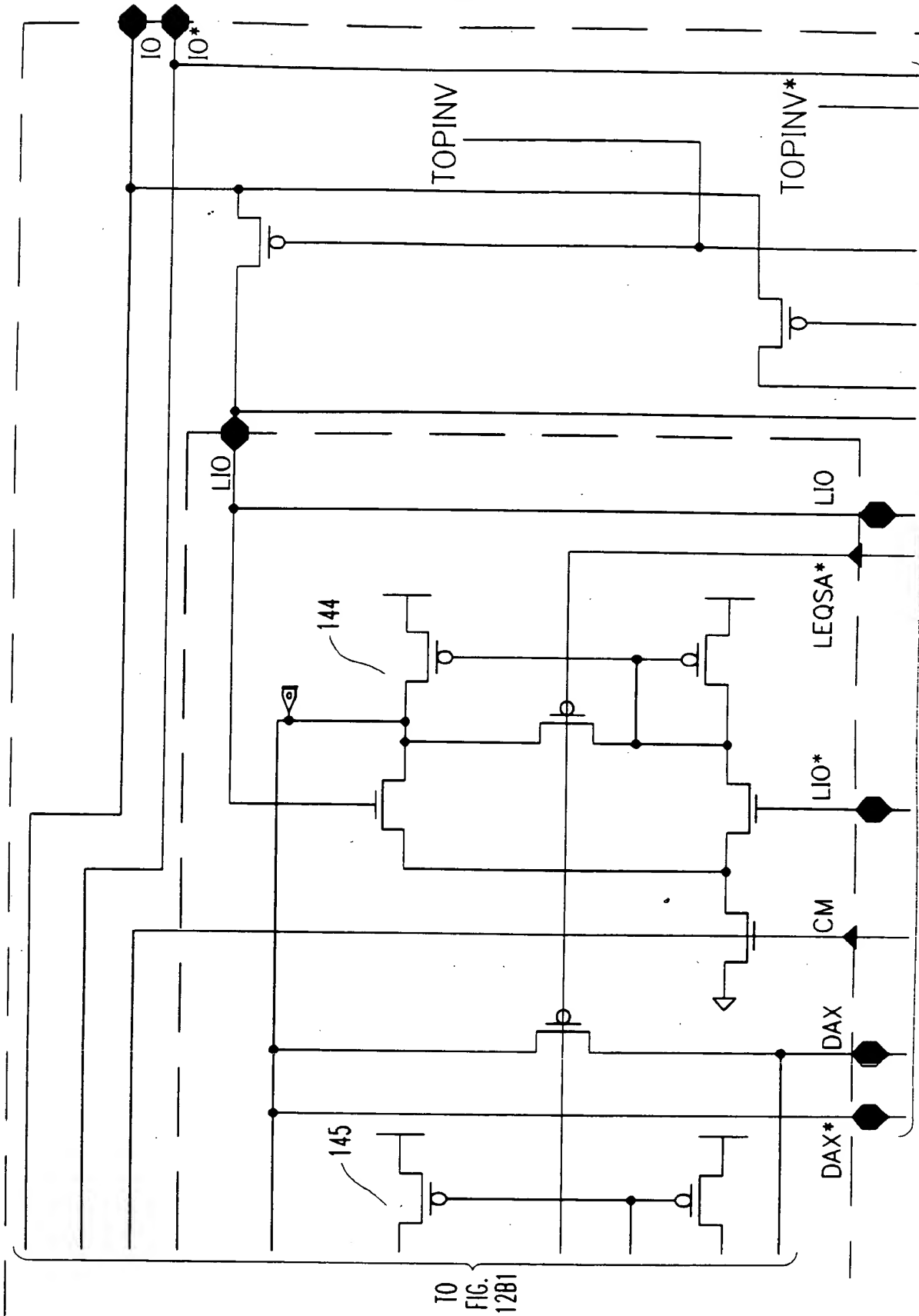
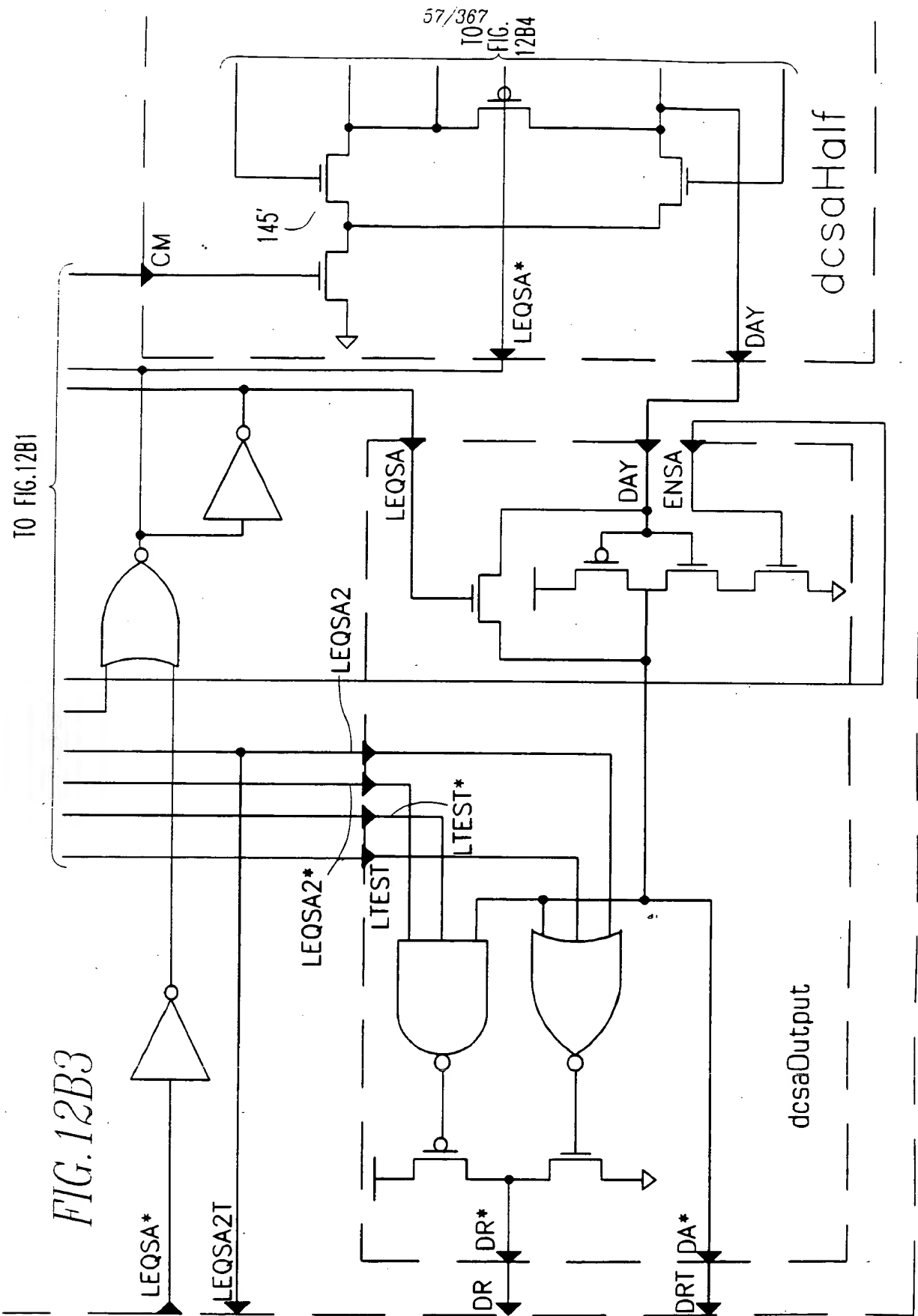
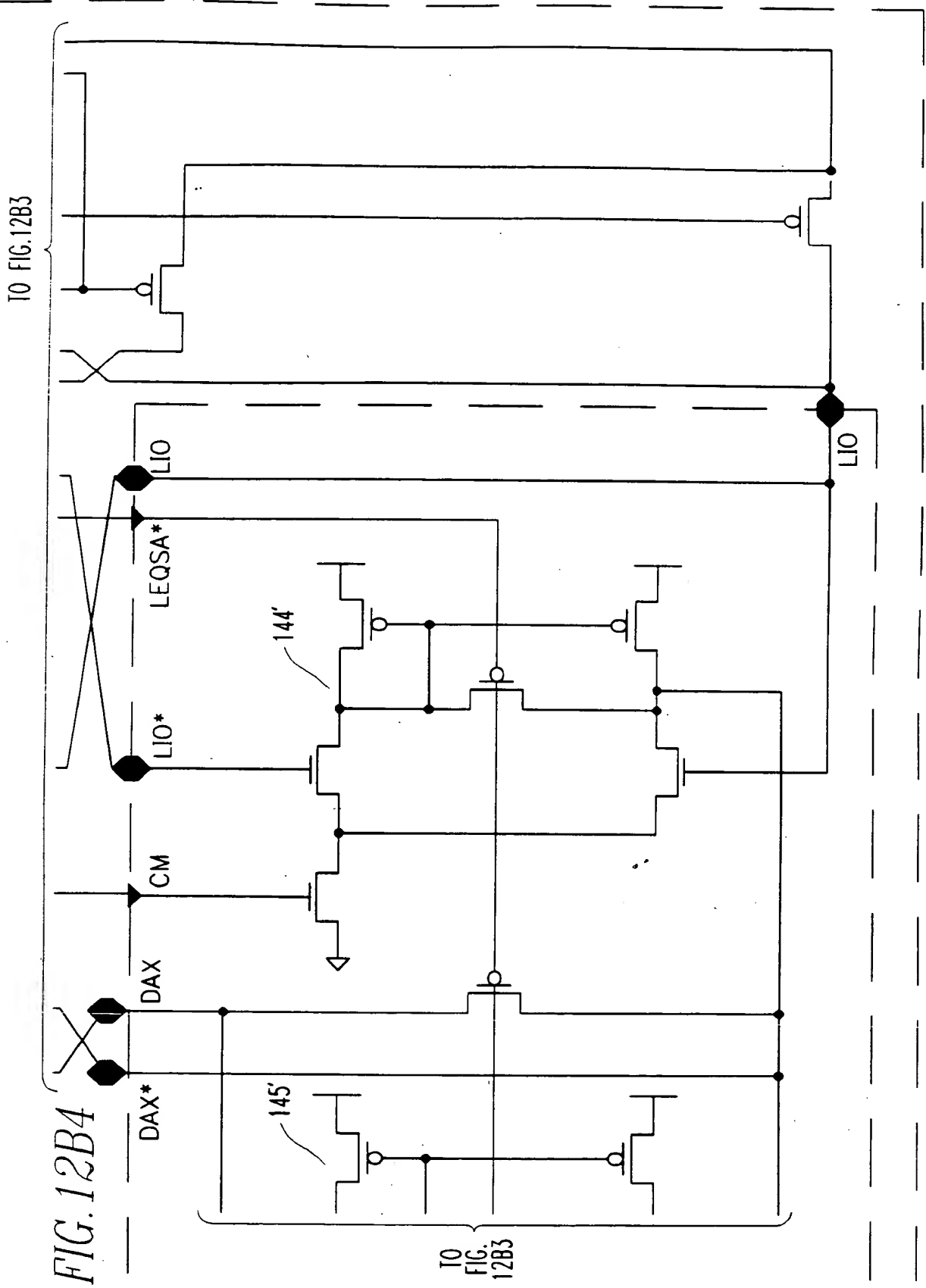
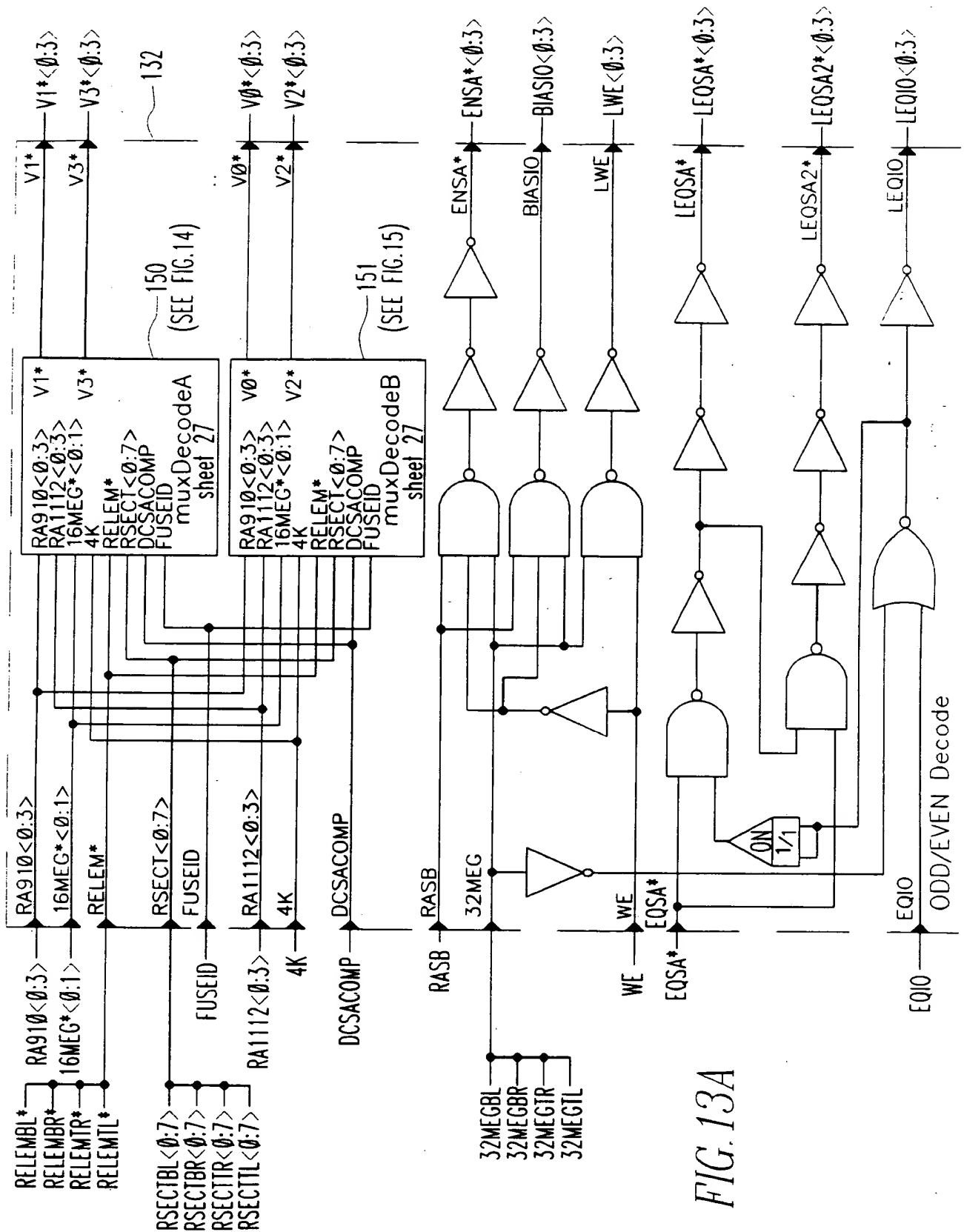


FIG. 12B2

TO FIG. 12B4







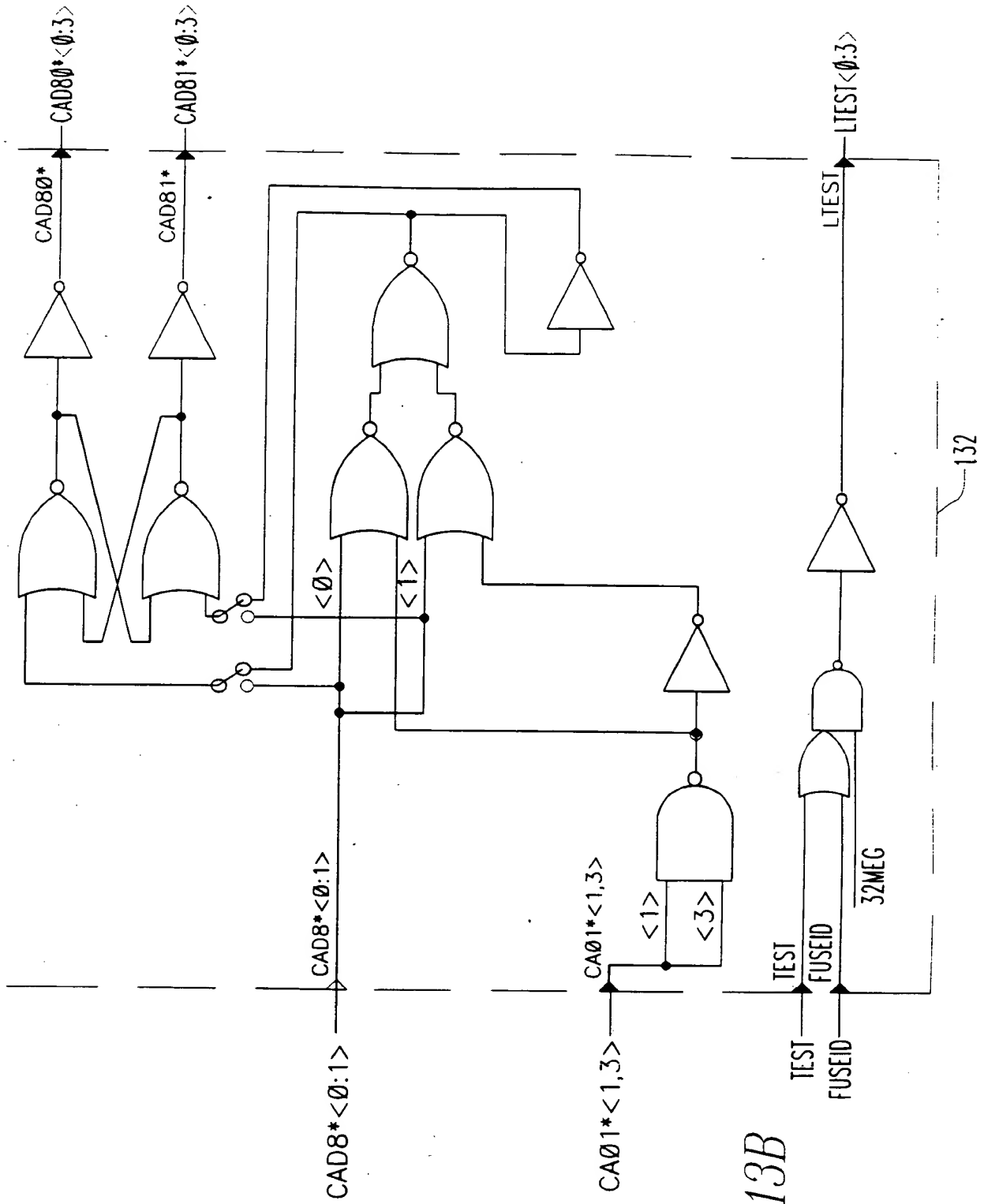


FIG. 13B

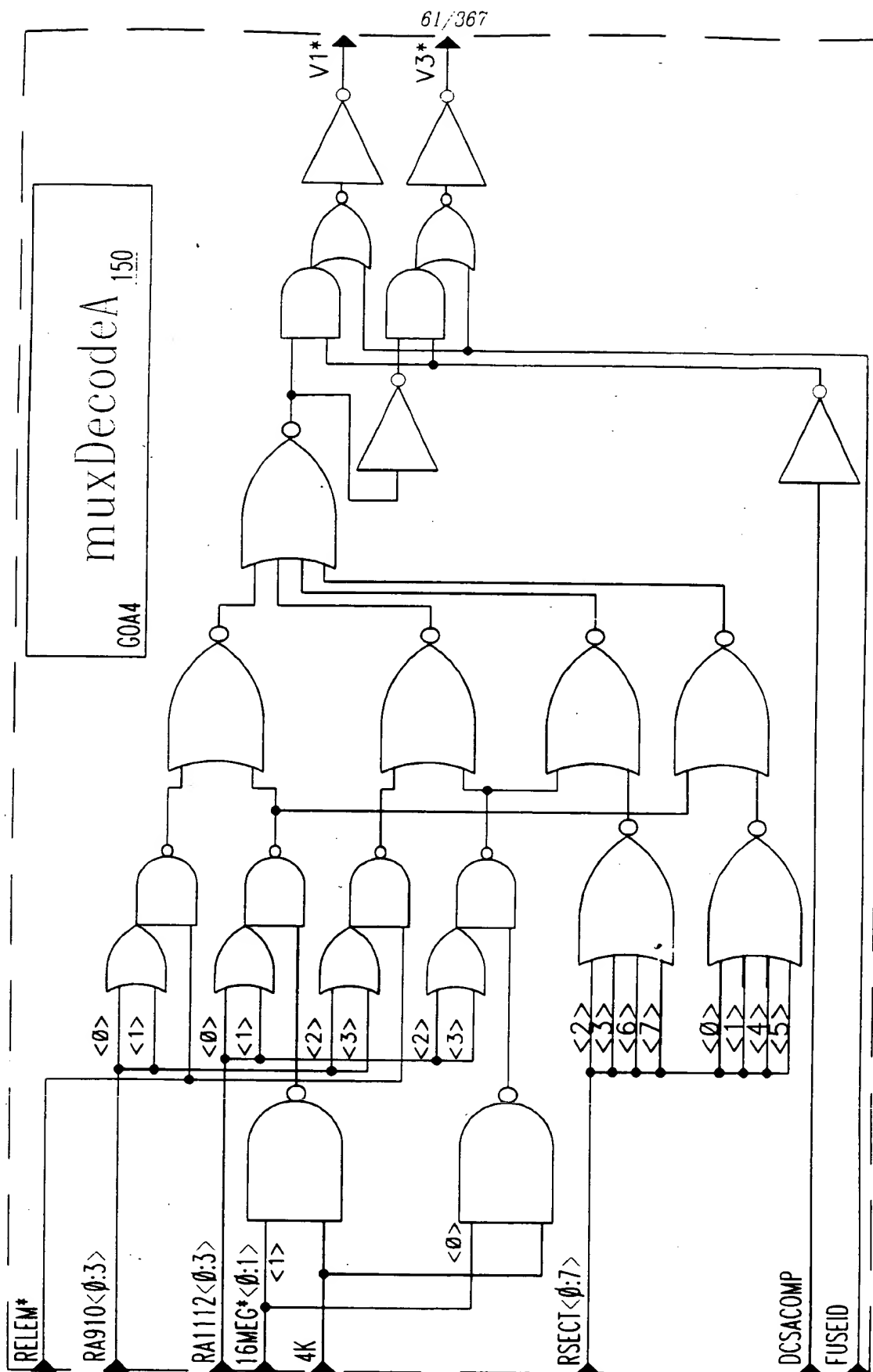
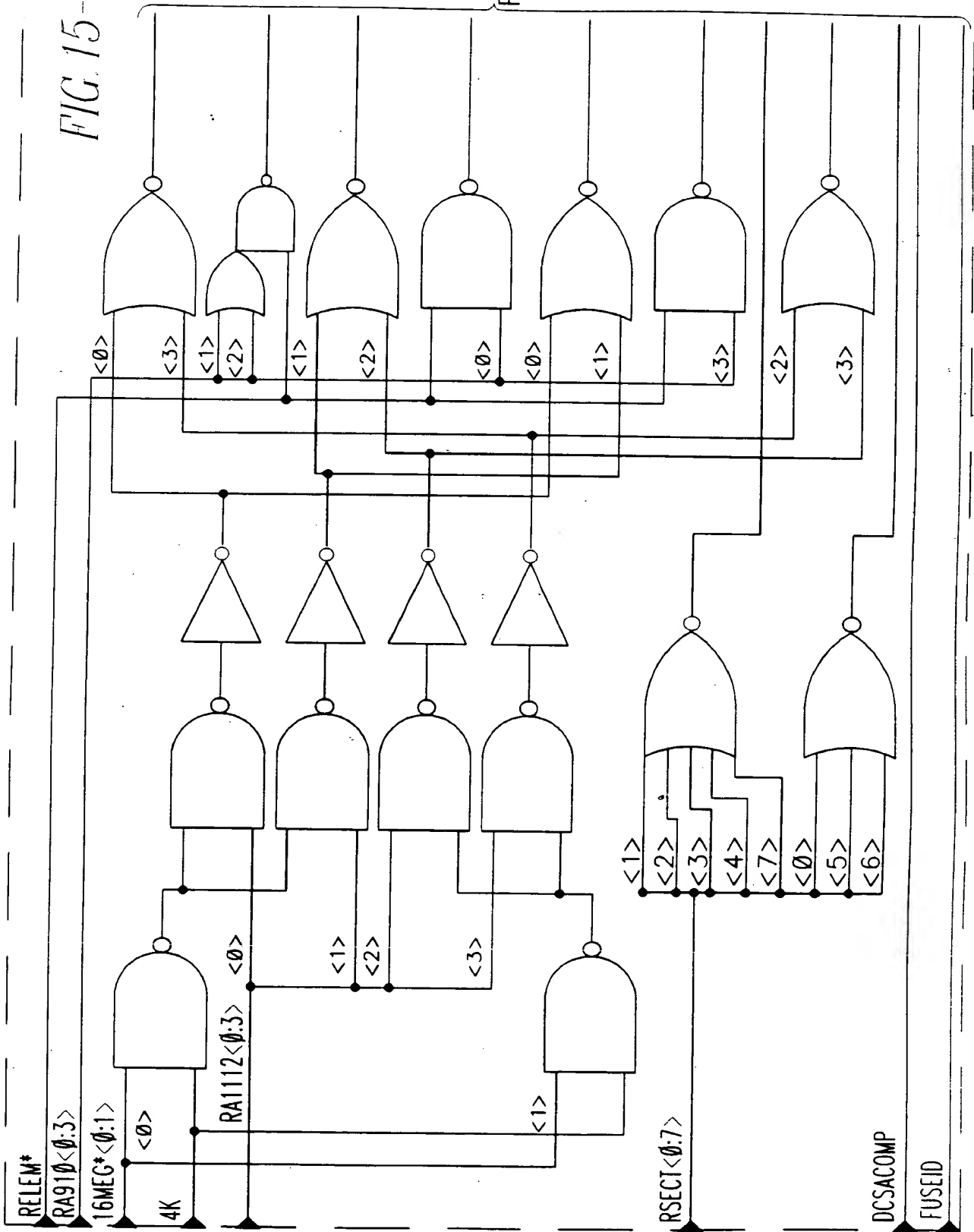


FIG. 14



muxDecodeB

151

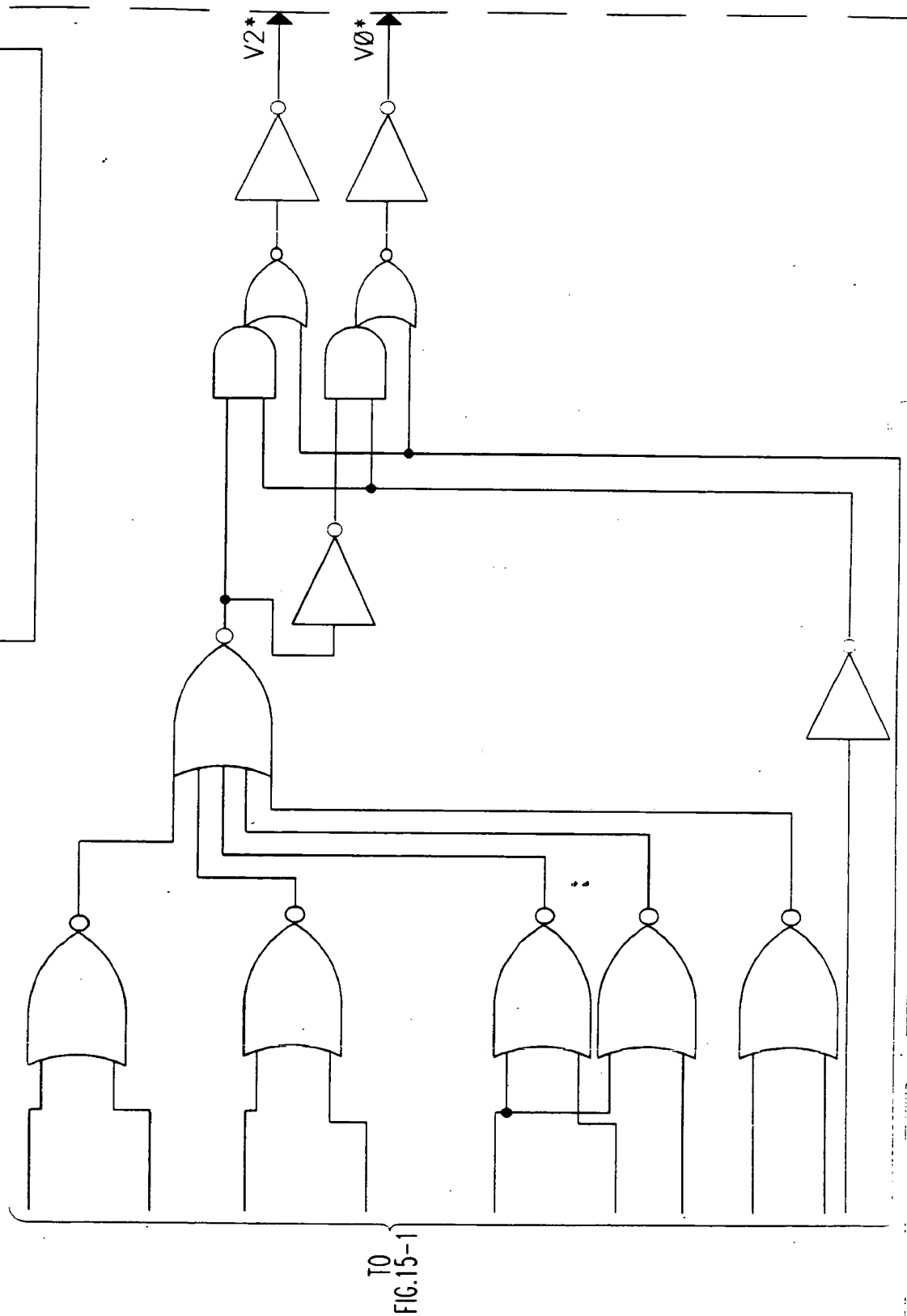
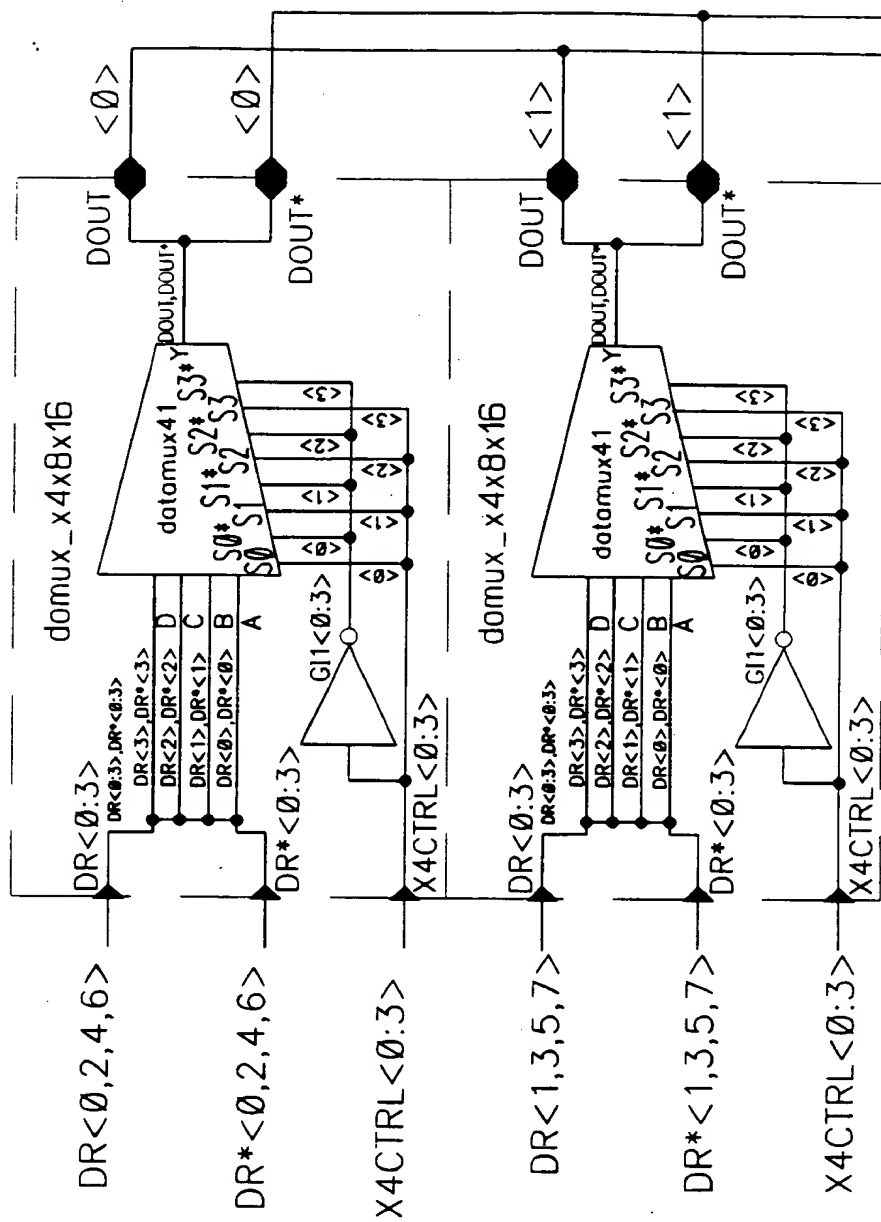
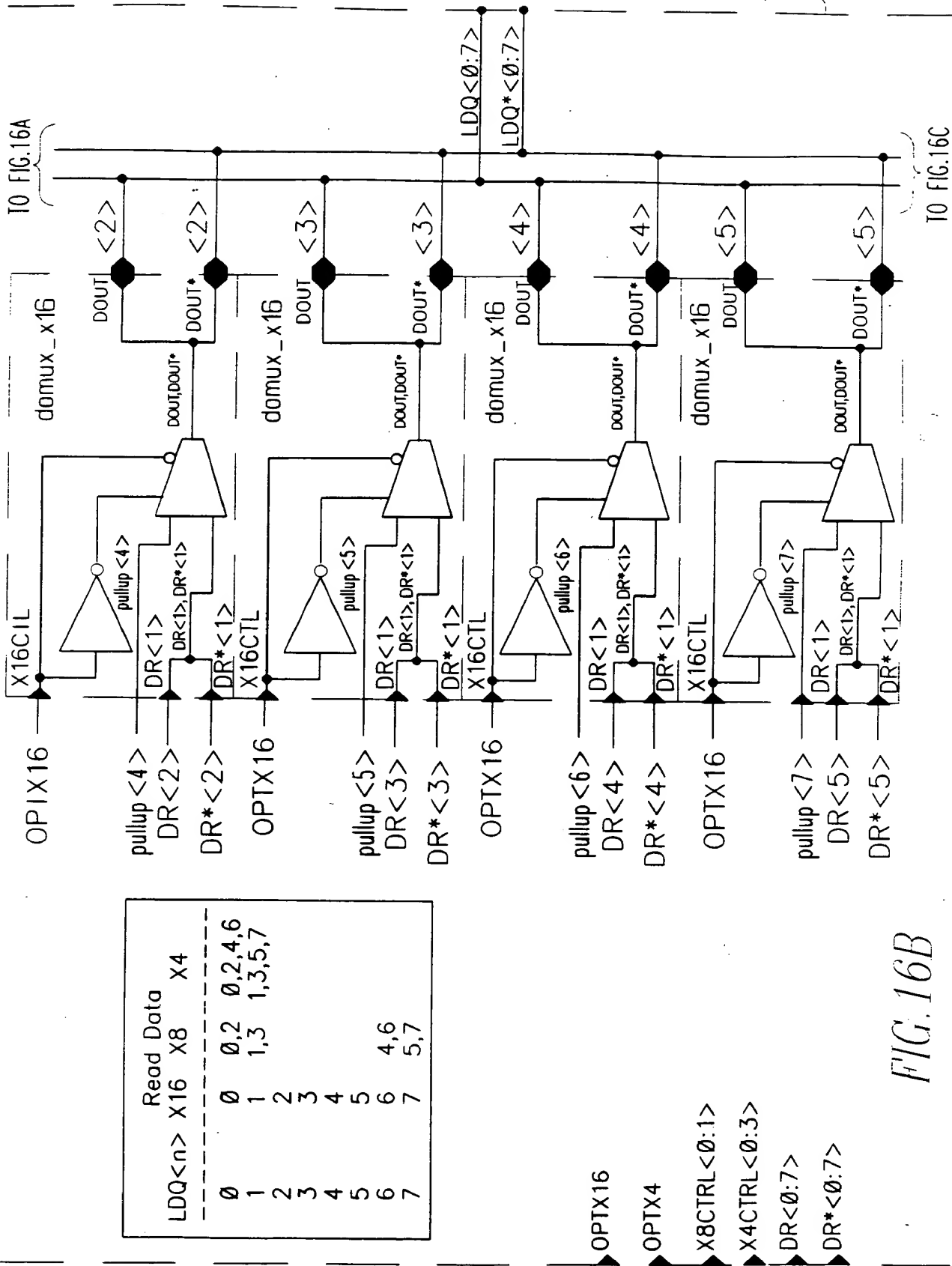
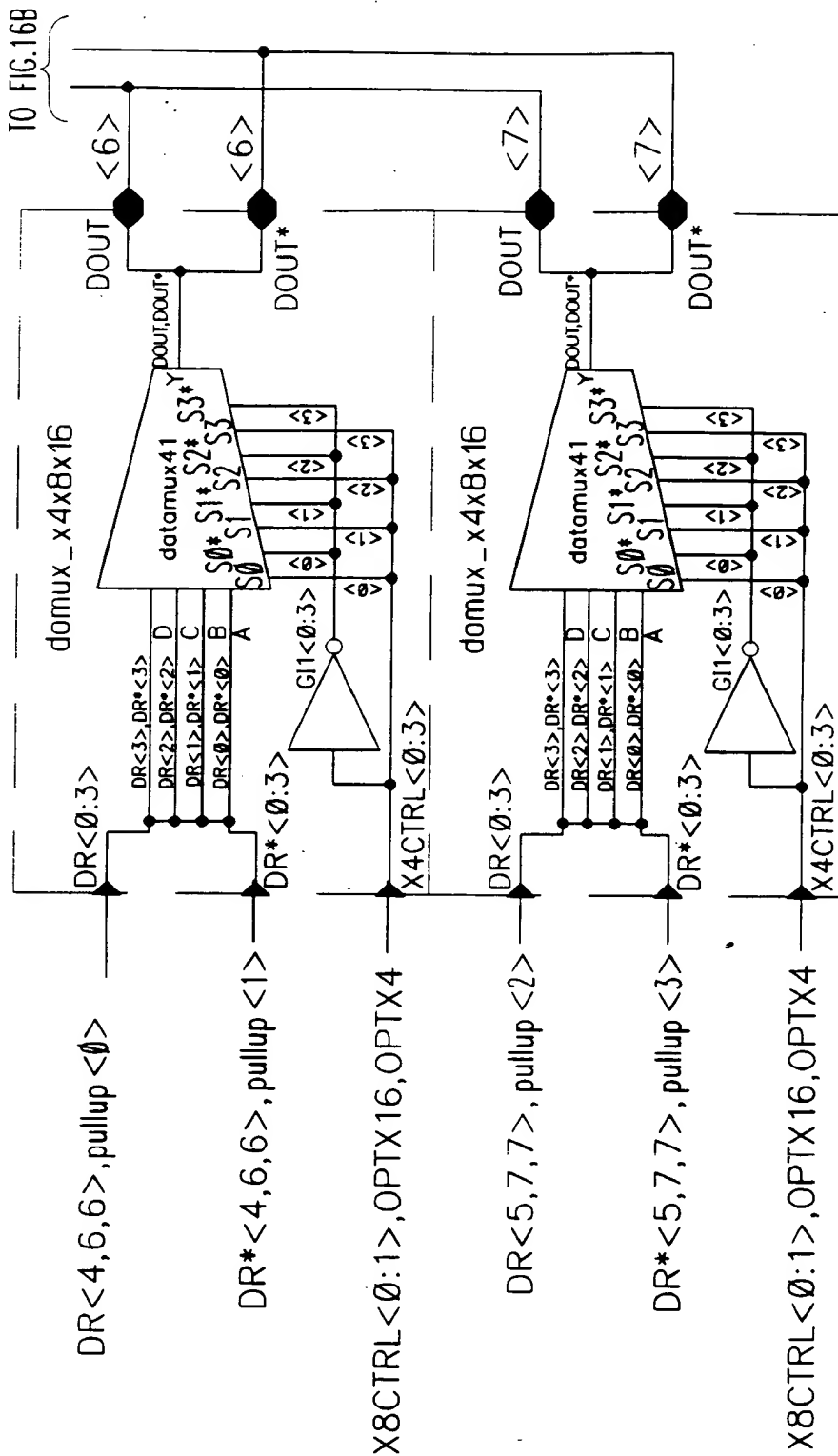


FIG. 16A



TO FIG. 16B

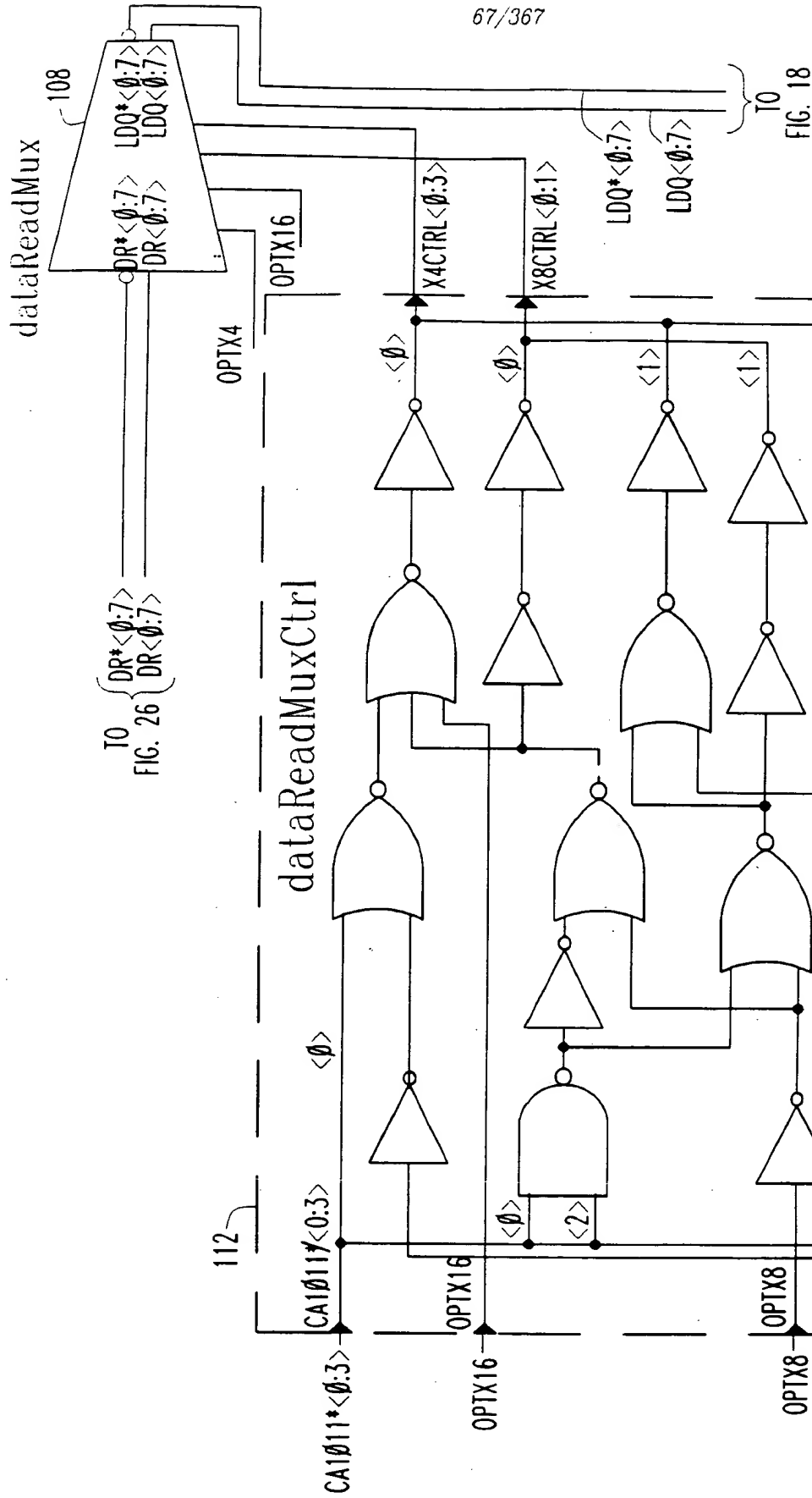


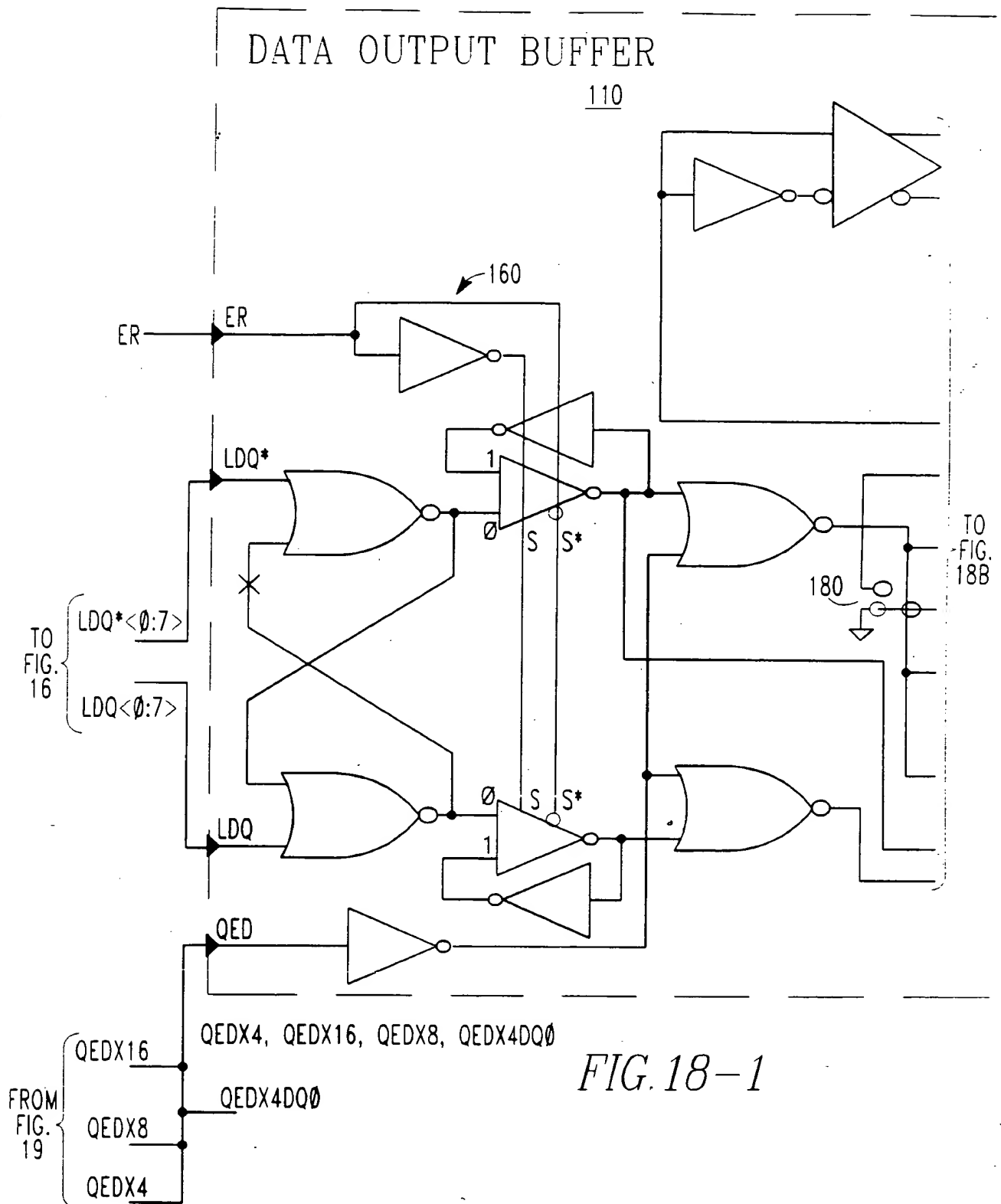


pullup <0:7>

dataReadMux

FIG. 16C





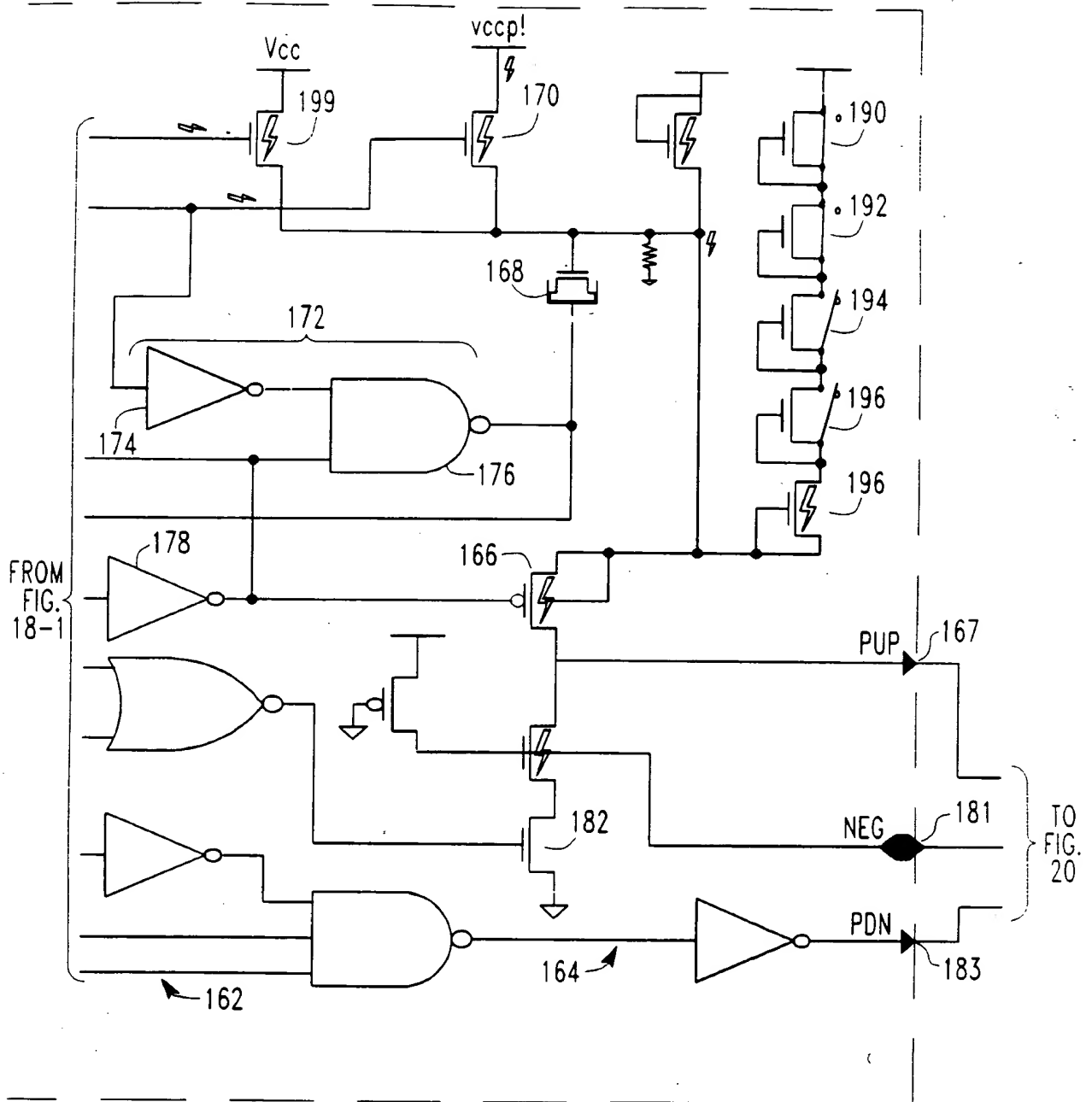


FIG. 18-2

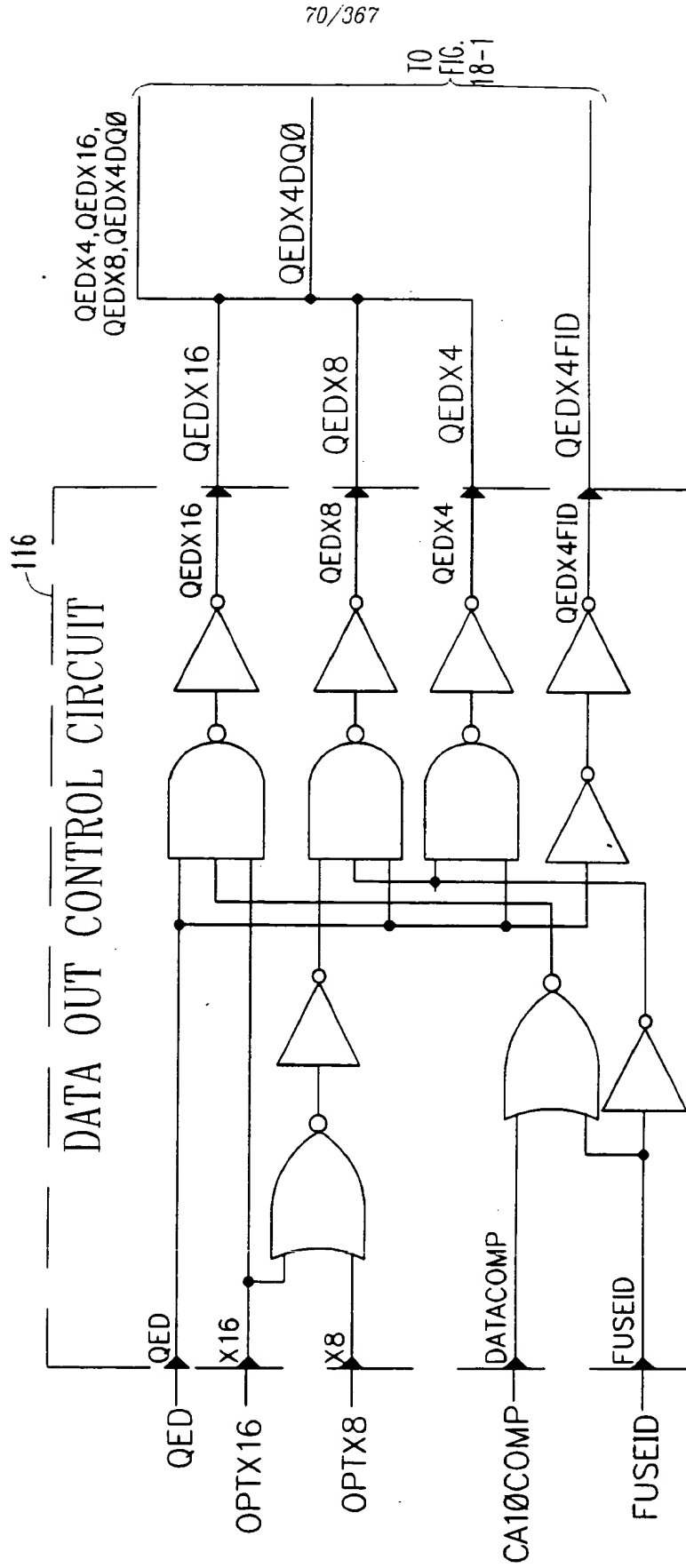


FIG. 19

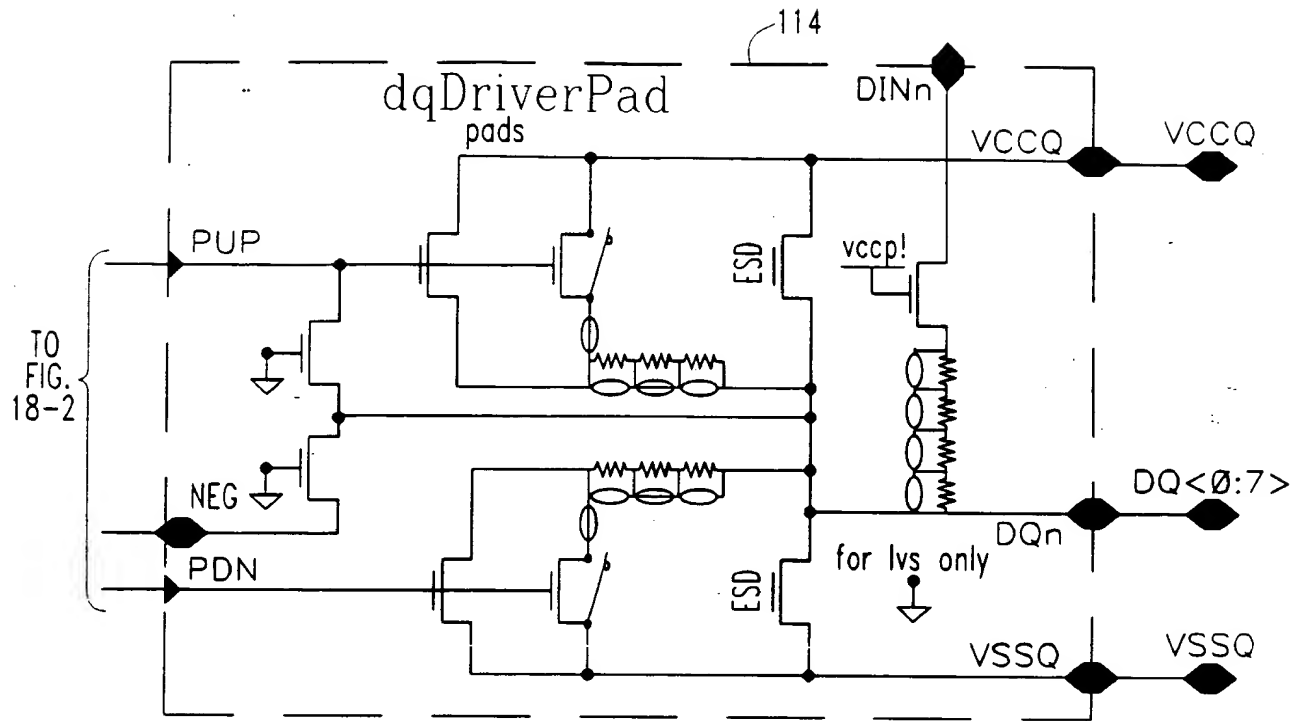
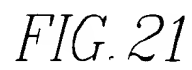


FIG. 20



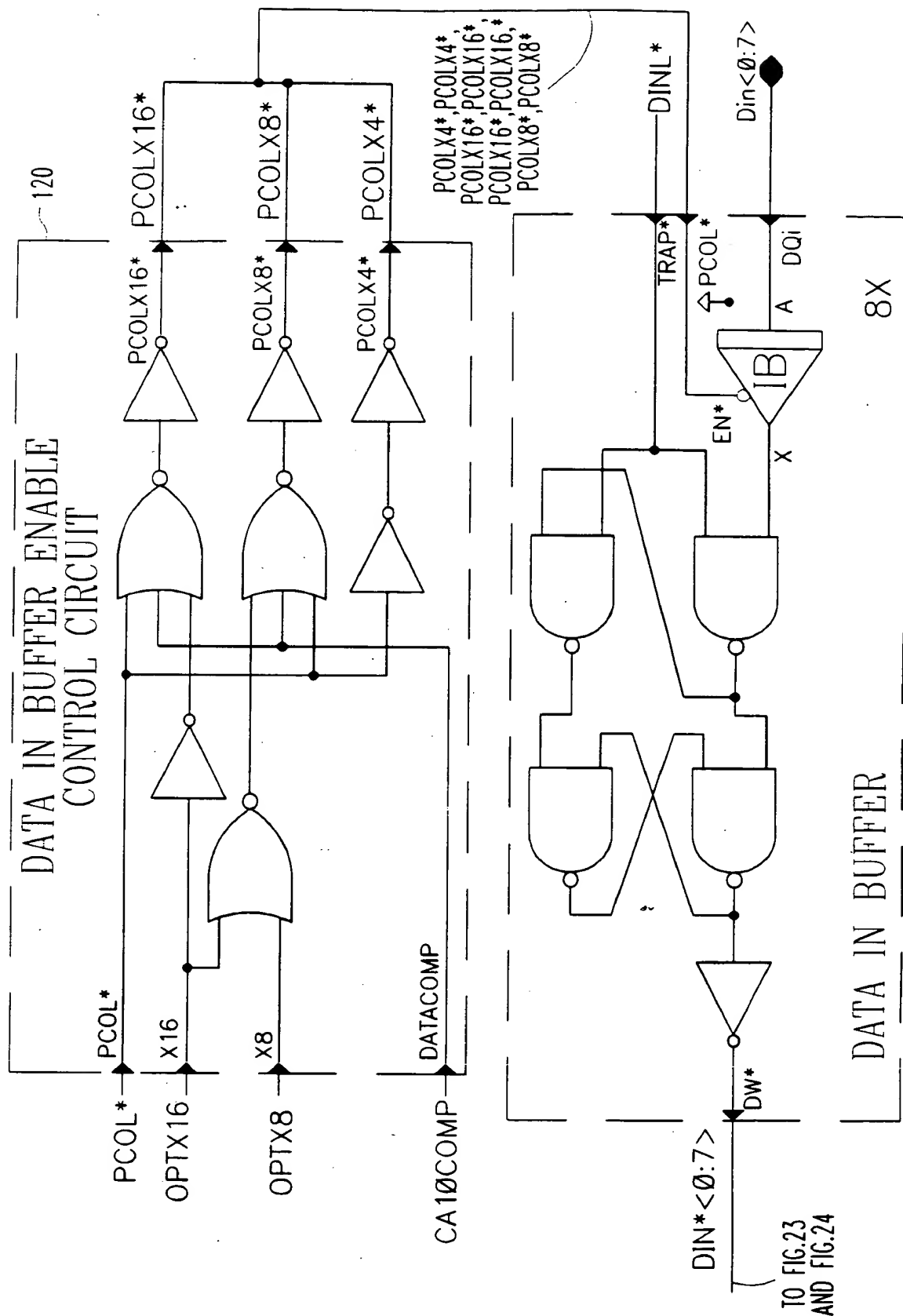
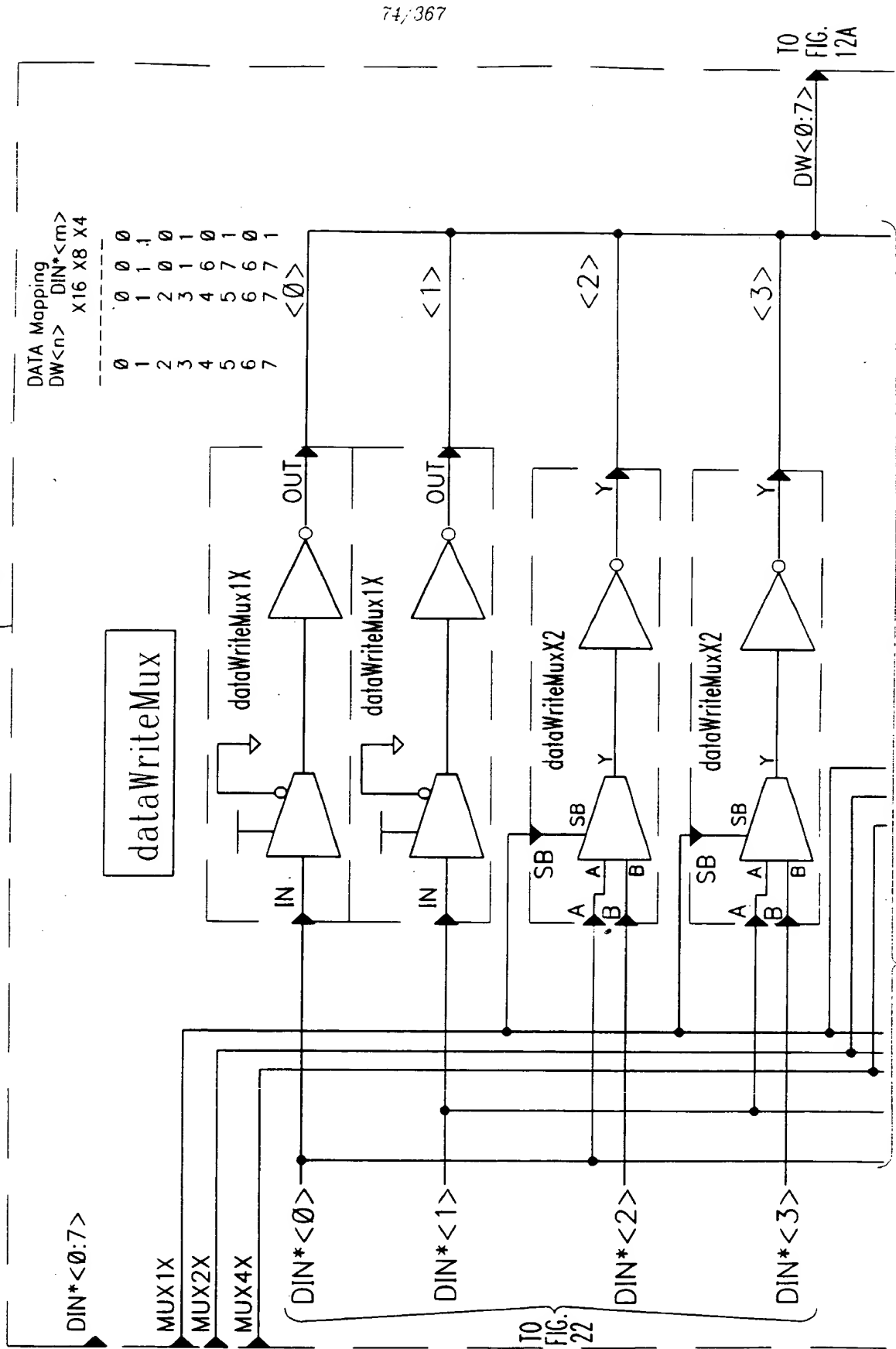


FIG. 22

TO FIG. 23
AND FIG. 24

FIG. 23-1

122



74/367

FIG. 12A

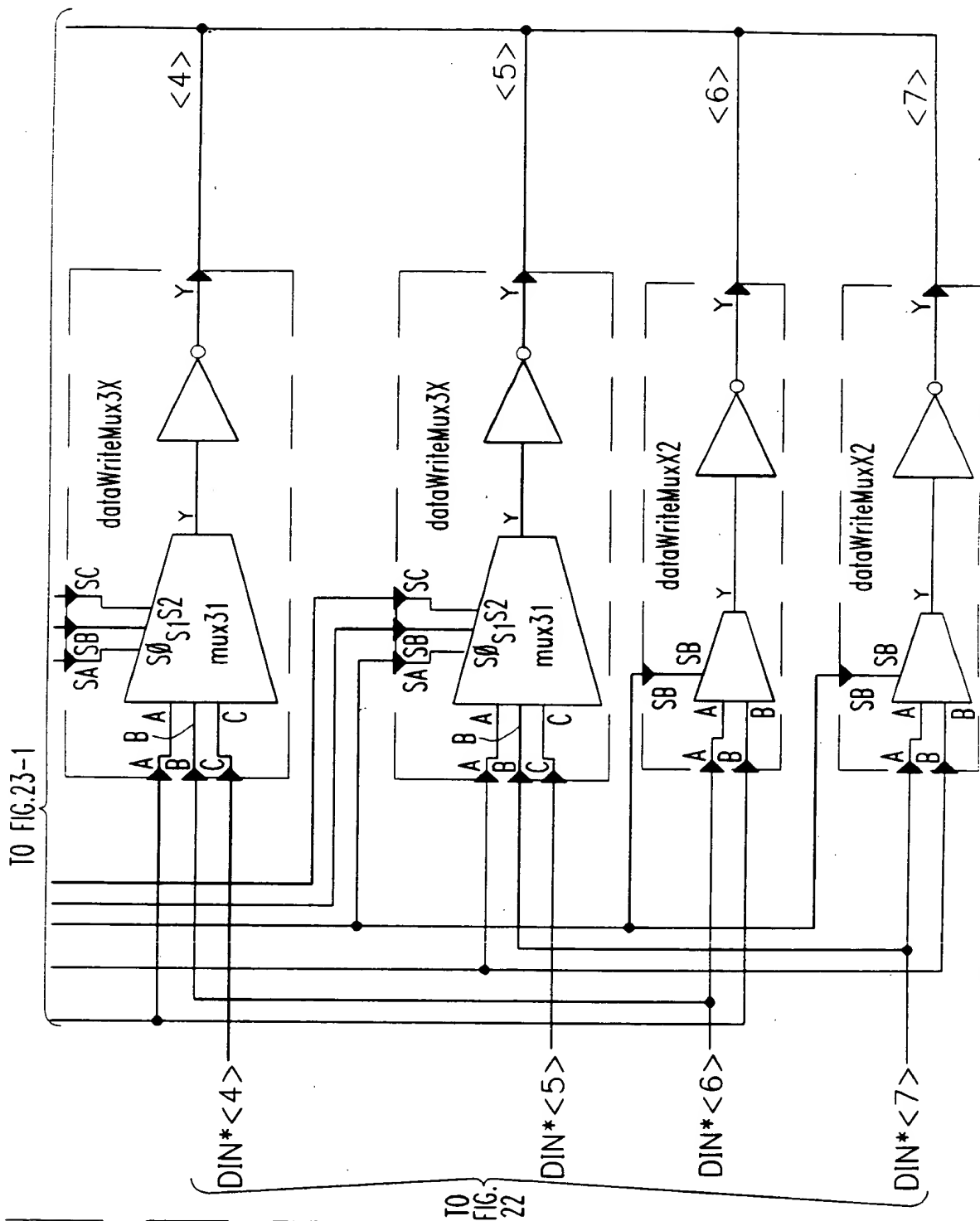
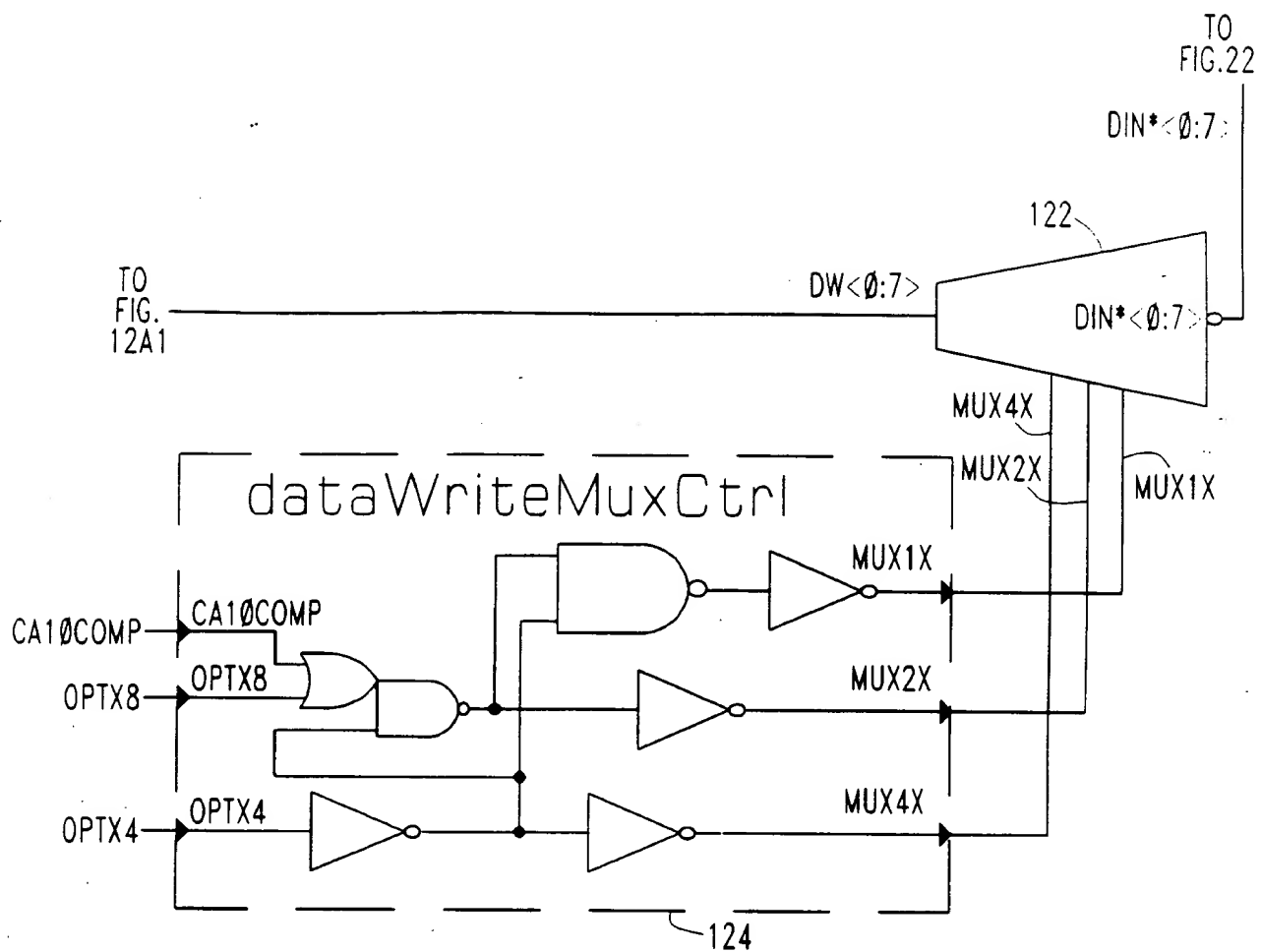


FIG. 23-2



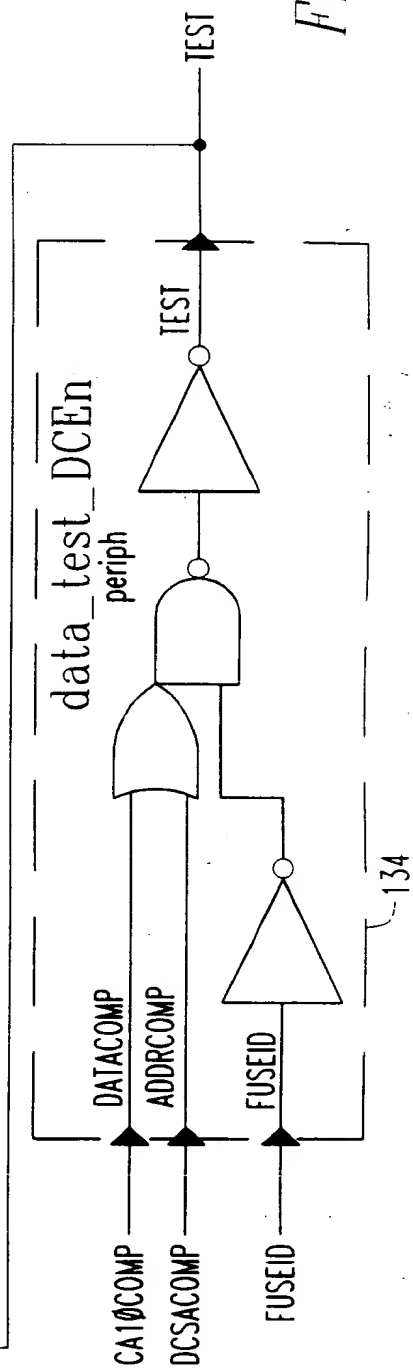
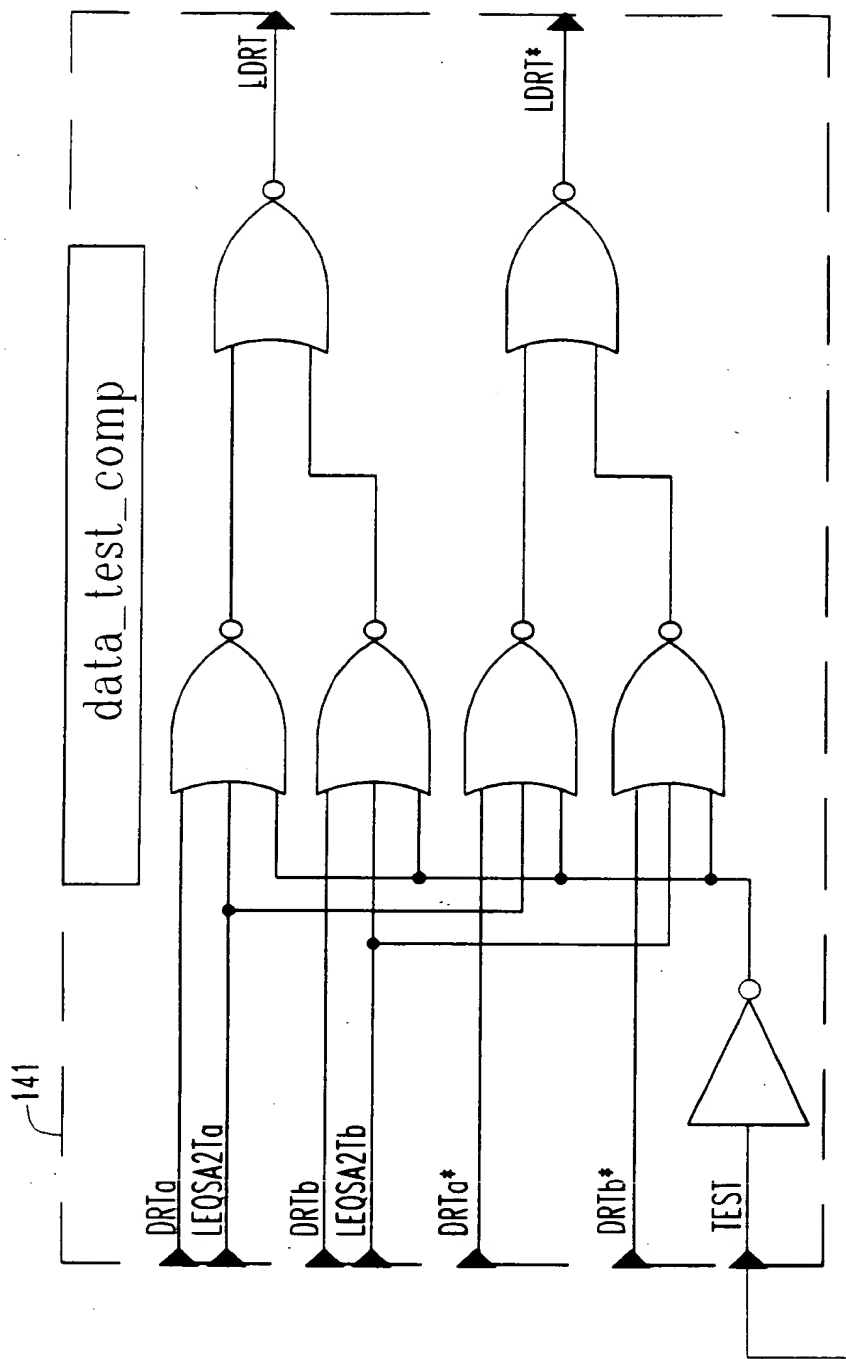
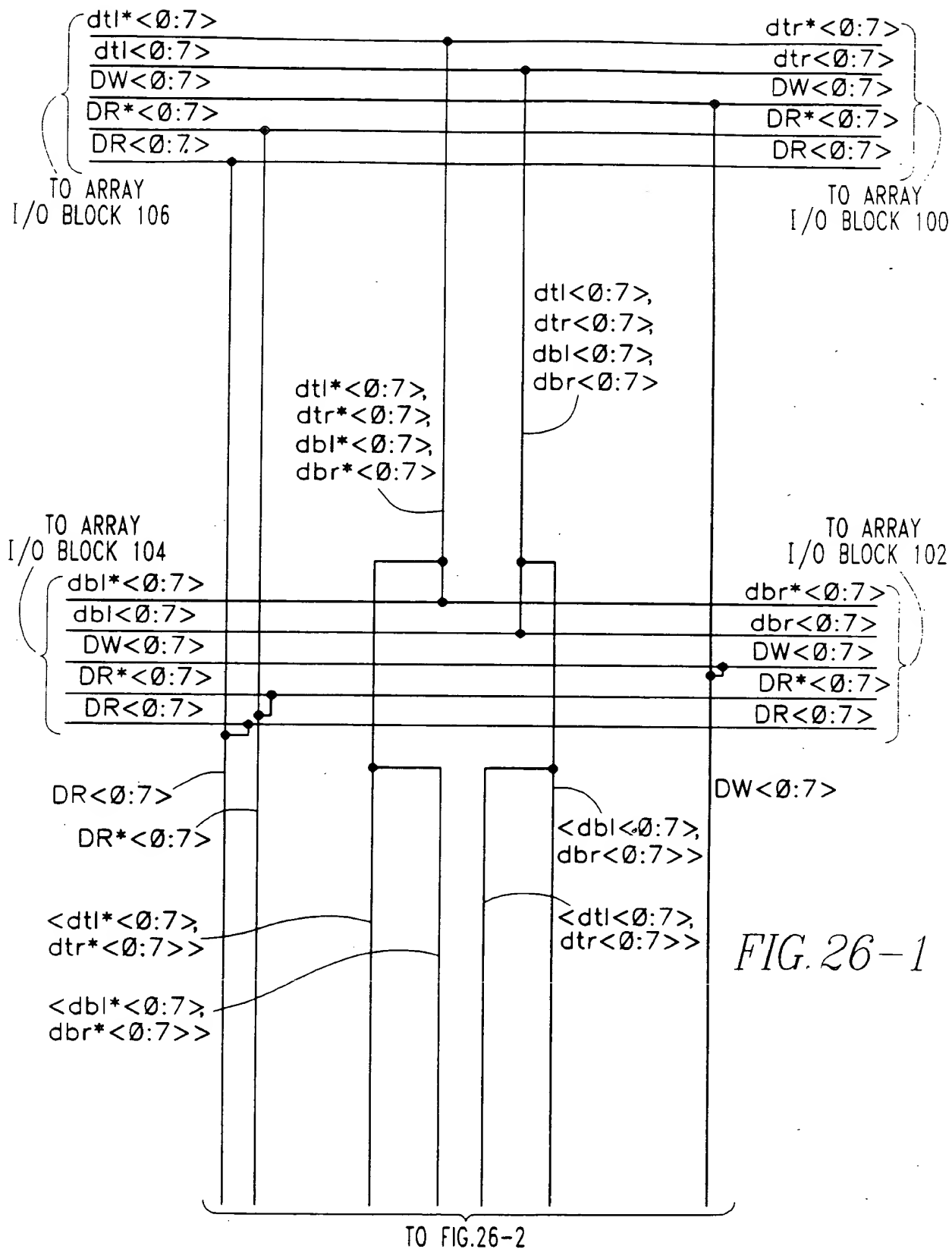


FIG. 25



FROM FIG.26-1

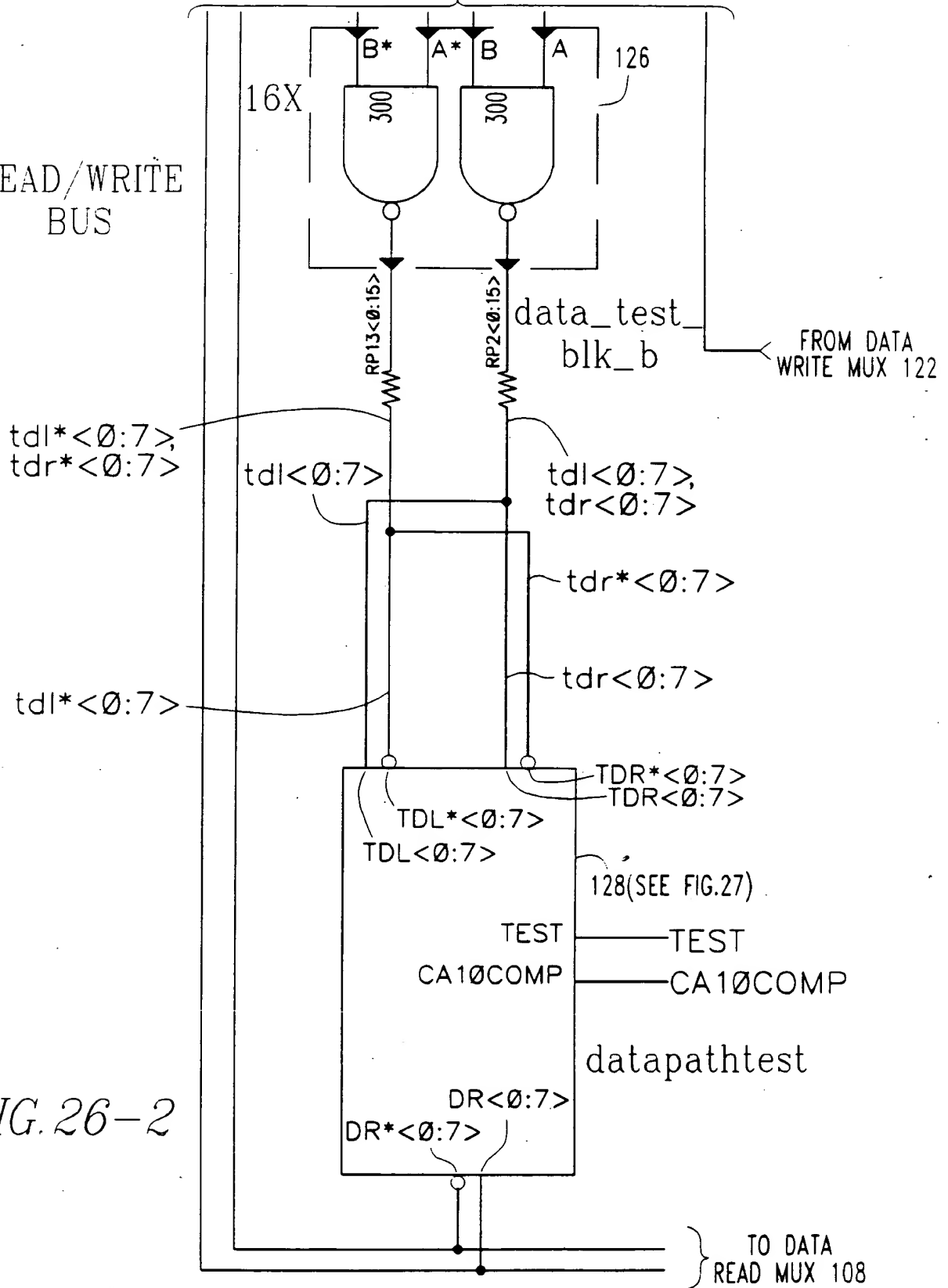
READ/WRITE
BUS

FIG. 26-2

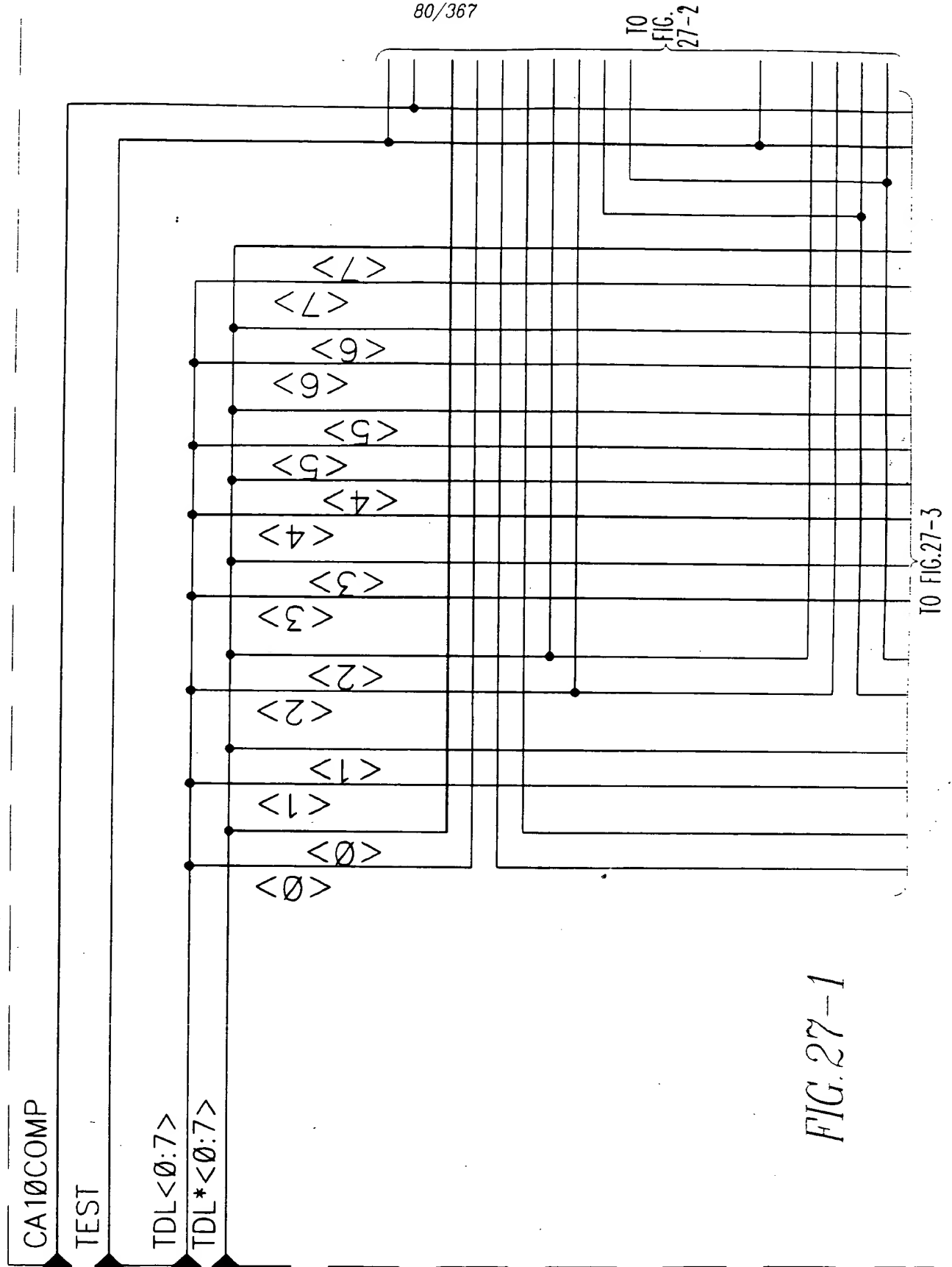
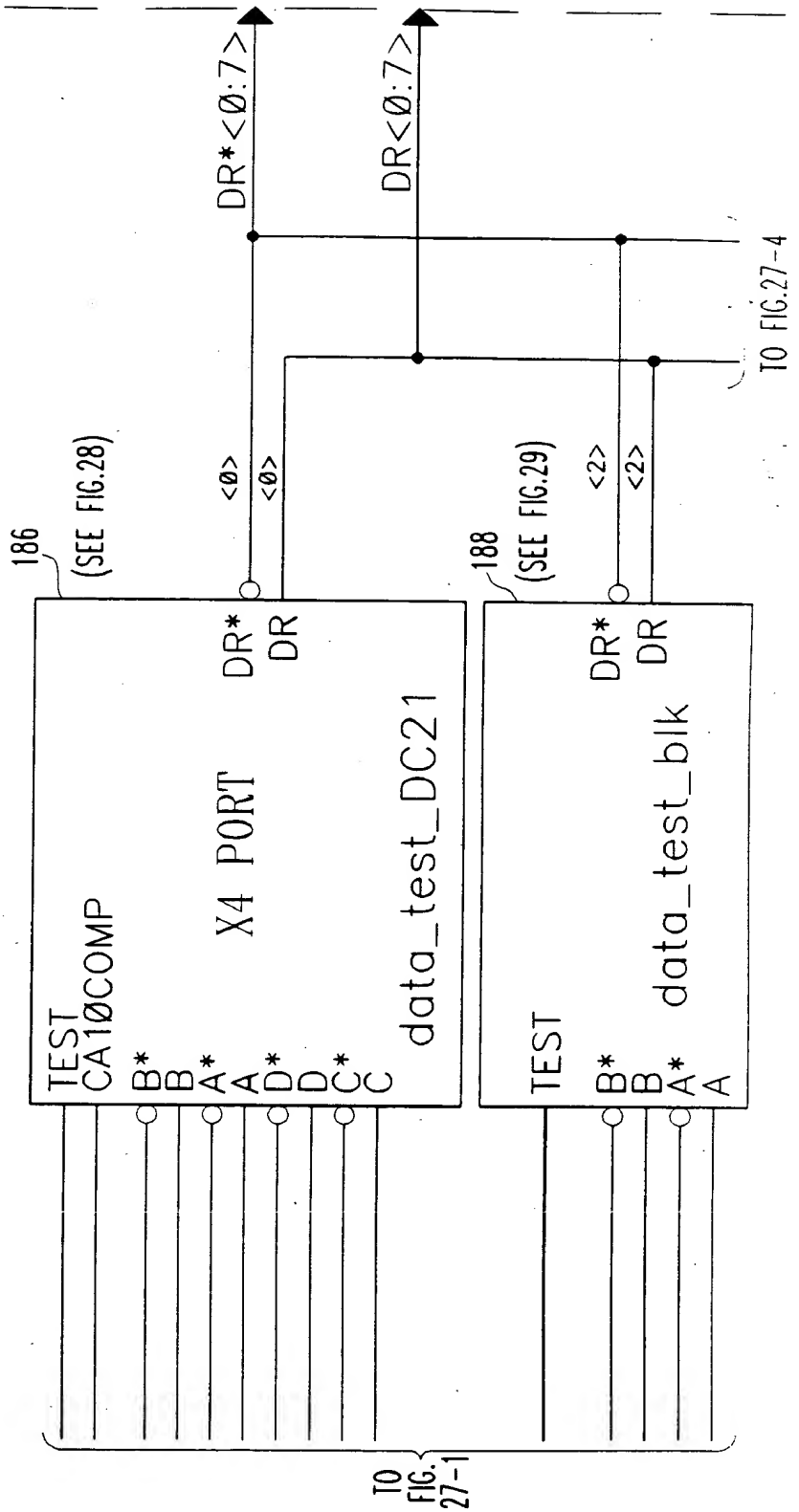
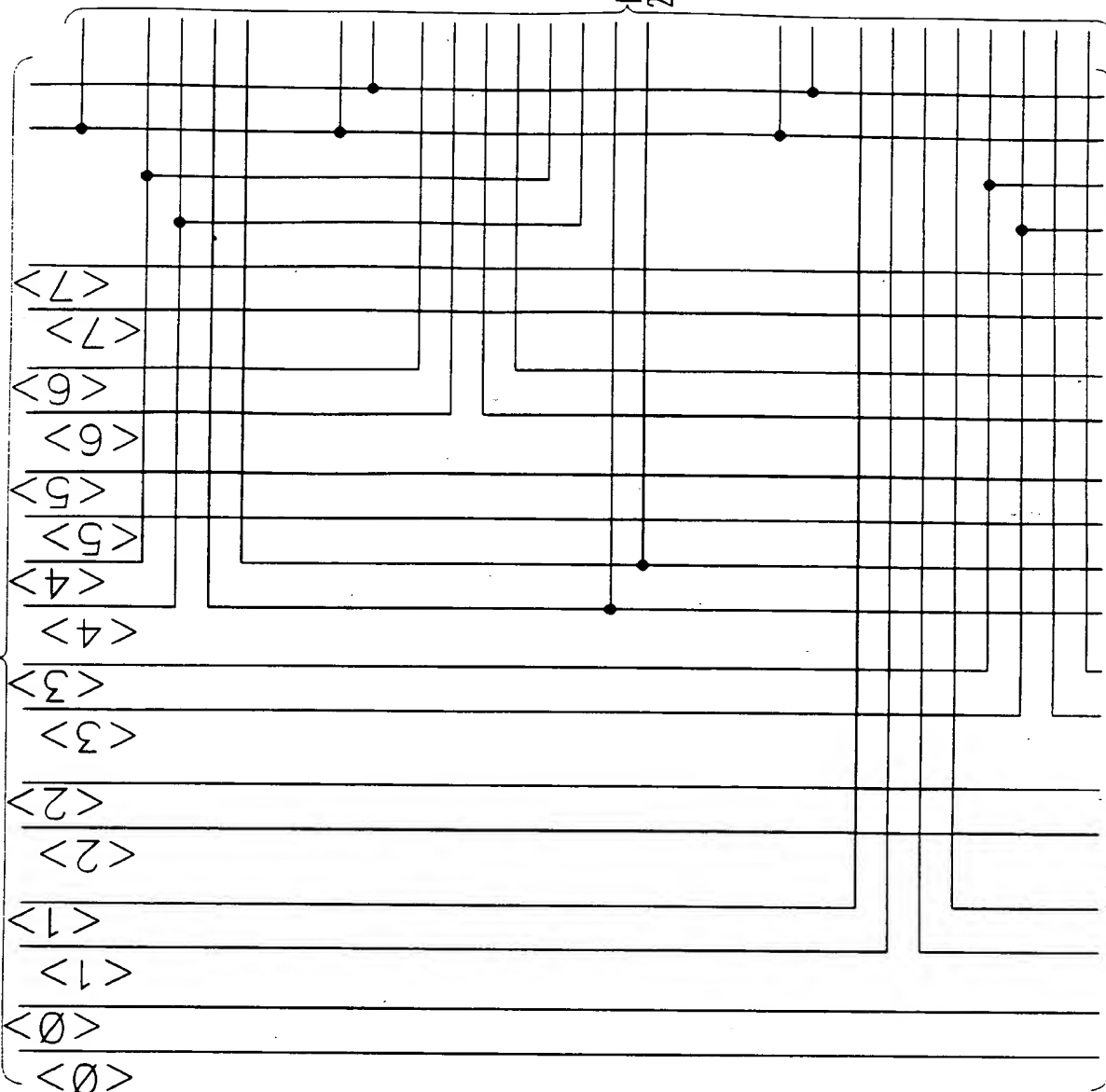


FIG. 27-2

dataPathTest
Block 128



TO FIG. 27-1



82/367

FIG. 27-4

FIG. 27-3

TO FIG. 27-5

FIG. 27-3

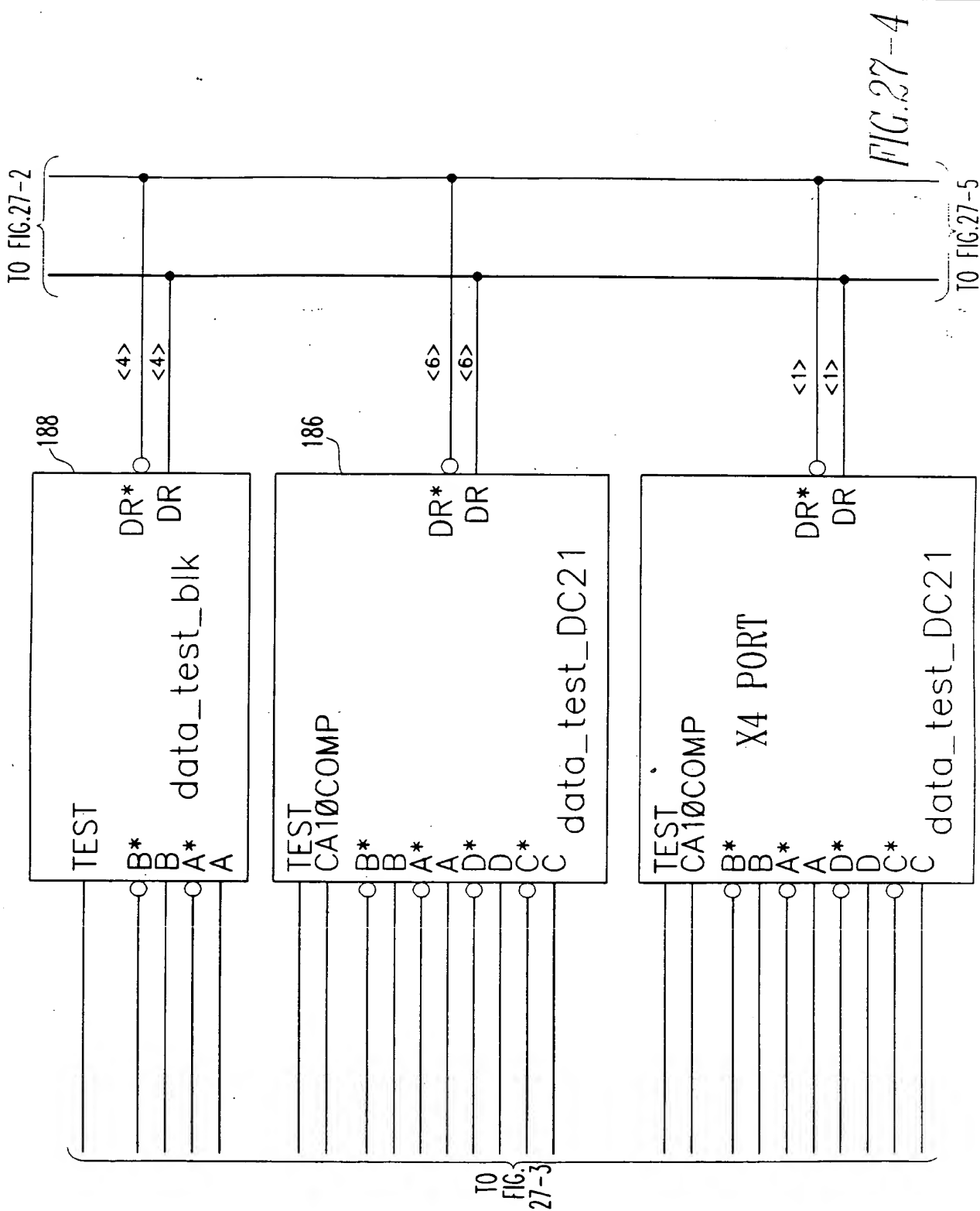


FIG. 27-4

FIG. 27-3

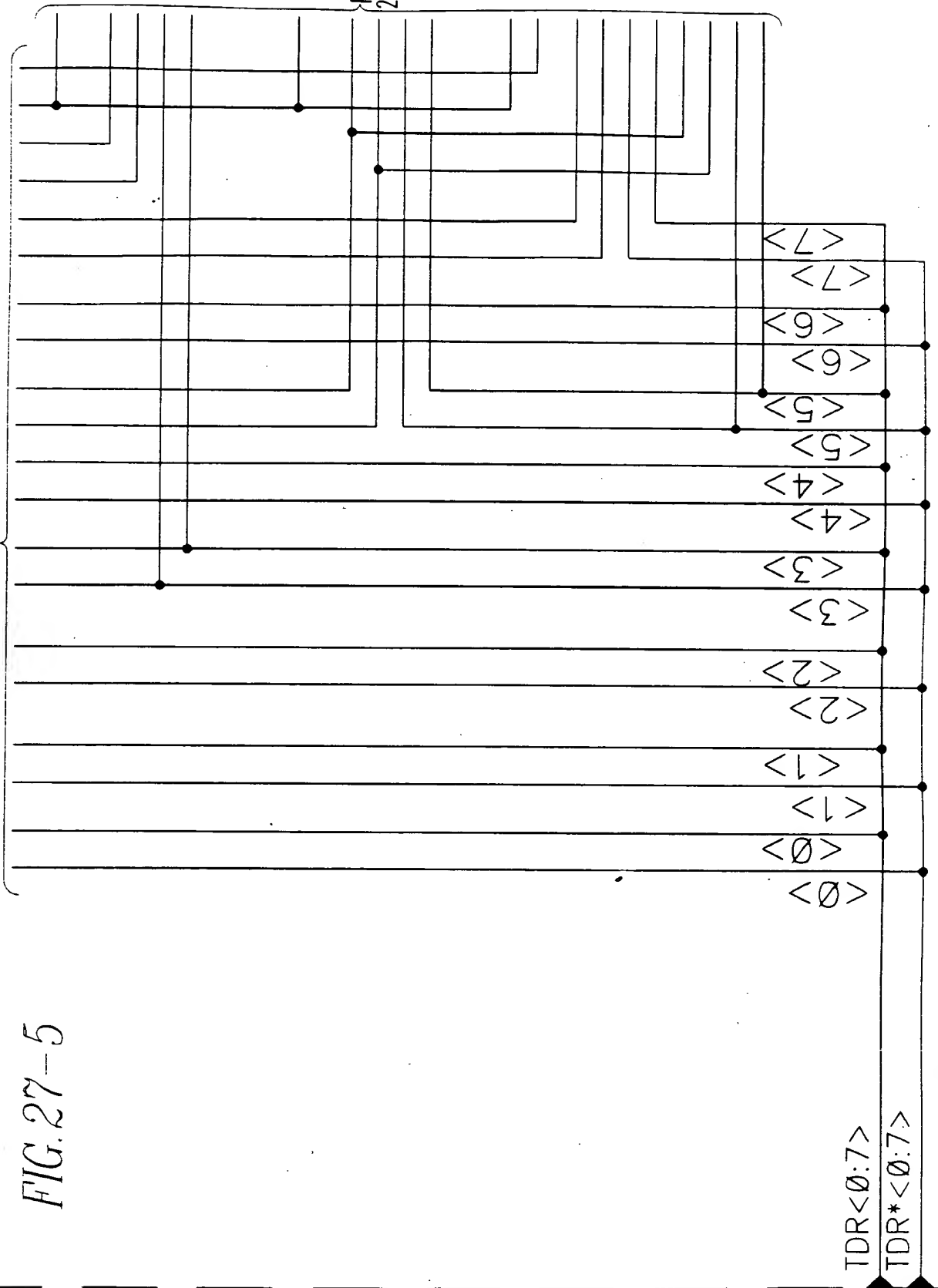


FIG. 27-5

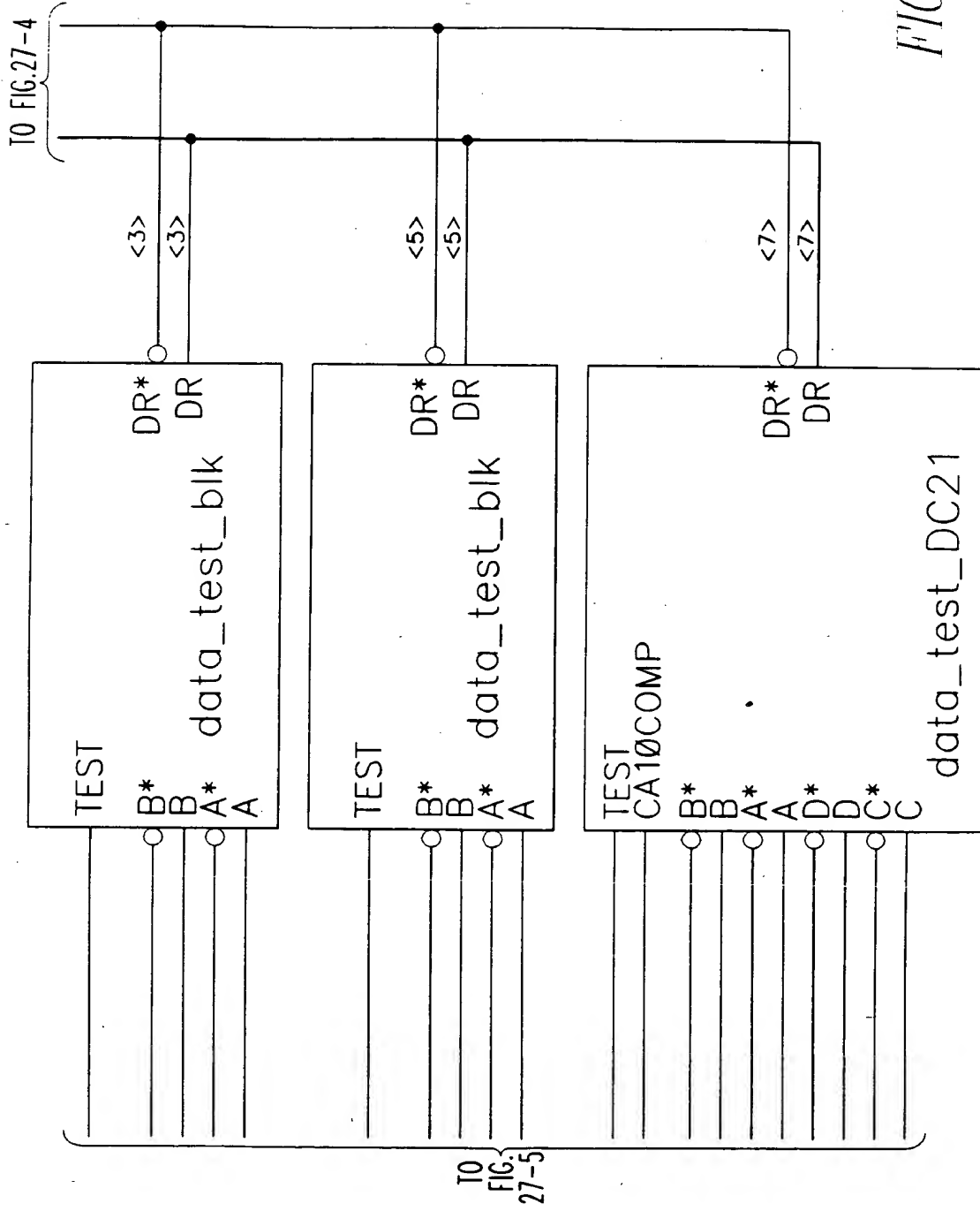


FIG. 27-6

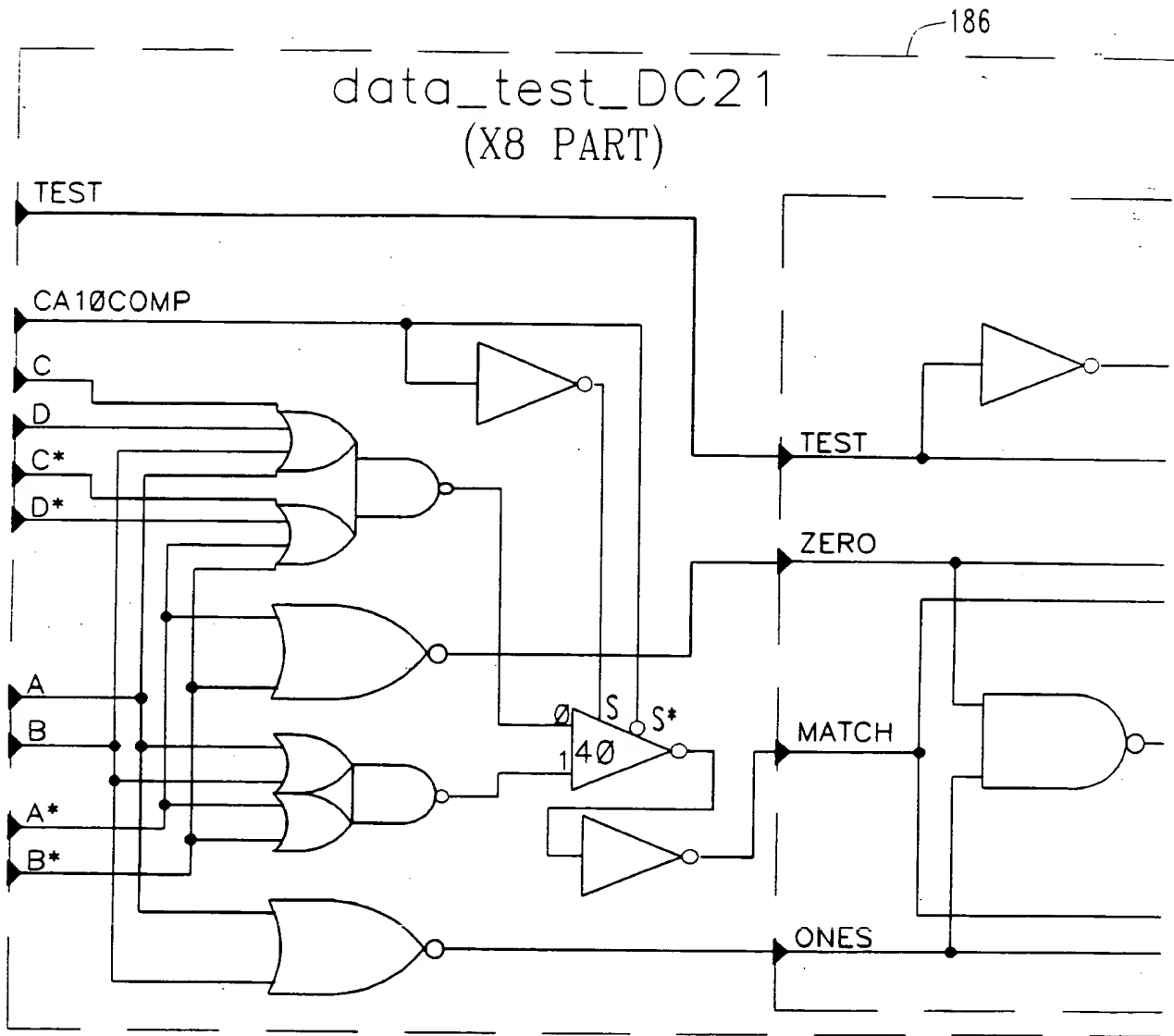


FIG. 28-1

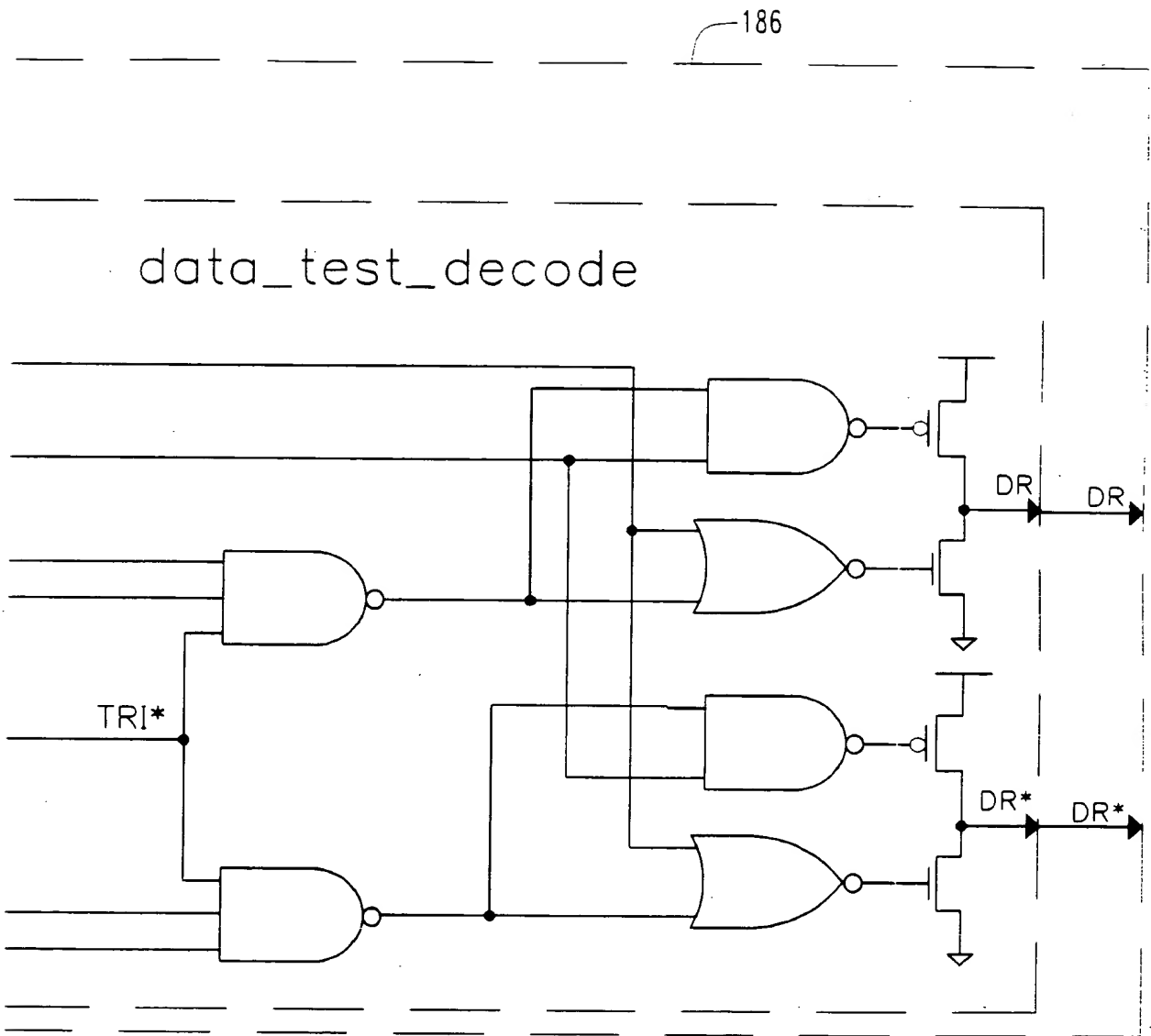


FIG. 28-2

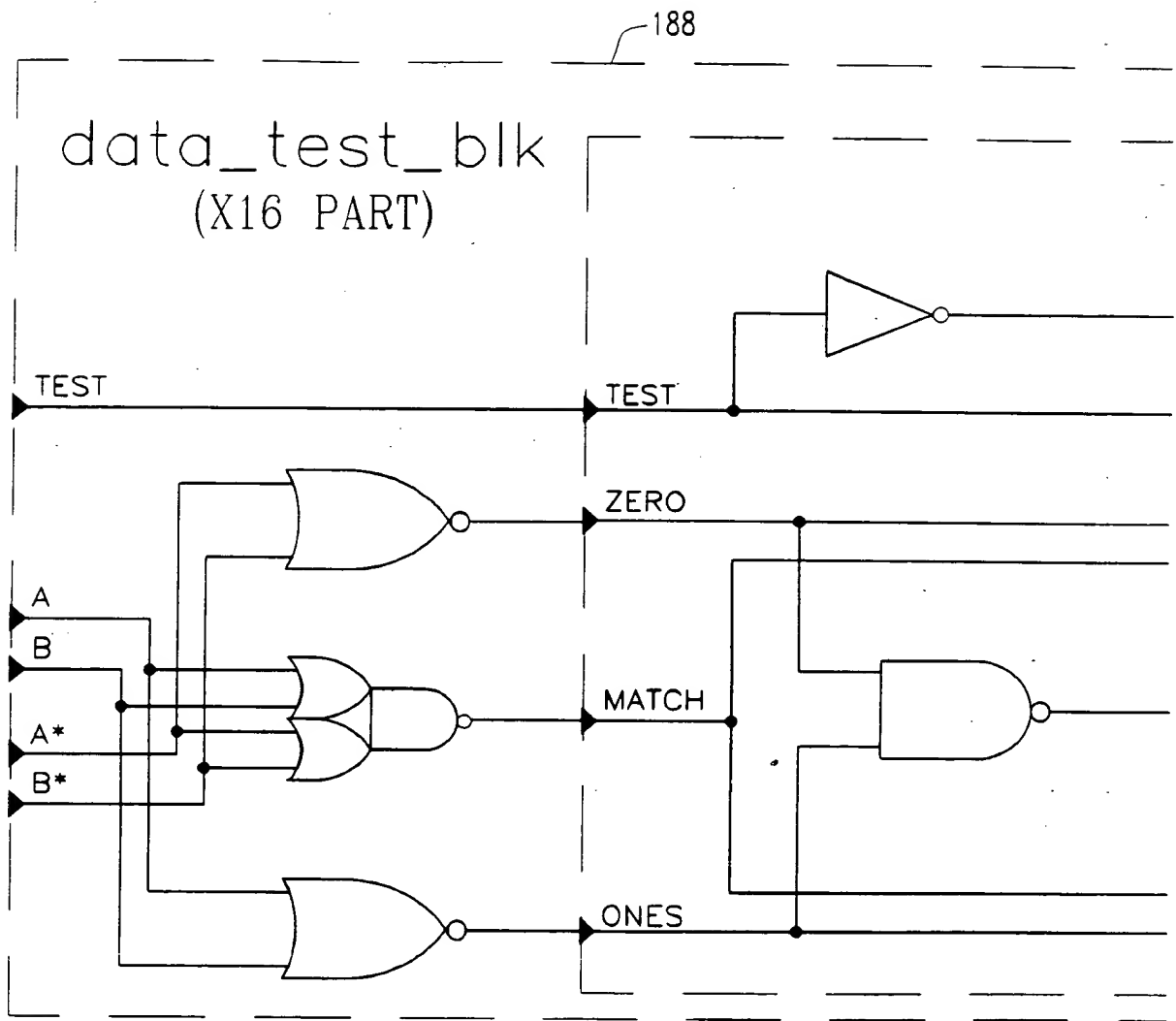


FIG. 29-1

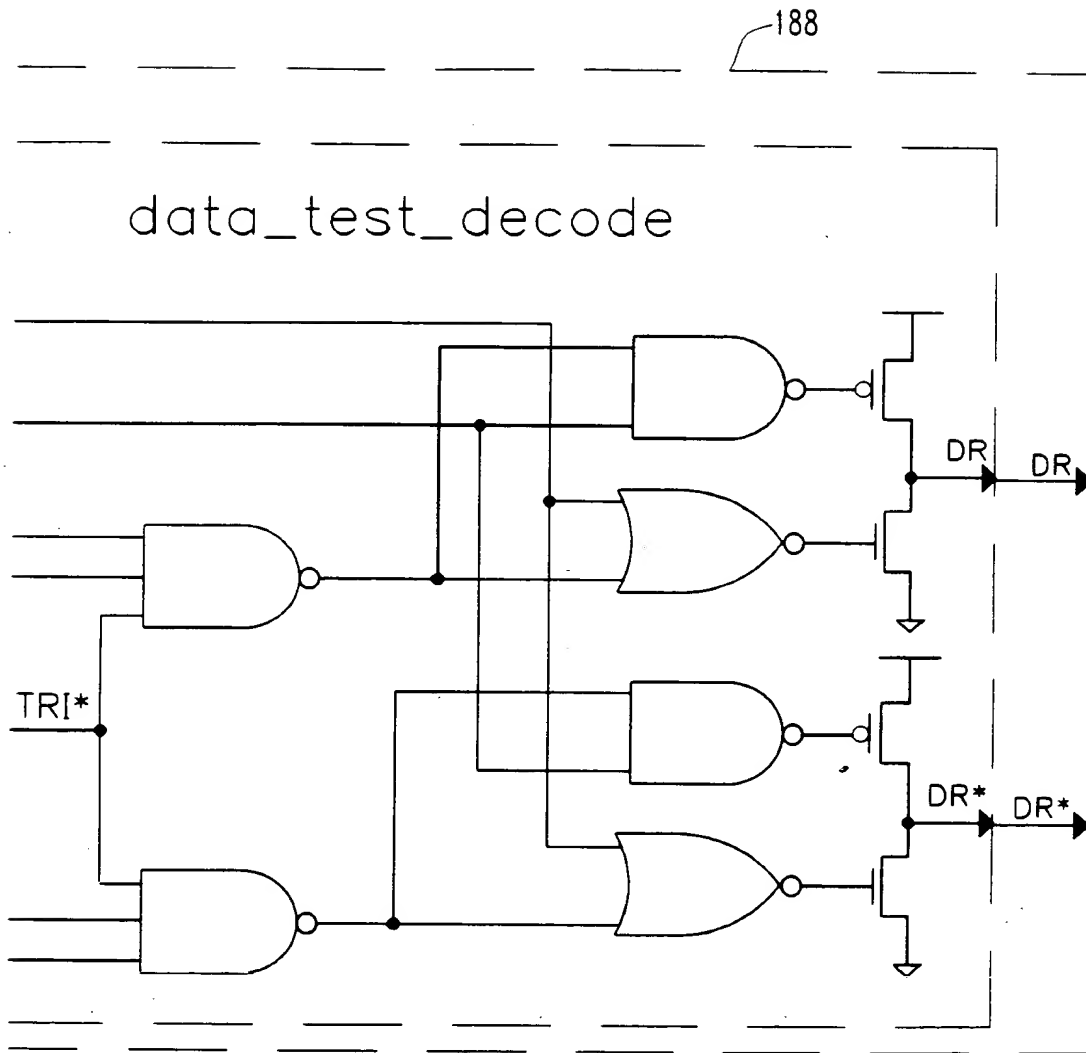
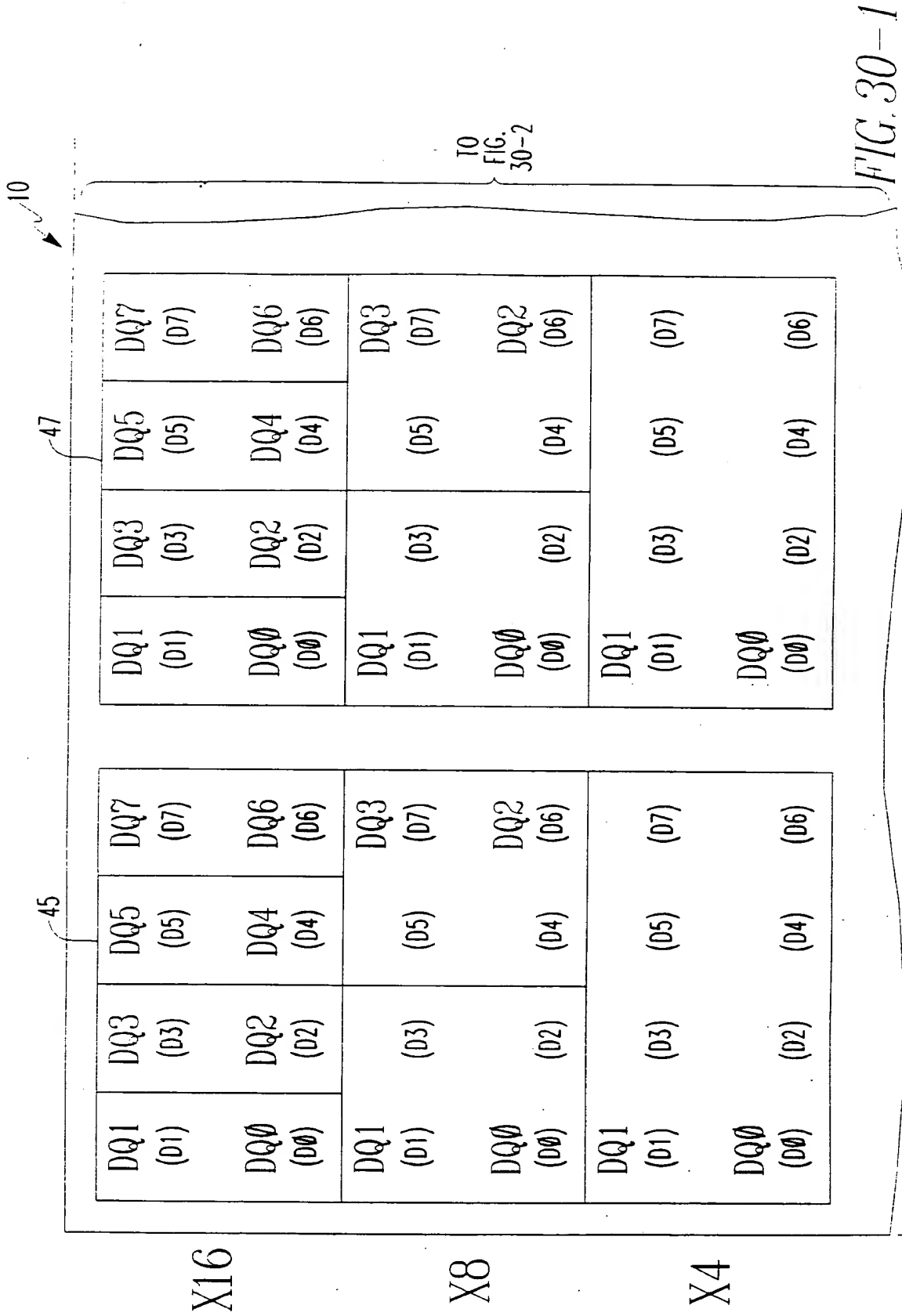


FIG. 29-2



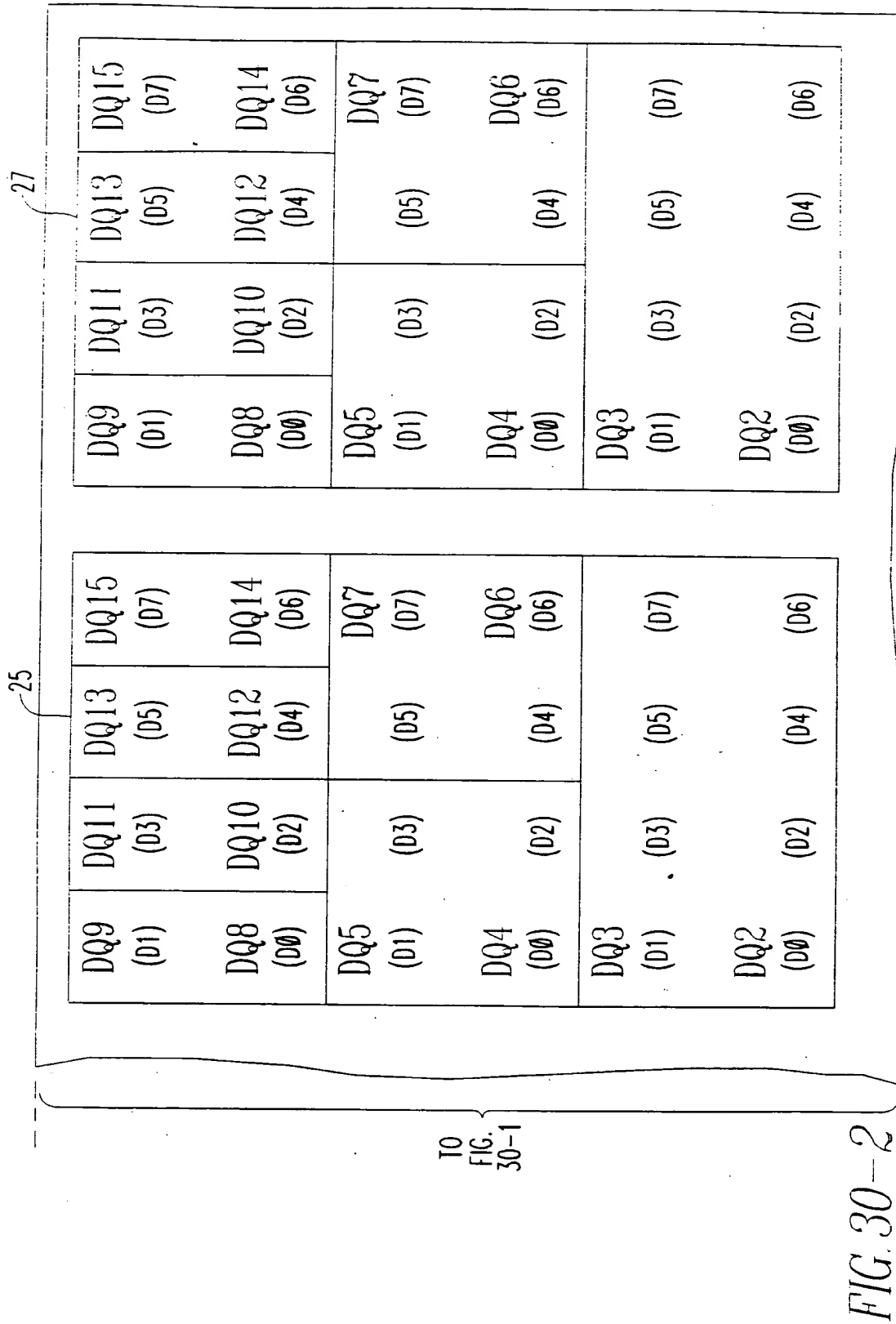


FIG. 30-1

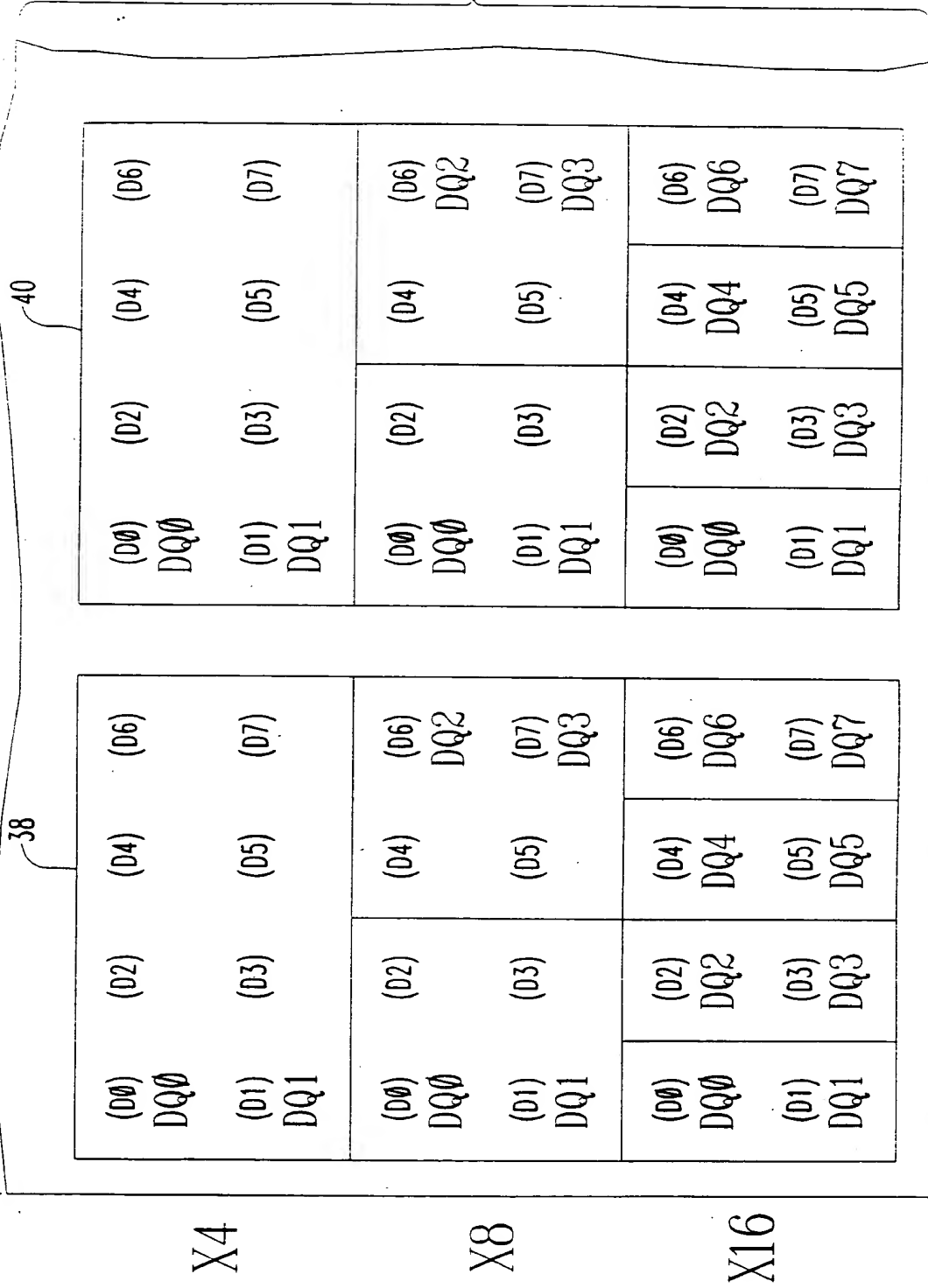
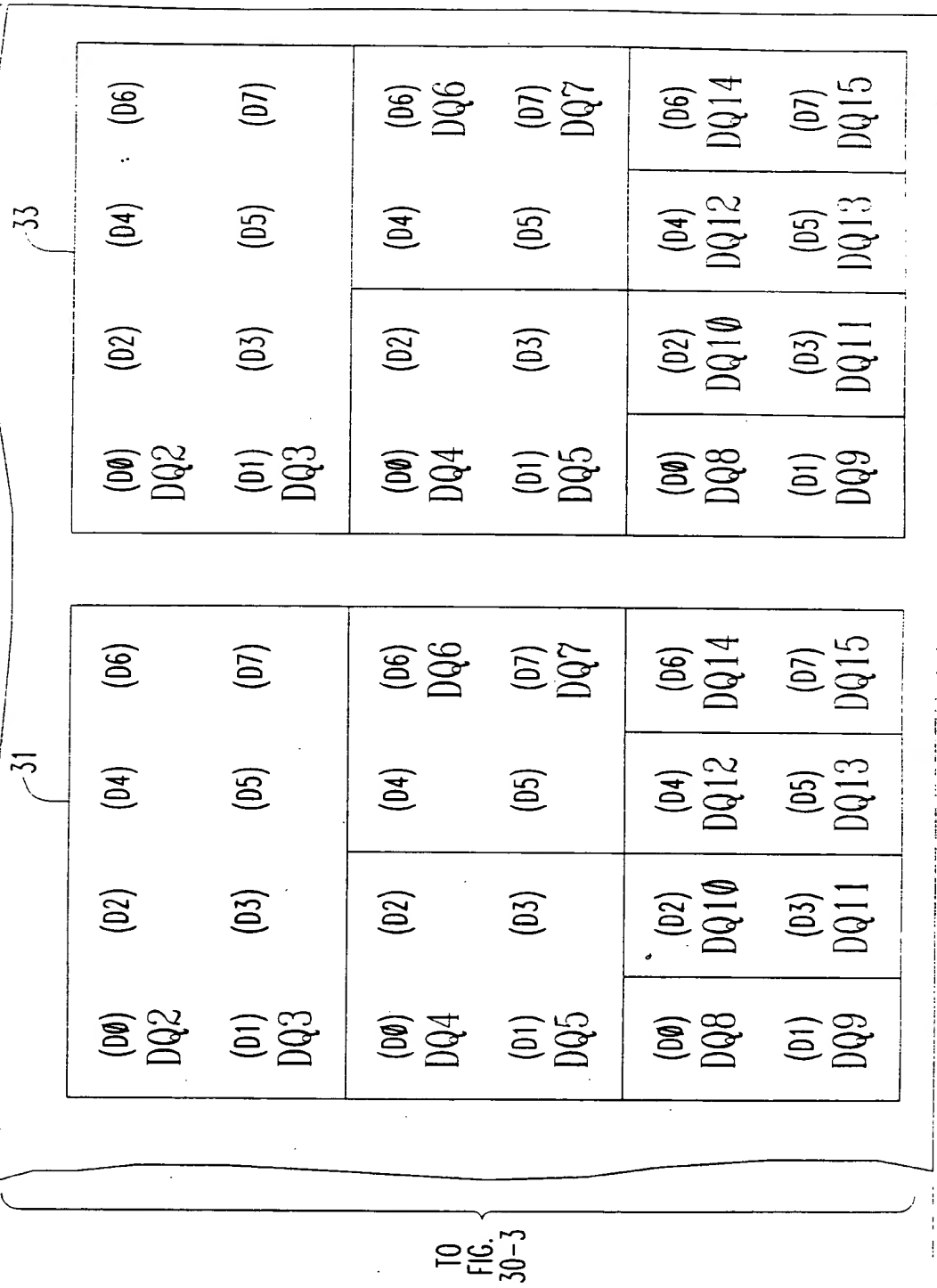


FIG. 30-3

TO FIG. 30-2



TO FIG. 31A1
↓

N/C	N/C	VSSQ	VSS	DVC2_L Probe	NCSV	VCC1 Probe					
N/C	N/C	VSSQ	VSS		NCSV						
VSSQ	DQ4	VSSQ	VSS		NCSV						
12	13	14	15	16	17	18	19	20	21	22	23
DQ3	VCCQ	VCCX	WE								
N/C	VCCQ	VCCX	WE								
N/C	VCCQ	VCCX	WE								

FIG. 31A2

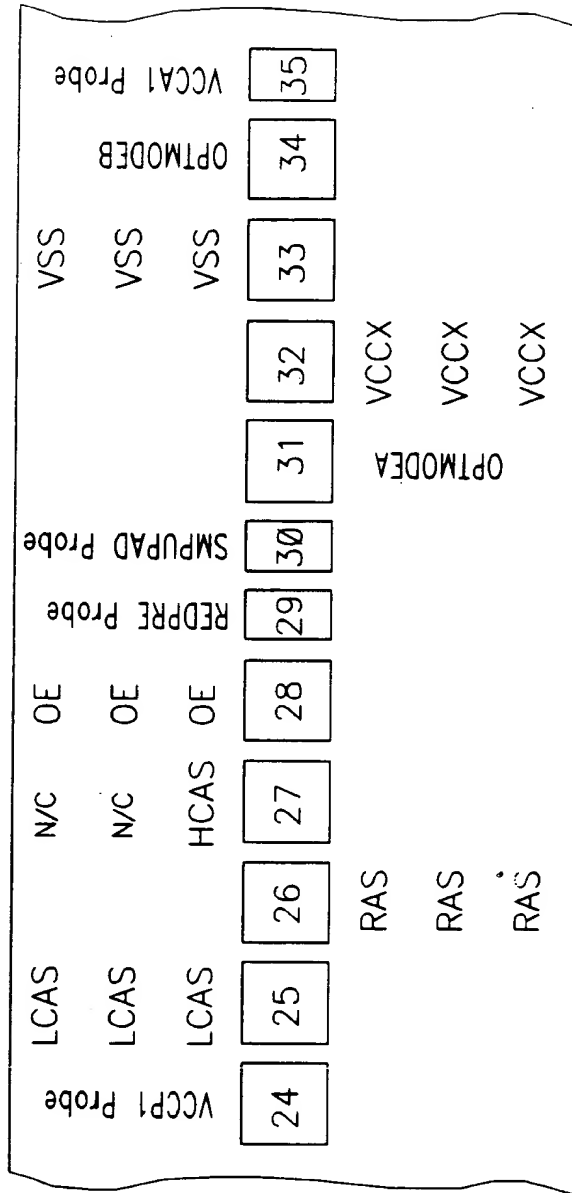


FIG. 31B1

FIG. 31B2

FIG. 31B1

TO FIG. 31B1

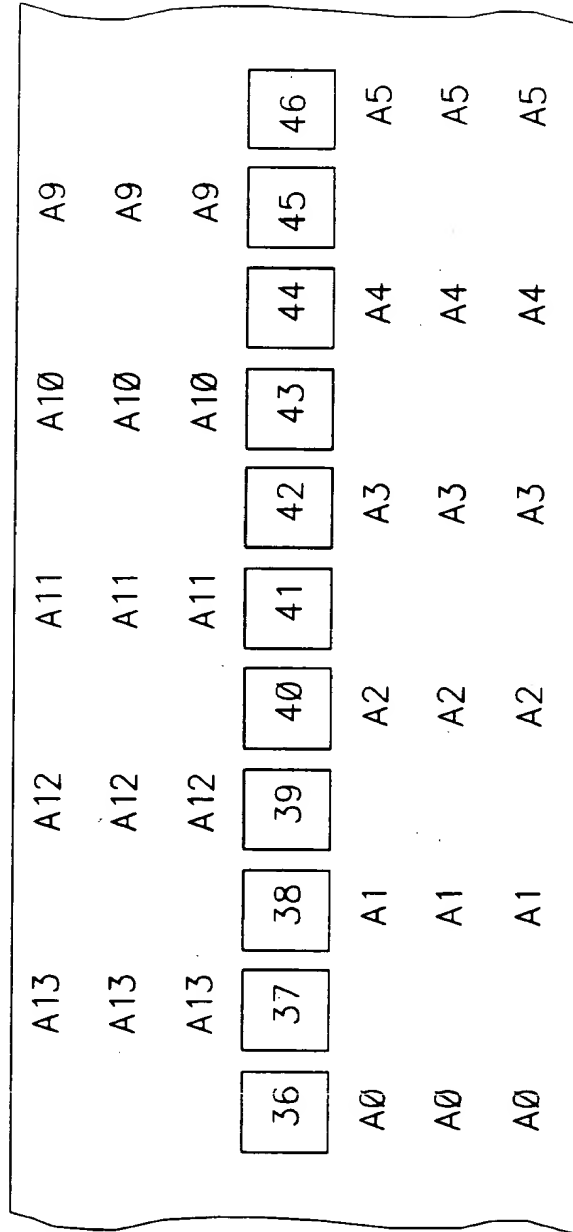


FIG. 31B2

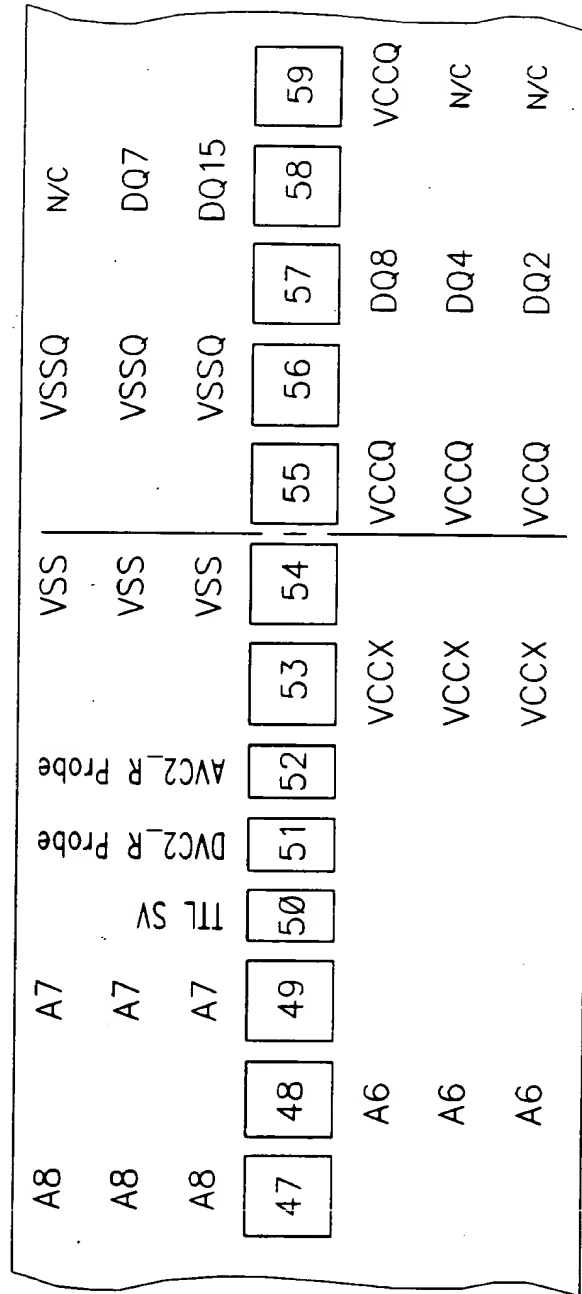


FIG. 31C1

TO FIG. 31C2

TO FIG. 31C1

N/C	N/C	N/C	N/C	VSSQ	N/C	VSSQ	VBB
N/C	DQ6	N/C	N/C	VSSQ	N/C	VSSQ	VBB
VSSQ	DQ14	DQ13	DQ12	VSSQ	DQ12	VSSQ	VBB
60	61	62	63	64	65	66	67
DQ9	DQ10	VCCQ	DQ11	VCCQ	VCCQ	VCCQ	VBB
DQ5	N/C	VCCQ	N/C	VCCQ	VCCQ	VCCQ	VBB
DQ3*	N/C	VCCQ	N/C	VCCQ	VCCQ	VCCQ	VBB

FIG. 31C2

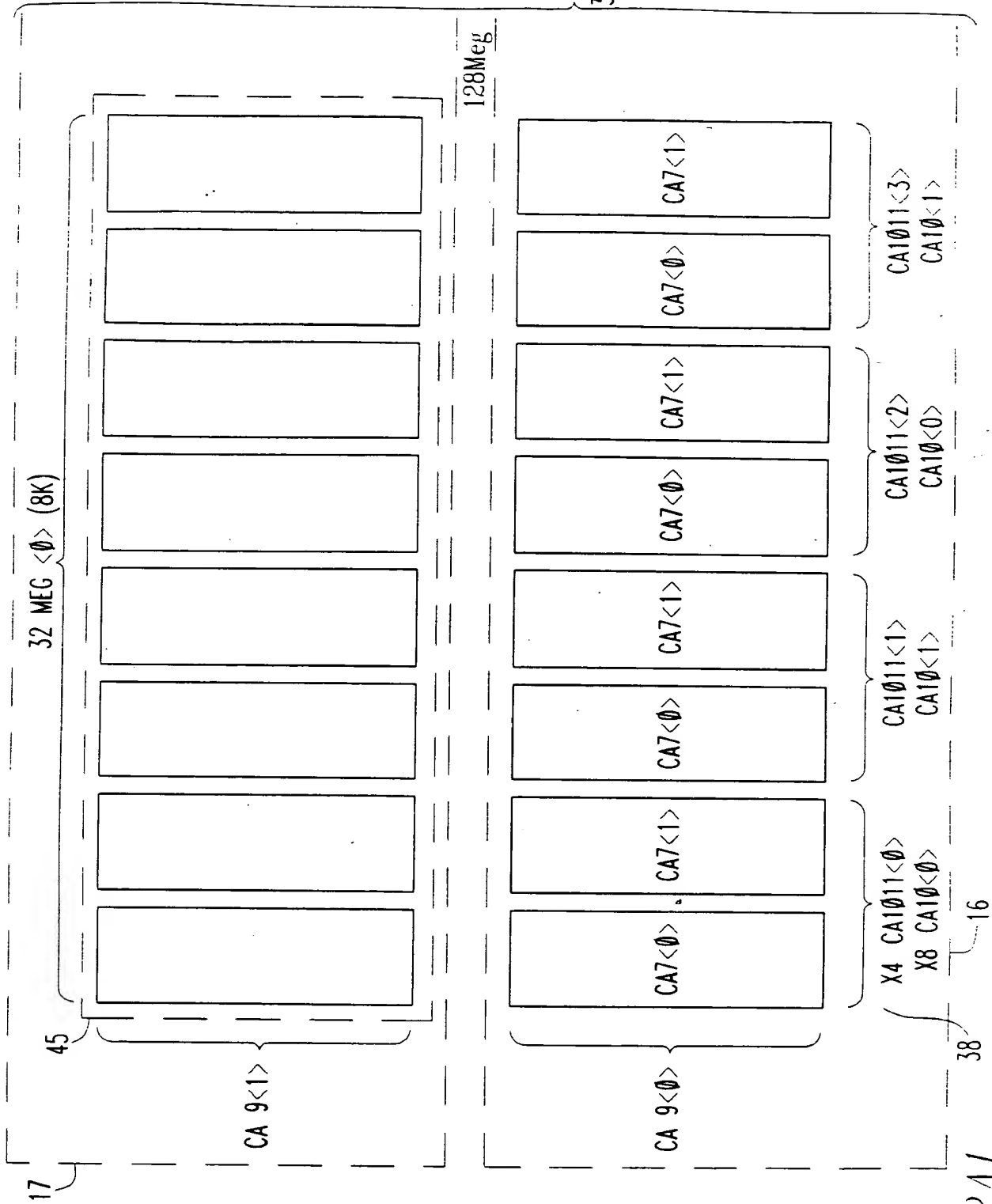
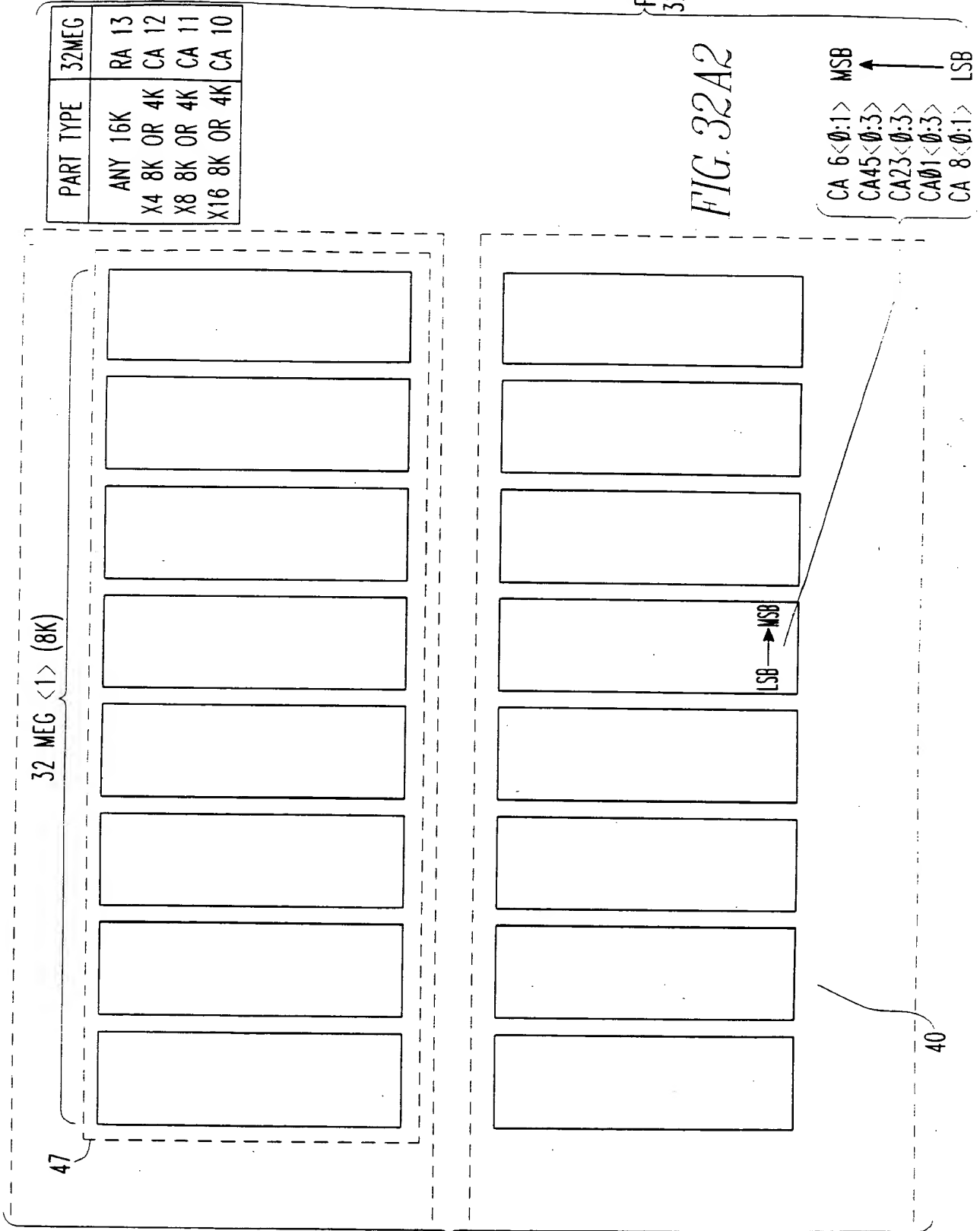


FIG. 32A1

FROM
FIG.
32A1



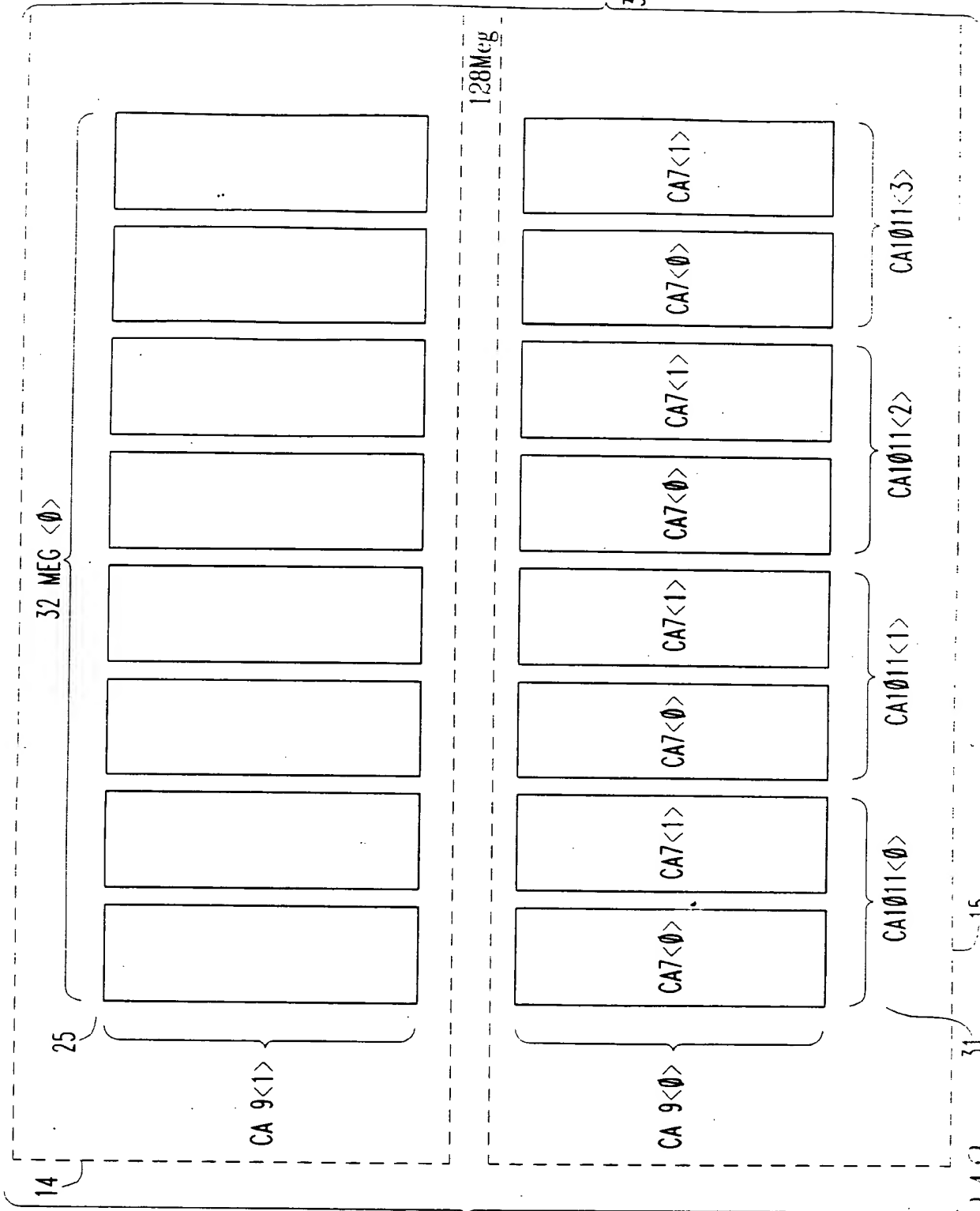


FIG. 32A3

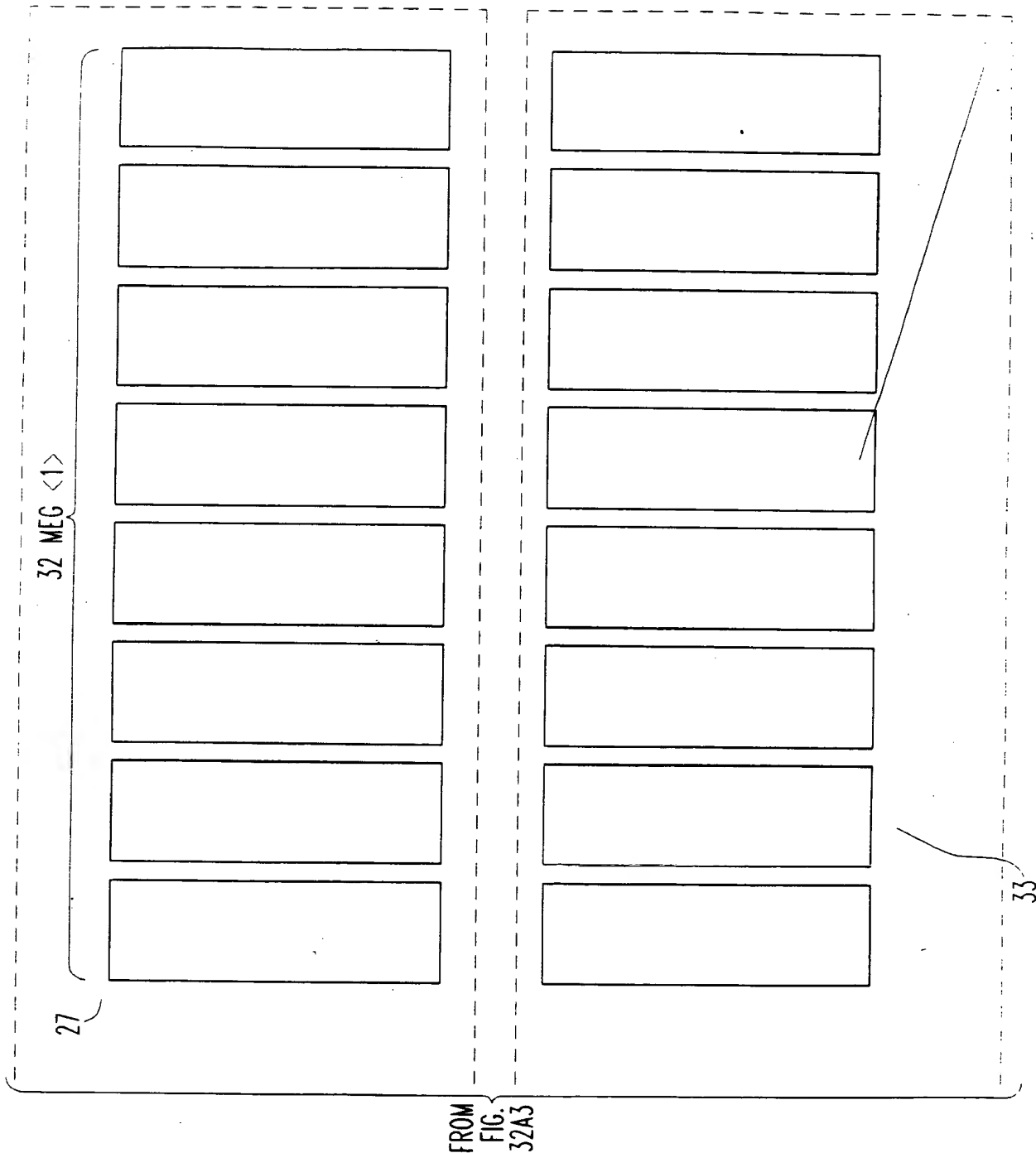


FIG. 32A4

CA 6<0:1> MSB
CA45<0:3>
CA23<0:3>
CA01<0:3>
CA 8<0:1> LSB

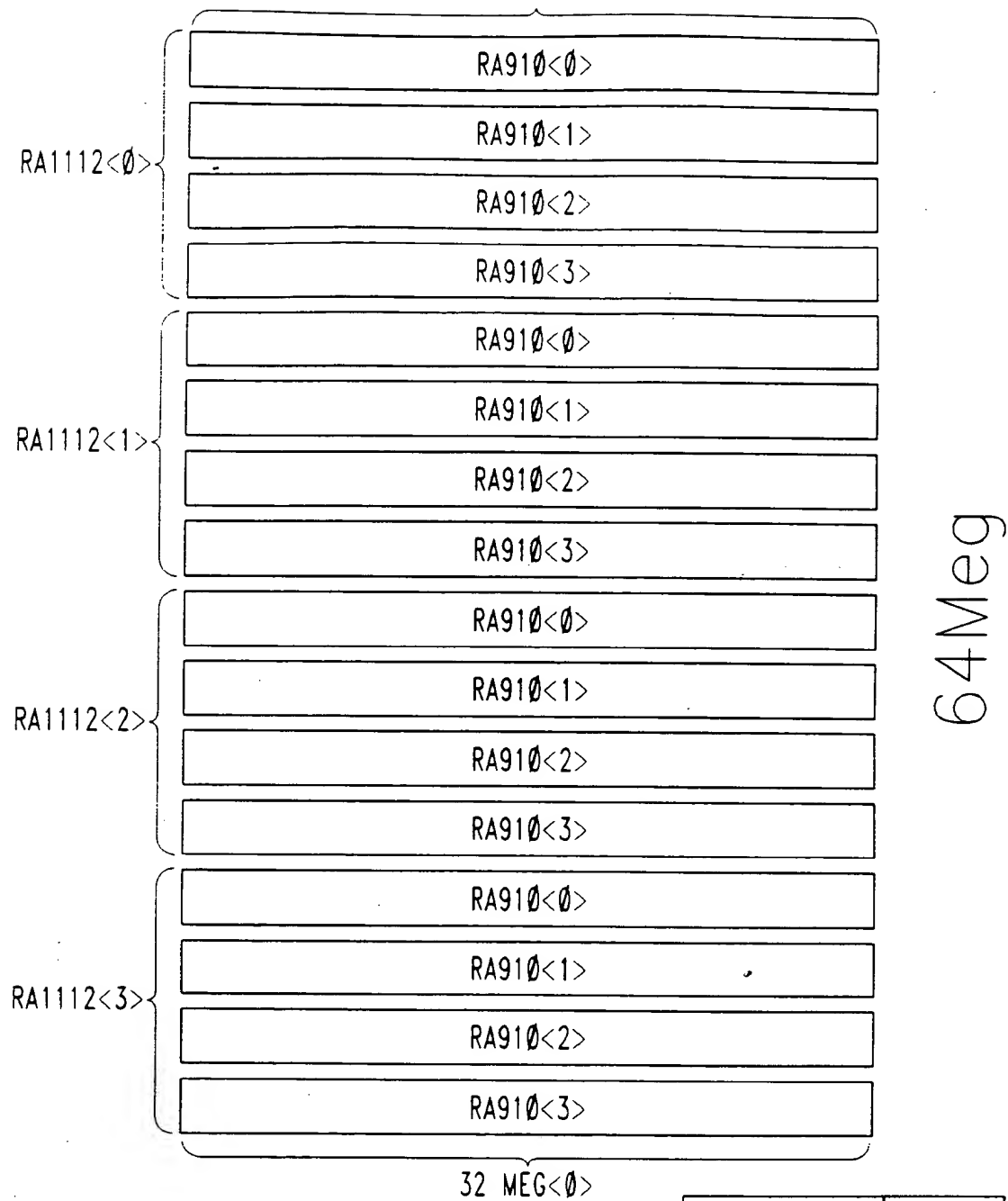


FIG. 32B1

PART TYPE	32MEG
ANY 16K	RA_13
X4 8K OR 4K	CA_12
X8 8K OR 4K	CA_11
X16 8K OR 4K	CA_10

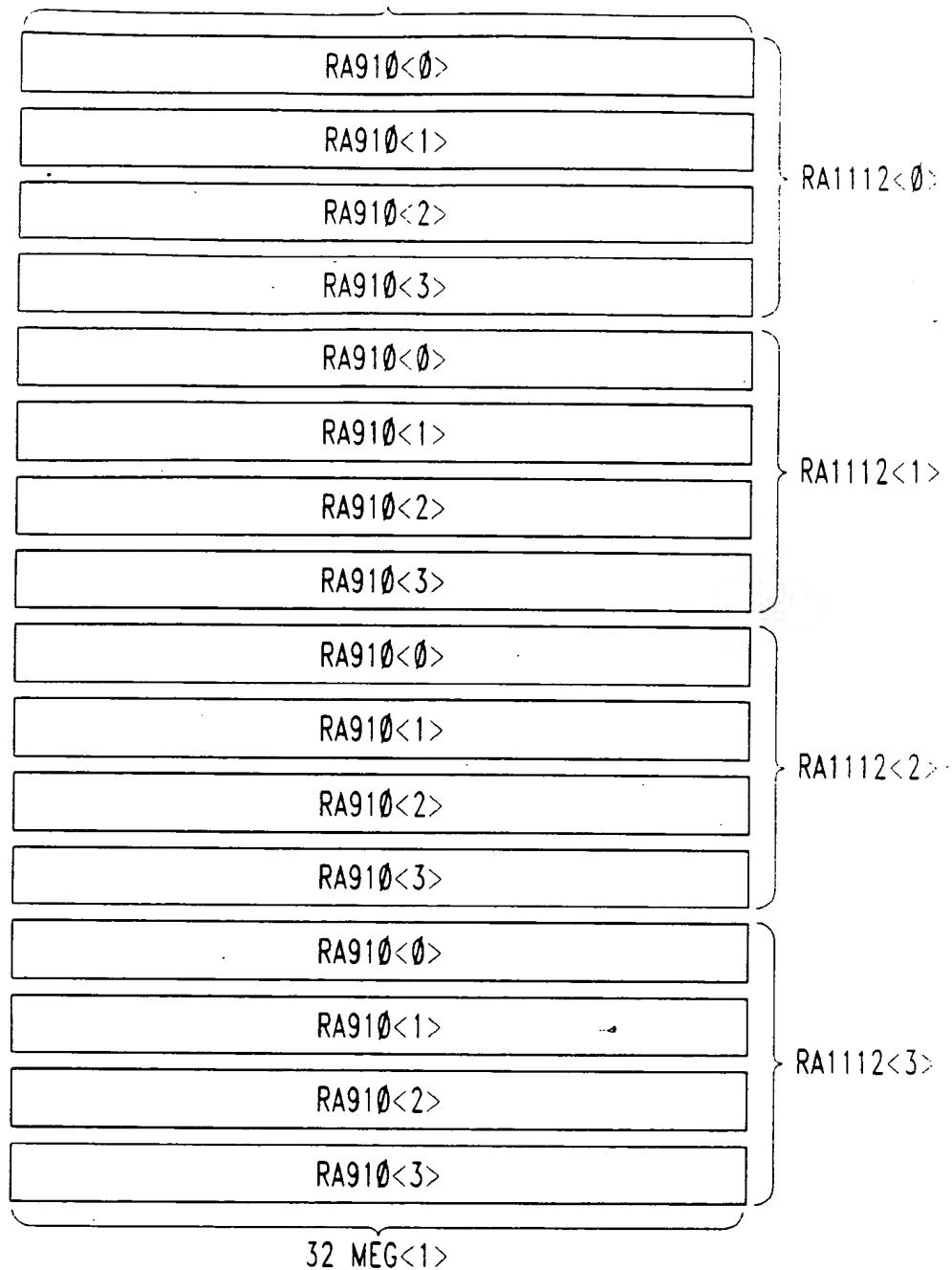
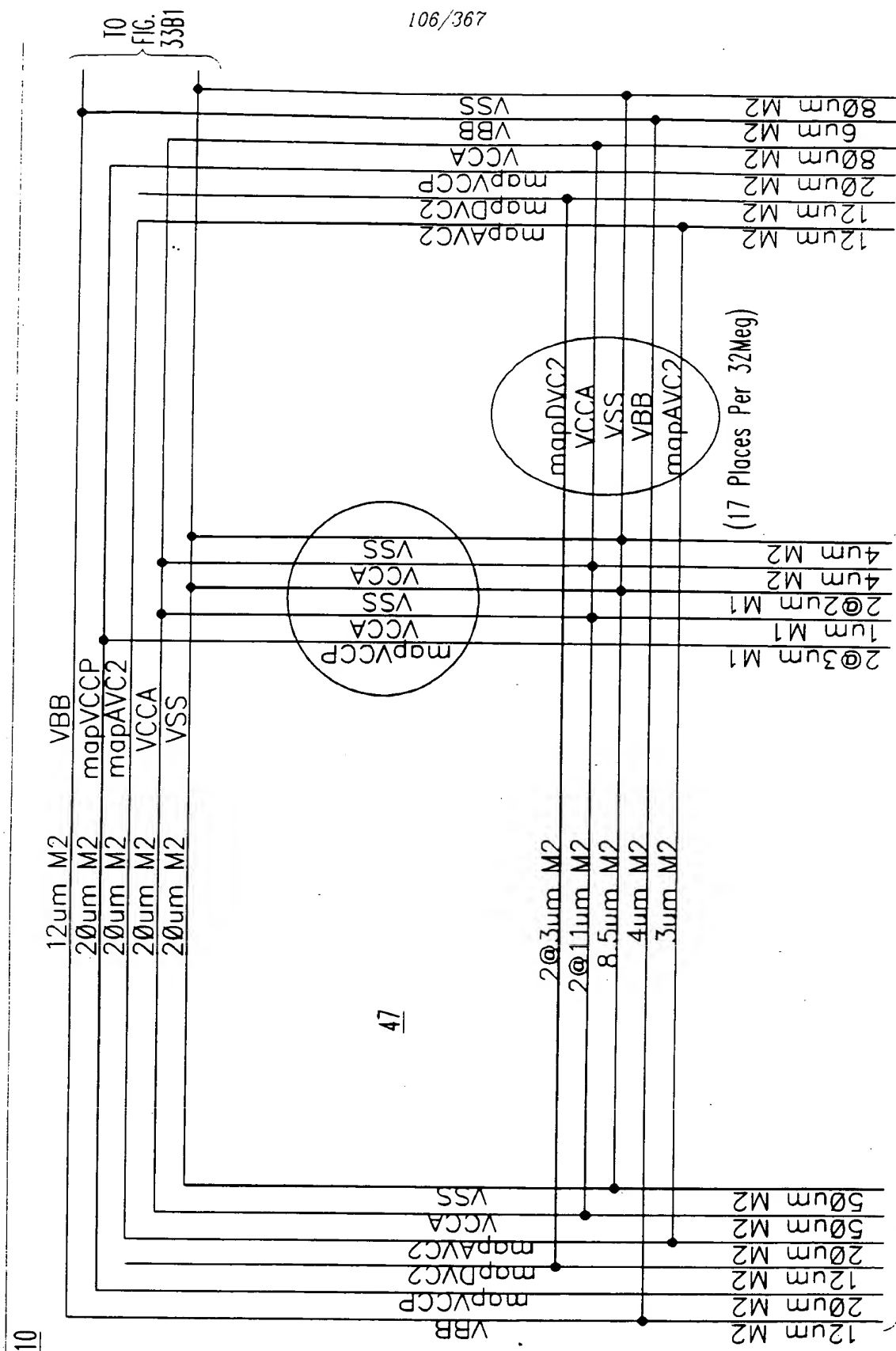


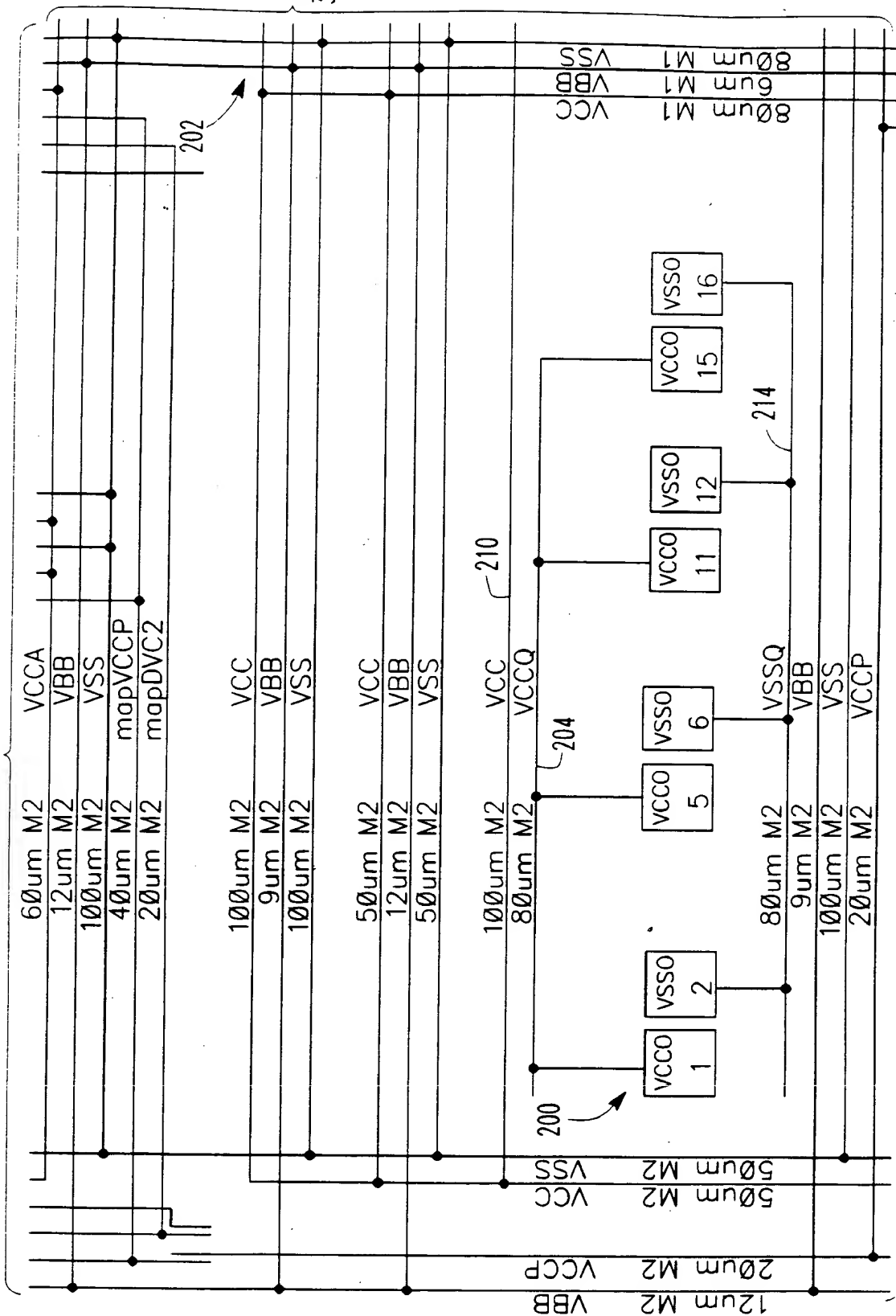
FIG. 32B2



TO FIG. 33A2

FIG. 331

FROM FIG. 33A1



TO FIG. 33B3

107/367

TO FIG. 33A3

FIG. 33A2

FROM FIG. 33A2

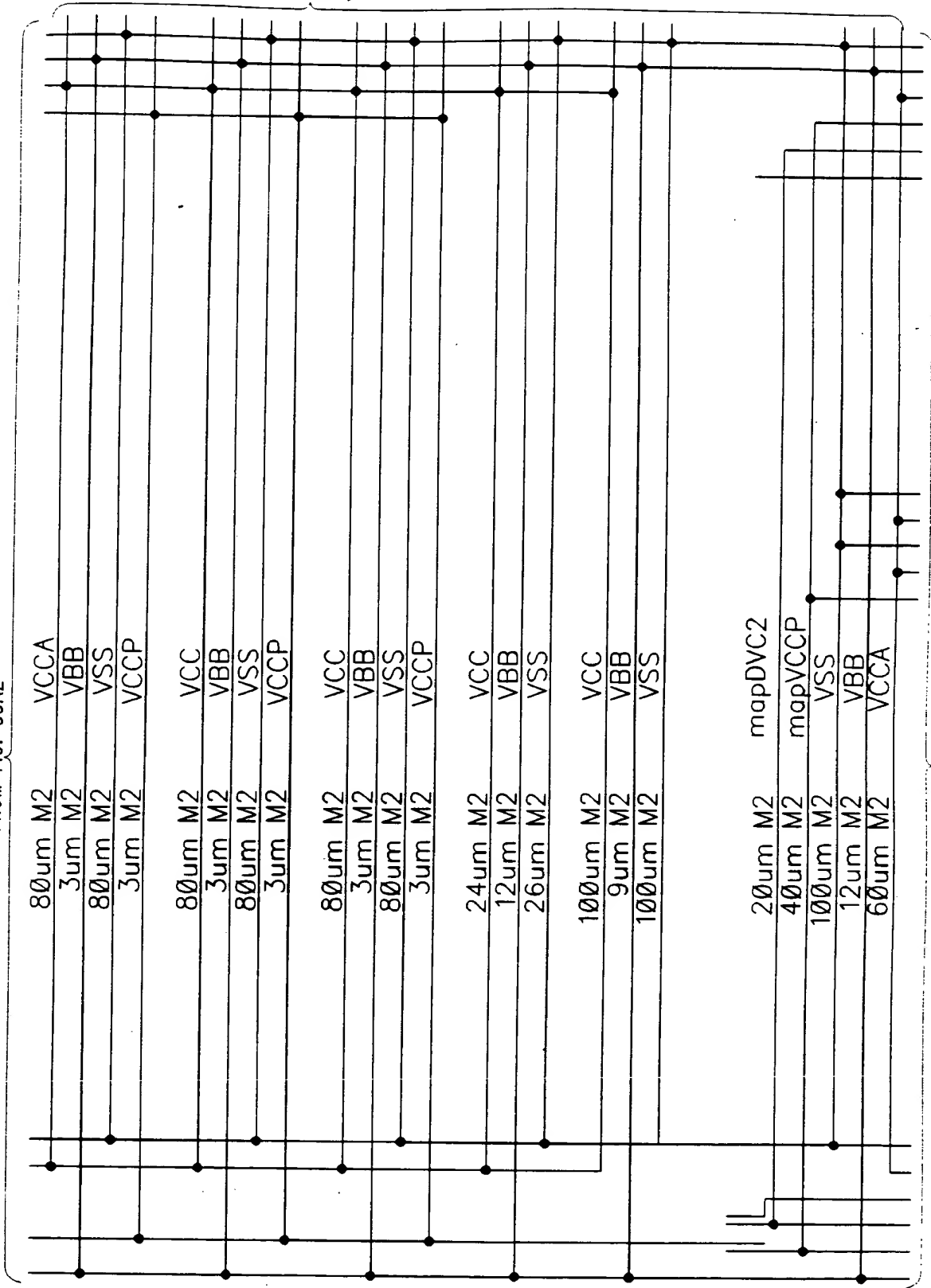
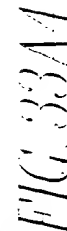


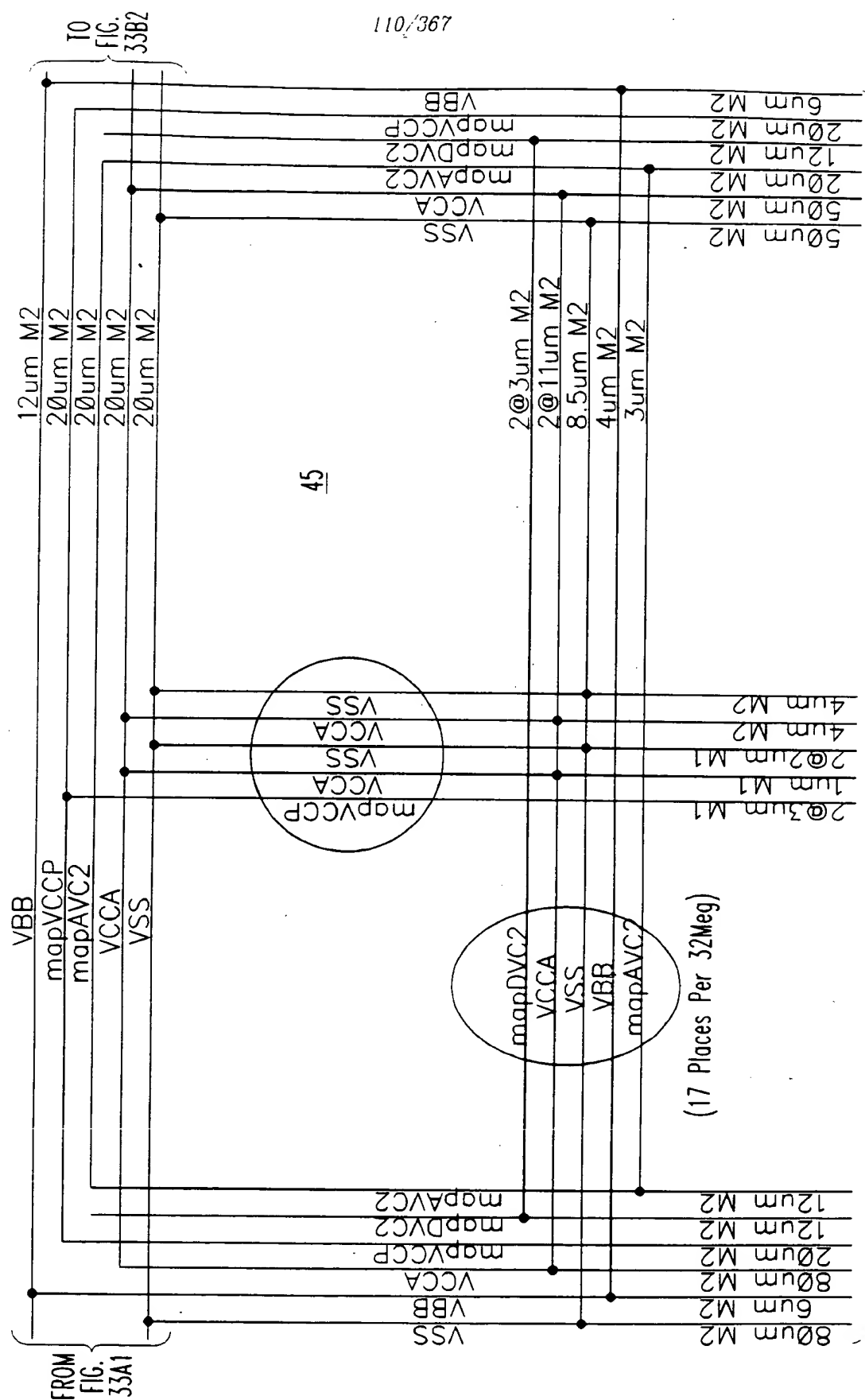
FIG. 33B5

108/367

TO FIG. 33A4

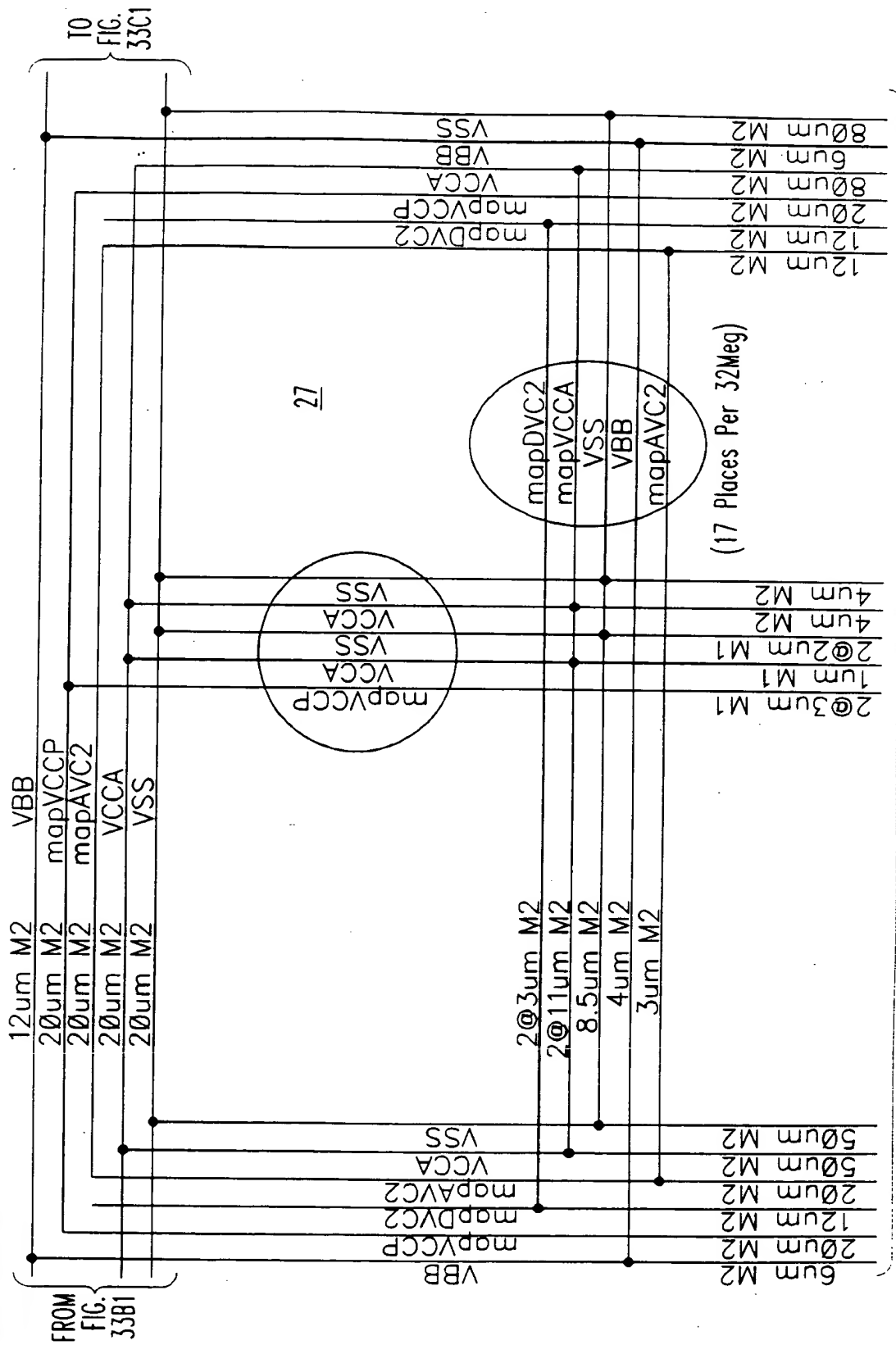
FIG. 33A3





TO FIG. 33B3

176:3331



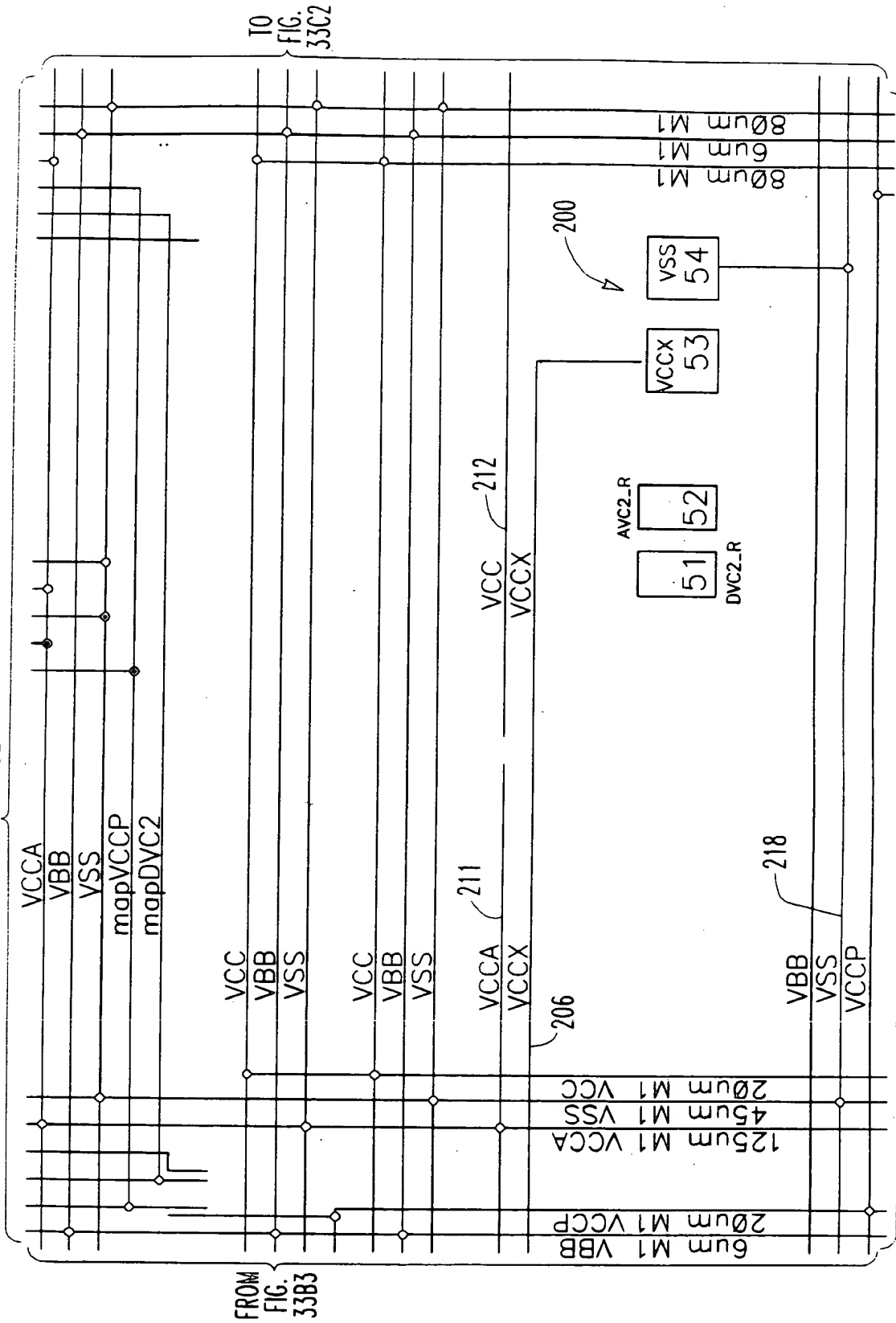
TO FIG. 33B4

FIG. 33B2



1763333

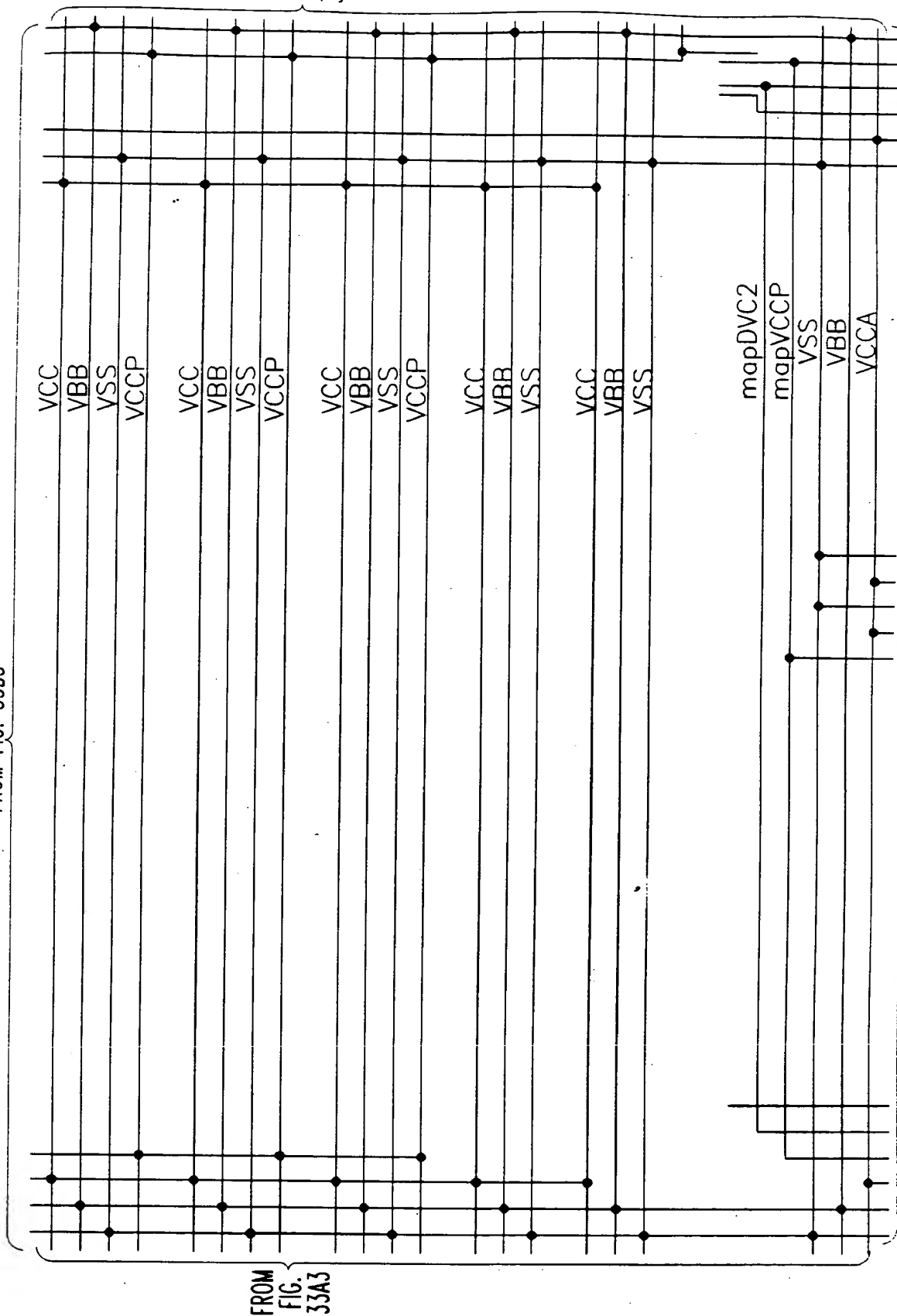
FROM FIG. 33B2



TO FIG. 33B6

FIG. 33B3

FROM FIG. 33B3



FROM
FIG.
33A3

TO
FIG.
33B6

114/367

TO FIG. 33B7

FIG. 33B5

FROM
FIG.
33B5

The diagram shows a sequence of signals over time. The signals are labeled as follows:

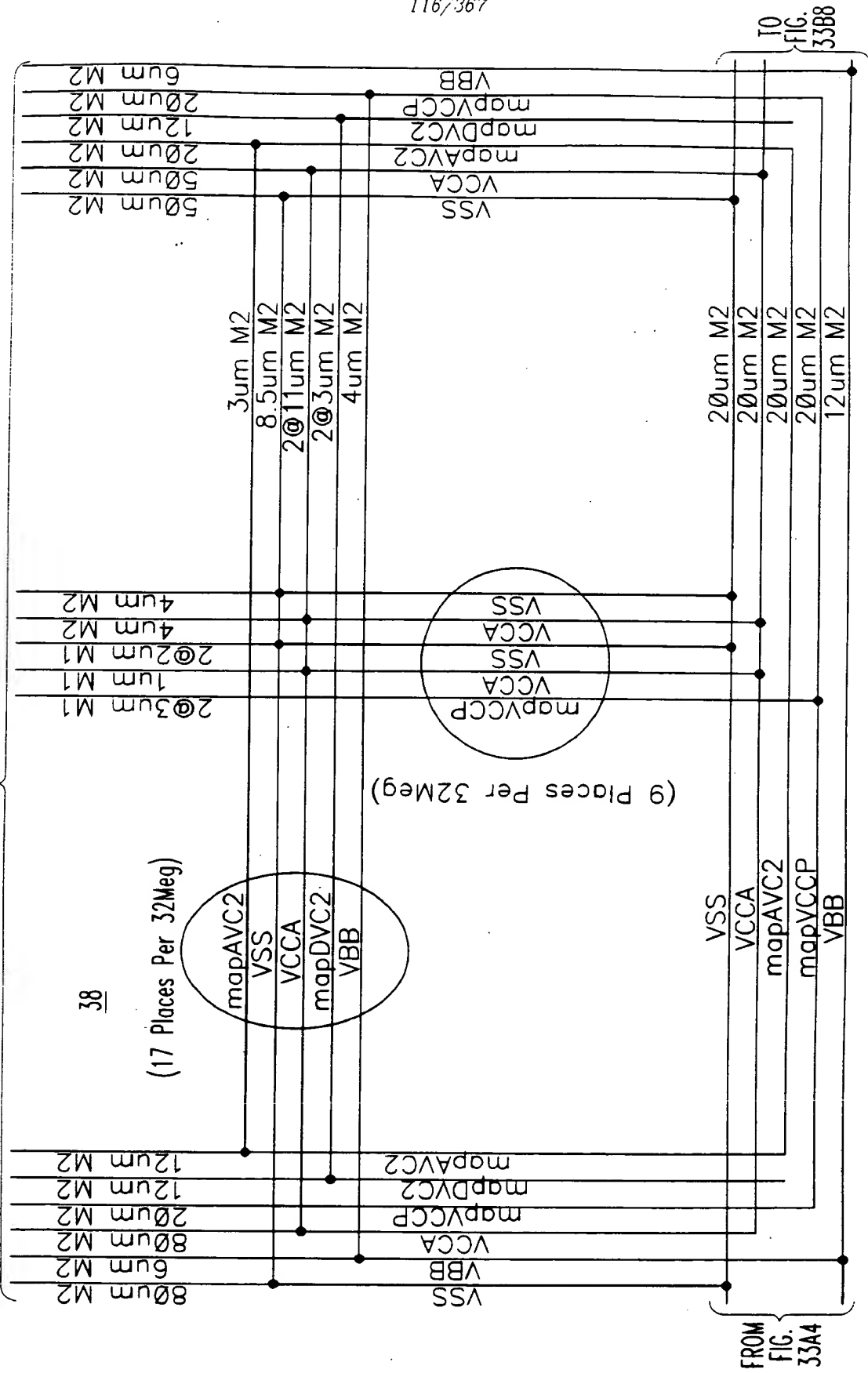
- VCC
- VBB
- VSS
- VCCP
- mapDVC2
- mapVCCP
- VSS
- VBB
- VCCA

The signals are plotted on a grid. The horizontal axis represents time, and the vertical axis represents the signal level. The signals are shown as follows:

- VCC: A series of pulses.
- VBB: A series of pulses.
- VSS: A series of pulses.
- VCCP: A series of pulses.
- mapDVC2: A series of pulses.
- mapVCCP: A series of pulses.
- VSS: A series of pulses.
- VBB: A series of pulses.
- VCCA: A series of pulses.

HC 336

FROM FIG. 33B5



116/367

FIG. 33B7

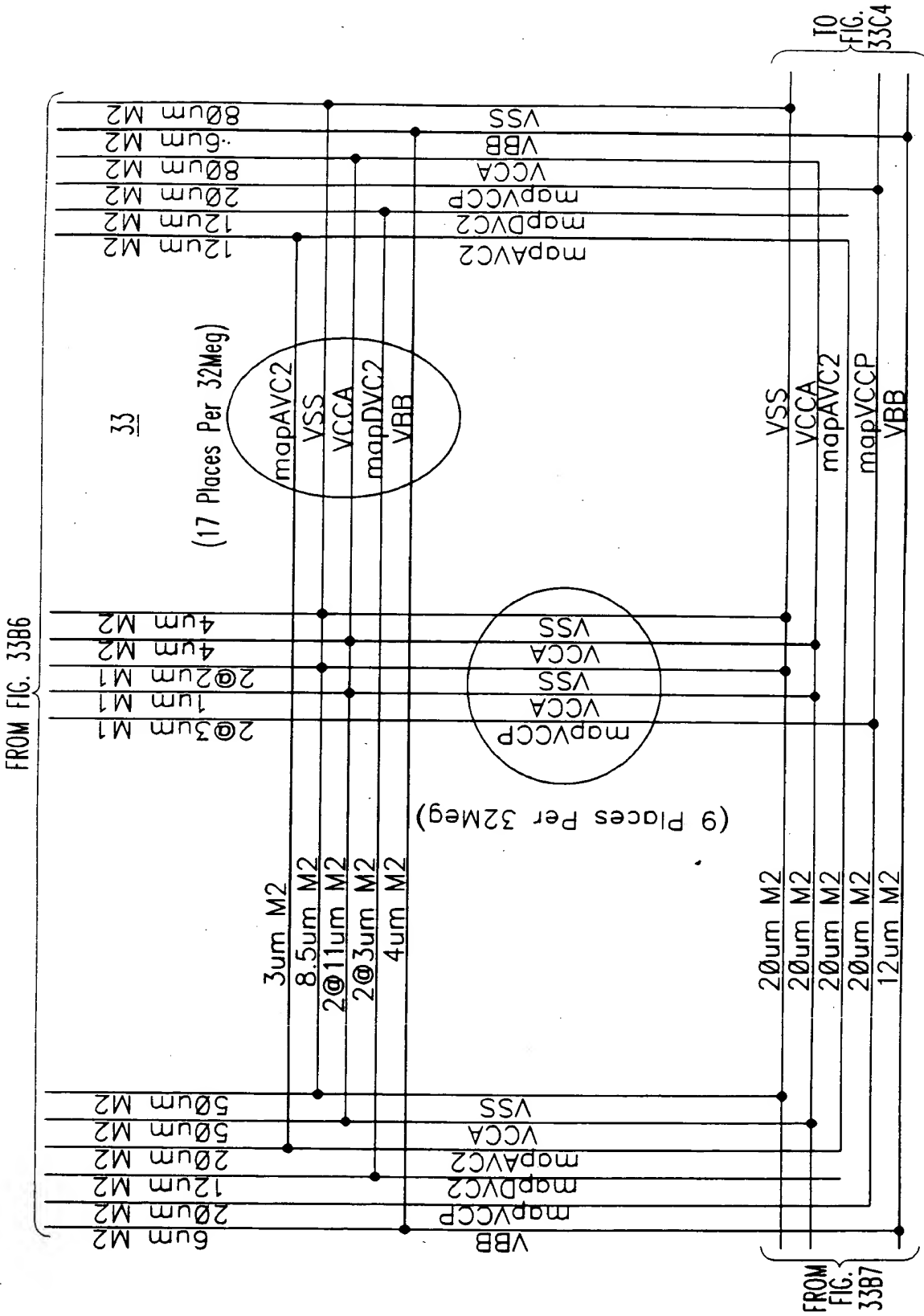
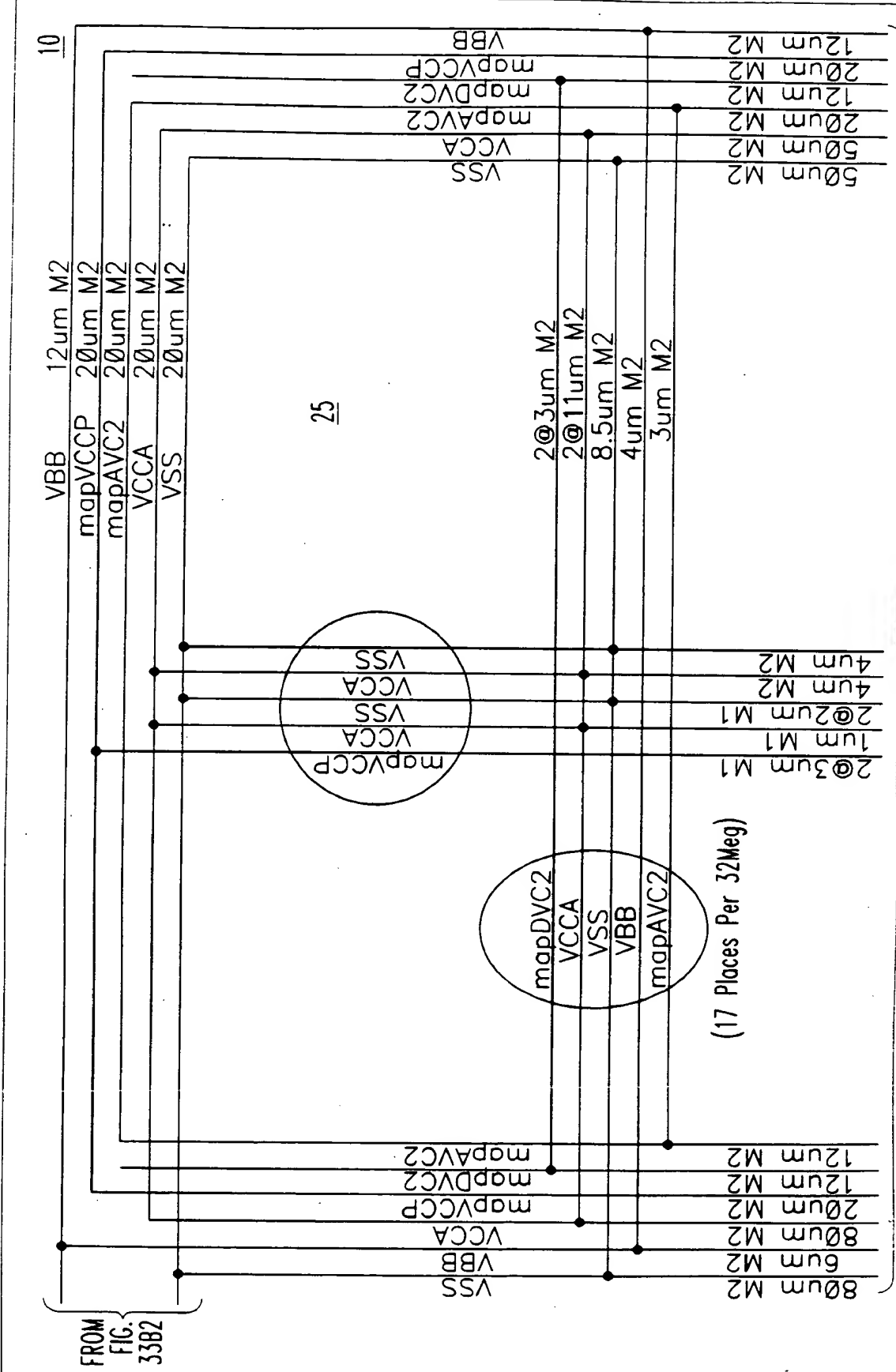


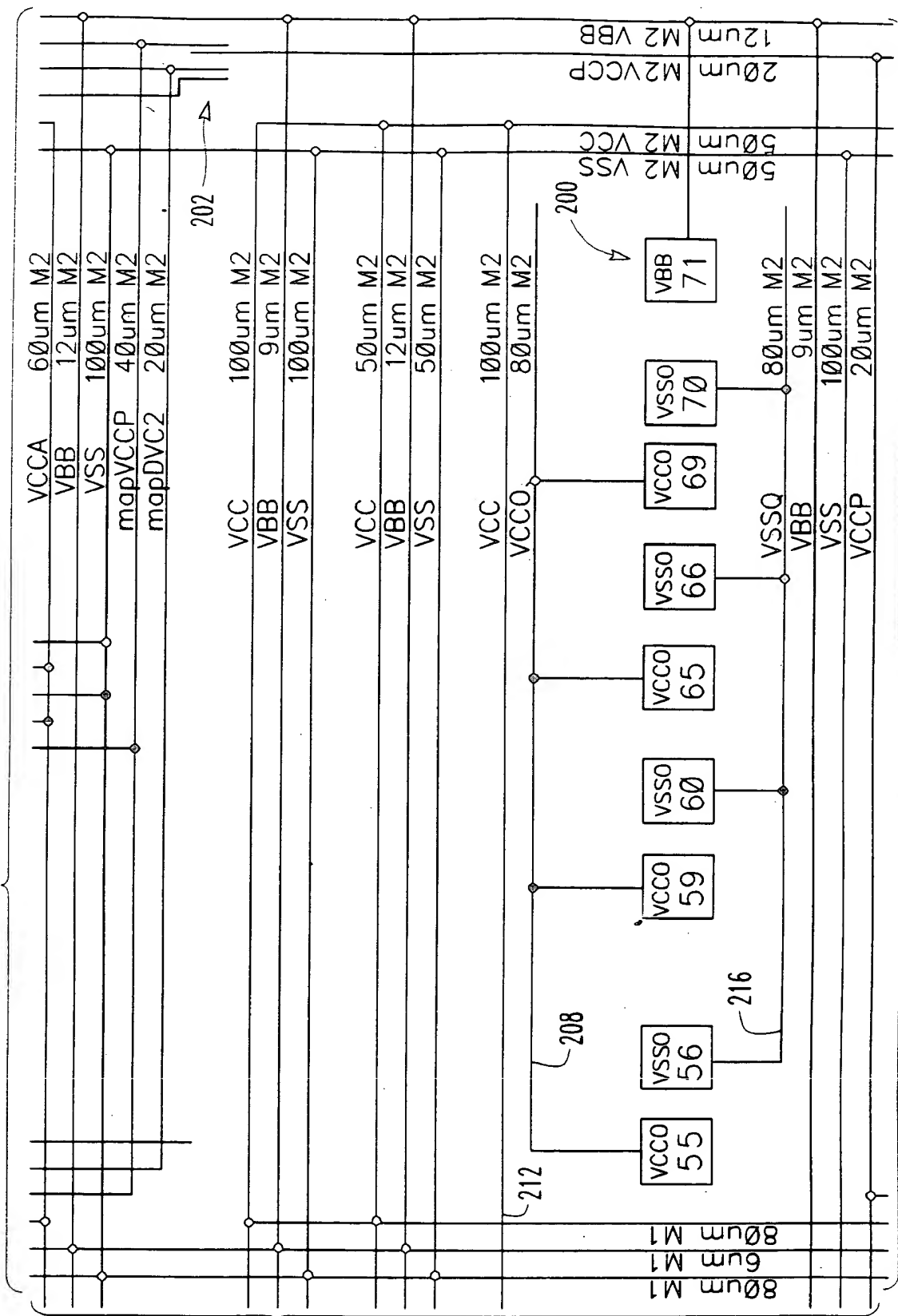
FIG. 33B8



TO FIG. 33C2

FIG. 33C1

FROM
FIG.
33B3



TO FIG. 33C3

FIG 33C2

FROM FIG. 33C2

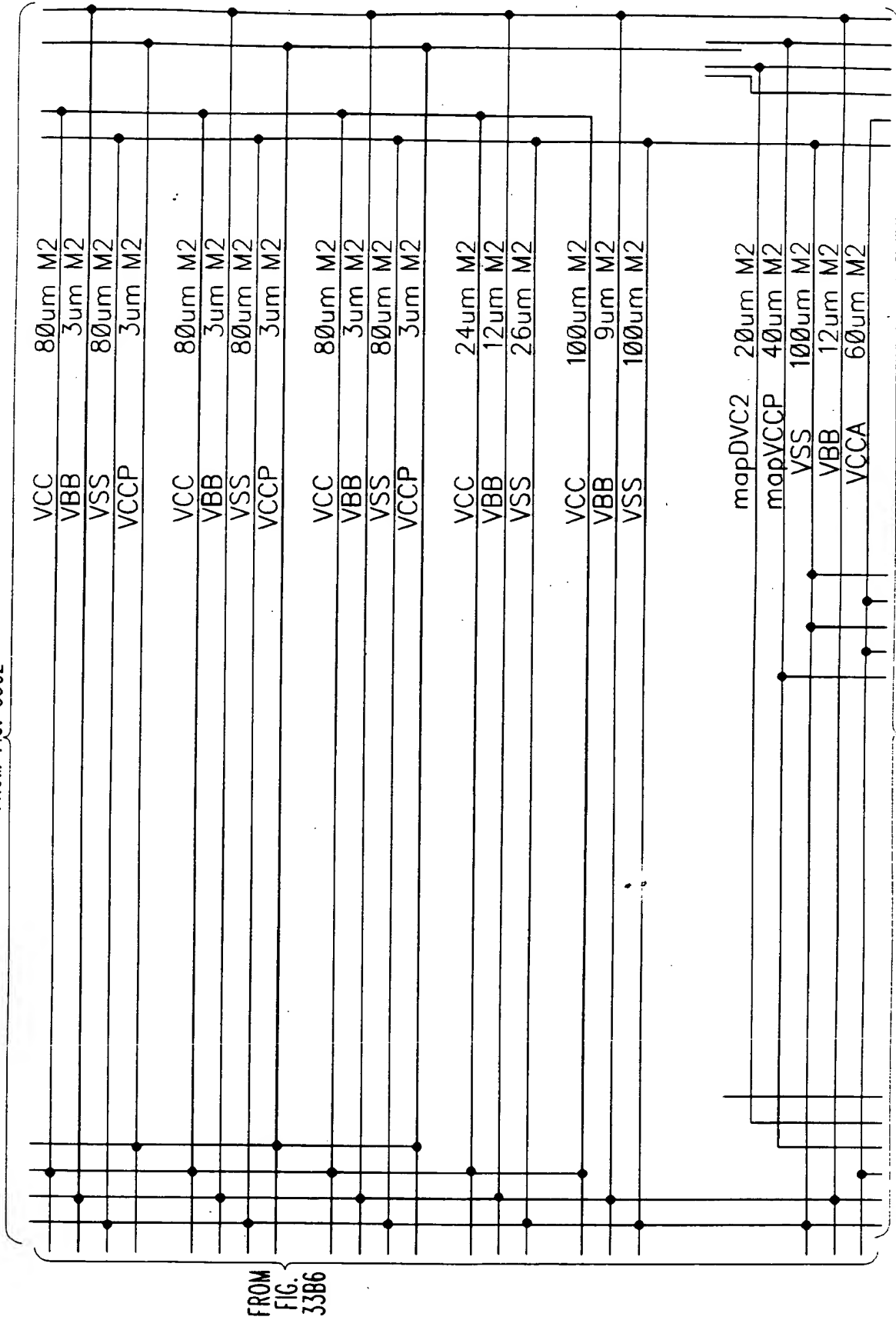


FIG. 33C3

FROM FIG. 3388

(17 Places Per 32Meg)

mapAVC2
VSS
VCCA
mapDVC2
VBB

(9 Places Per 32Meg)

mapVCCP
VCCA
VSS
VCCA
VSS

VSS
VBB
VCCA
mapVCCP
mapDVC2
mapAVC2

80um M2
60um M2
80um M2
20um M2
12um M2
12um M2

2@3um M1
1um M1
2@2um M1
4um M2
4um M2

mapVCCP
VCCA
VSS
VCCA
VSS

20um M2
20um M2
20um M2
20um M2
12um M2

VSS
VCCA
mapAVC2
mapVCCP
VBB

3um M2
8.5um M2
2@11um M2
2@3um M2
4um M2

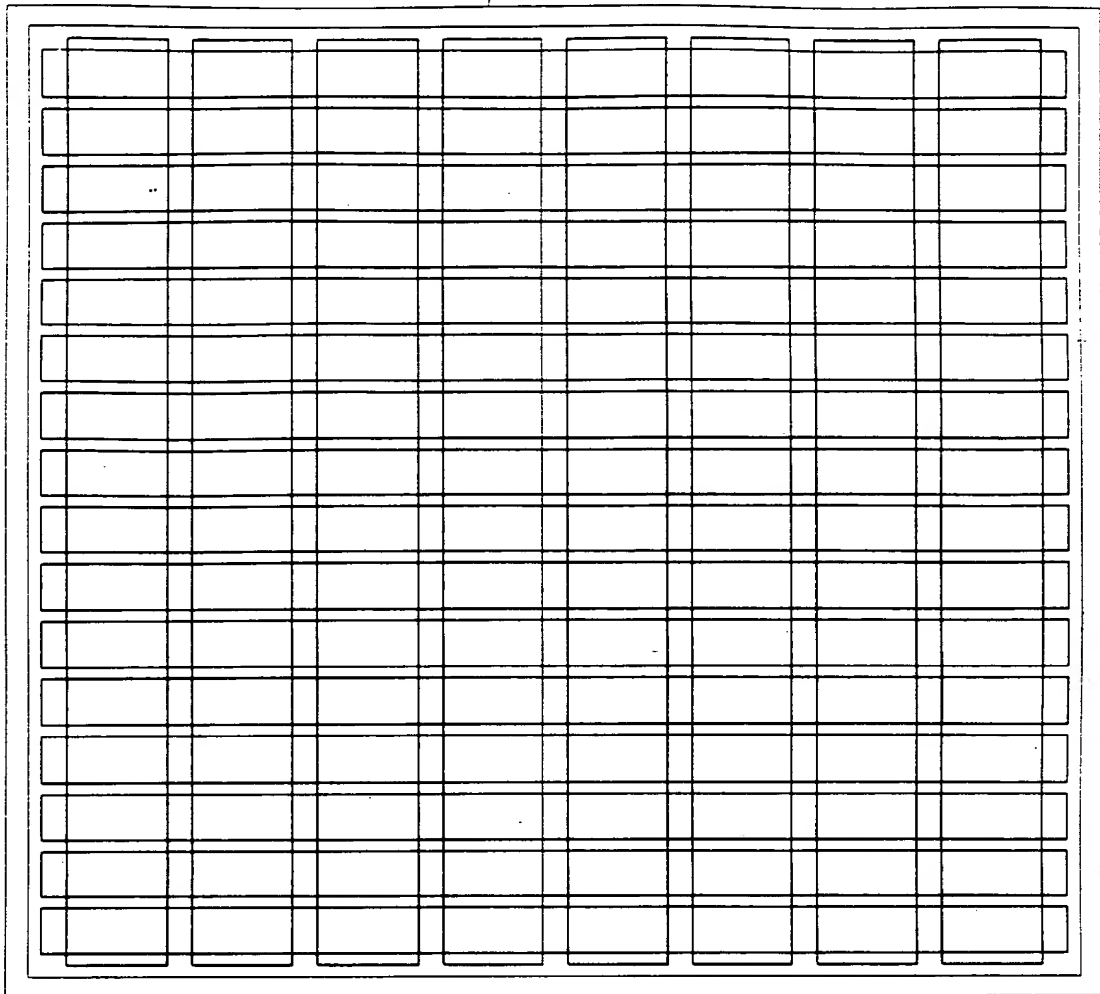
VSS
VCCA
mapAVC2
mapDVC2
mapVCCP
VBB

50um M2
50um M2
20um M2
12um M2
20um M2
12um M2

FIG. 33C4

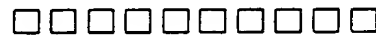
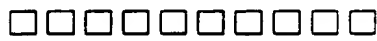
10

45



200

DVC2
GENERATOR 500



VCCP
PUMP CIRCUITS 402

DVC2
GENERATOR 501

TO
FIG.
33D3

TO FIG. 33D2

FIG. 33D1

TO FIG. 33D1

47

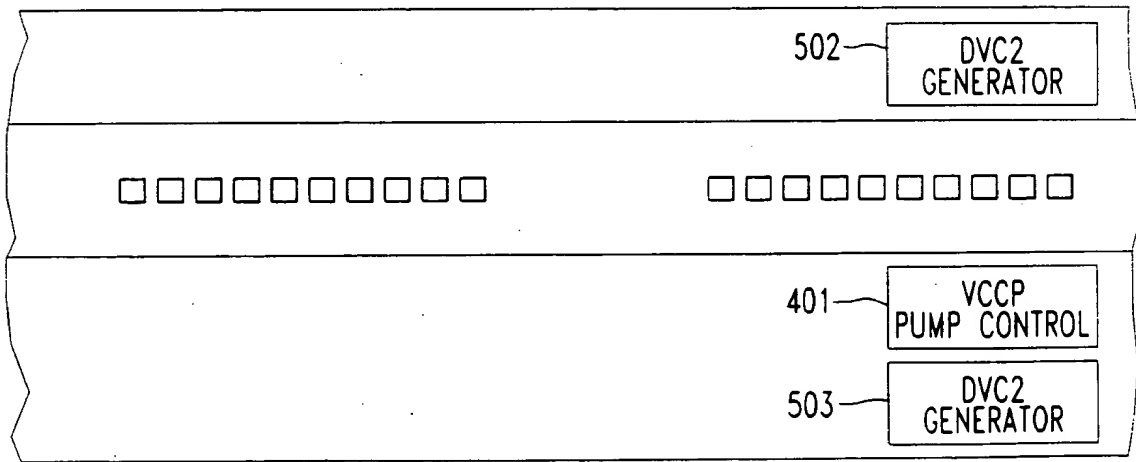
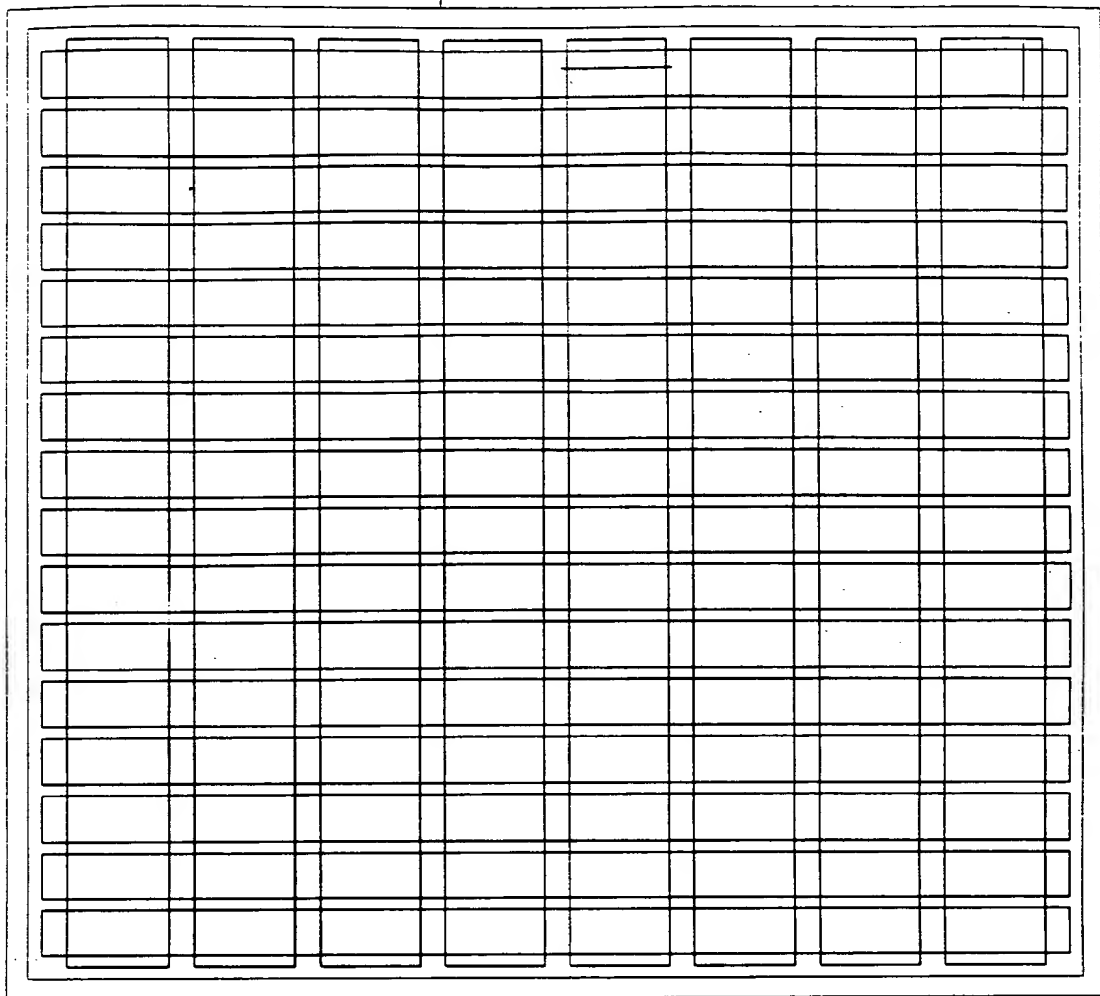


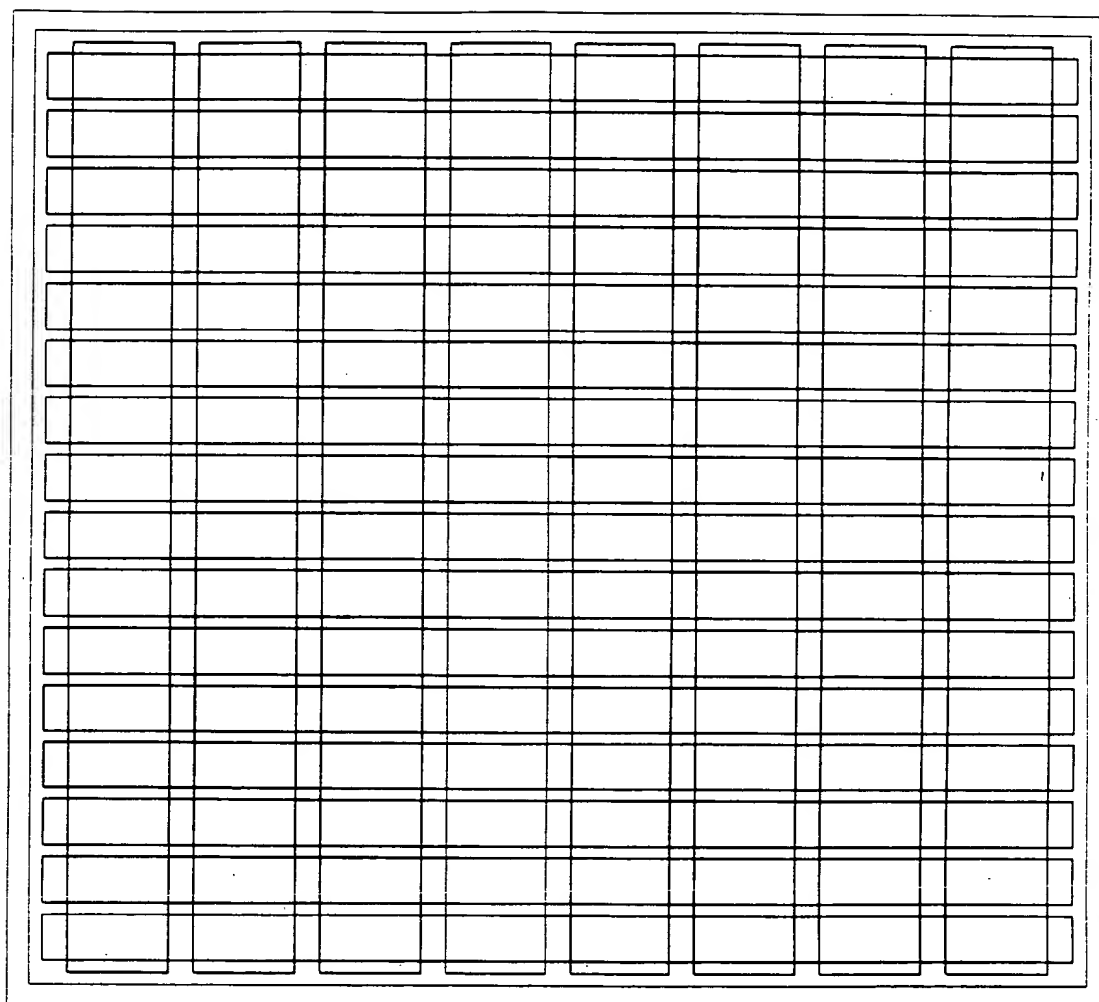
FIG. 33D2

TO
FIG.
33D4

FIG. 33D2

TO
FIG.
33D1

(SEE FIG. 33E1)



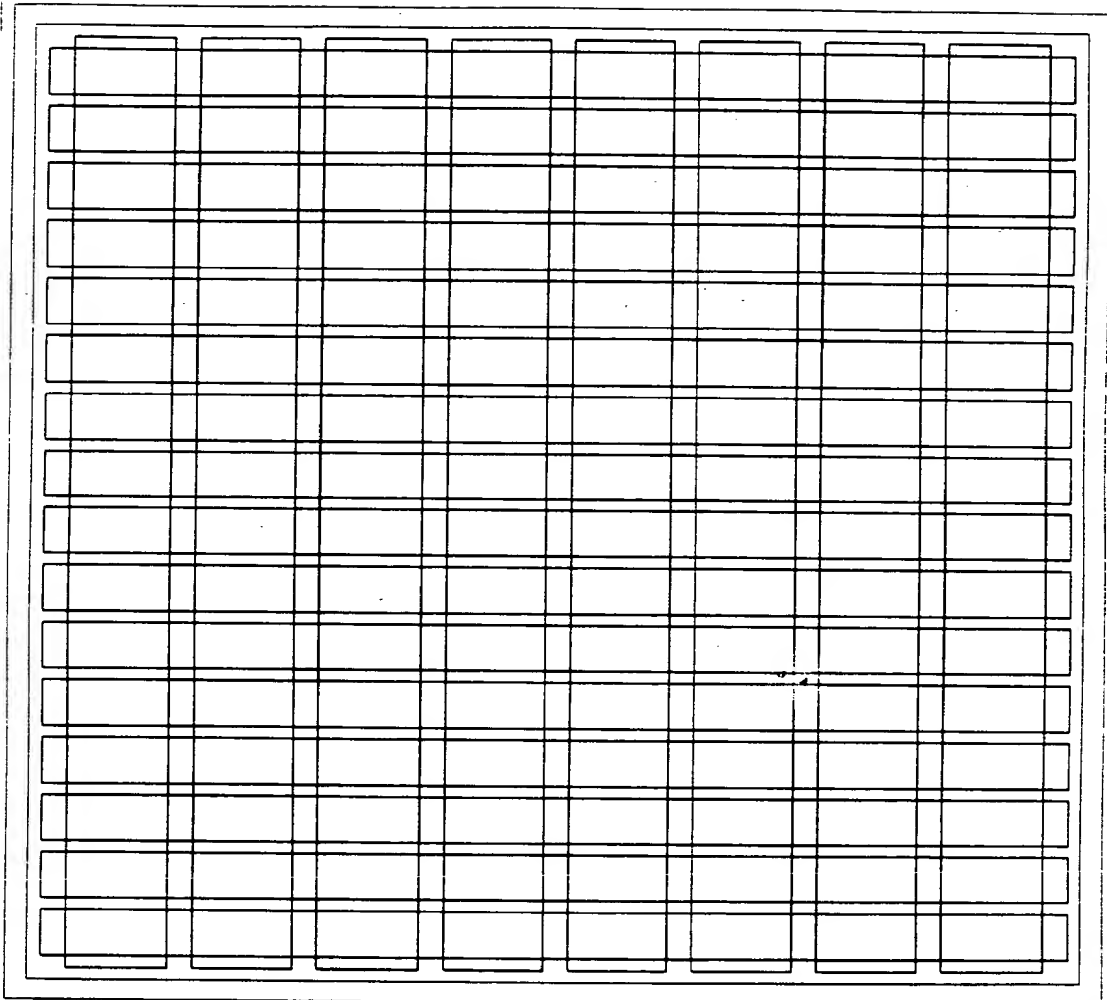
38

TO FIG. 33D4

FIG. 33D3

00034795-082201

TO
FIG.
33D2



40

TO FIG. 33D3

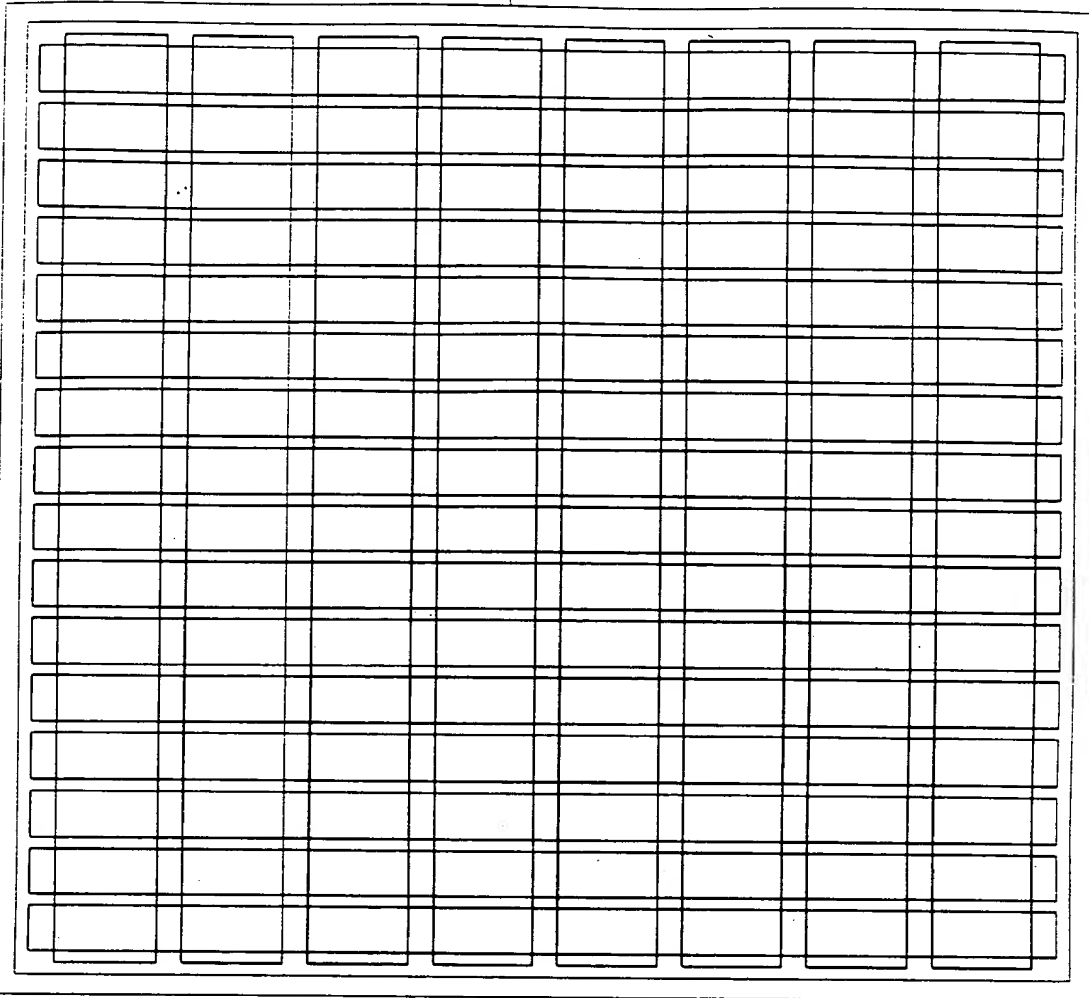
FIG. 33D4

FIG. 33D4

(SEE FIG. 33D2)

25

TO FIG. 33E2



DVC2
GENERATOR 504

□ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □

VCCP
REGULATOR 220

DVC2
GENERATOR 505

TO
FIG.
33E3

FIG. 33E1

127/367

TO FIG. 33E1

27

10

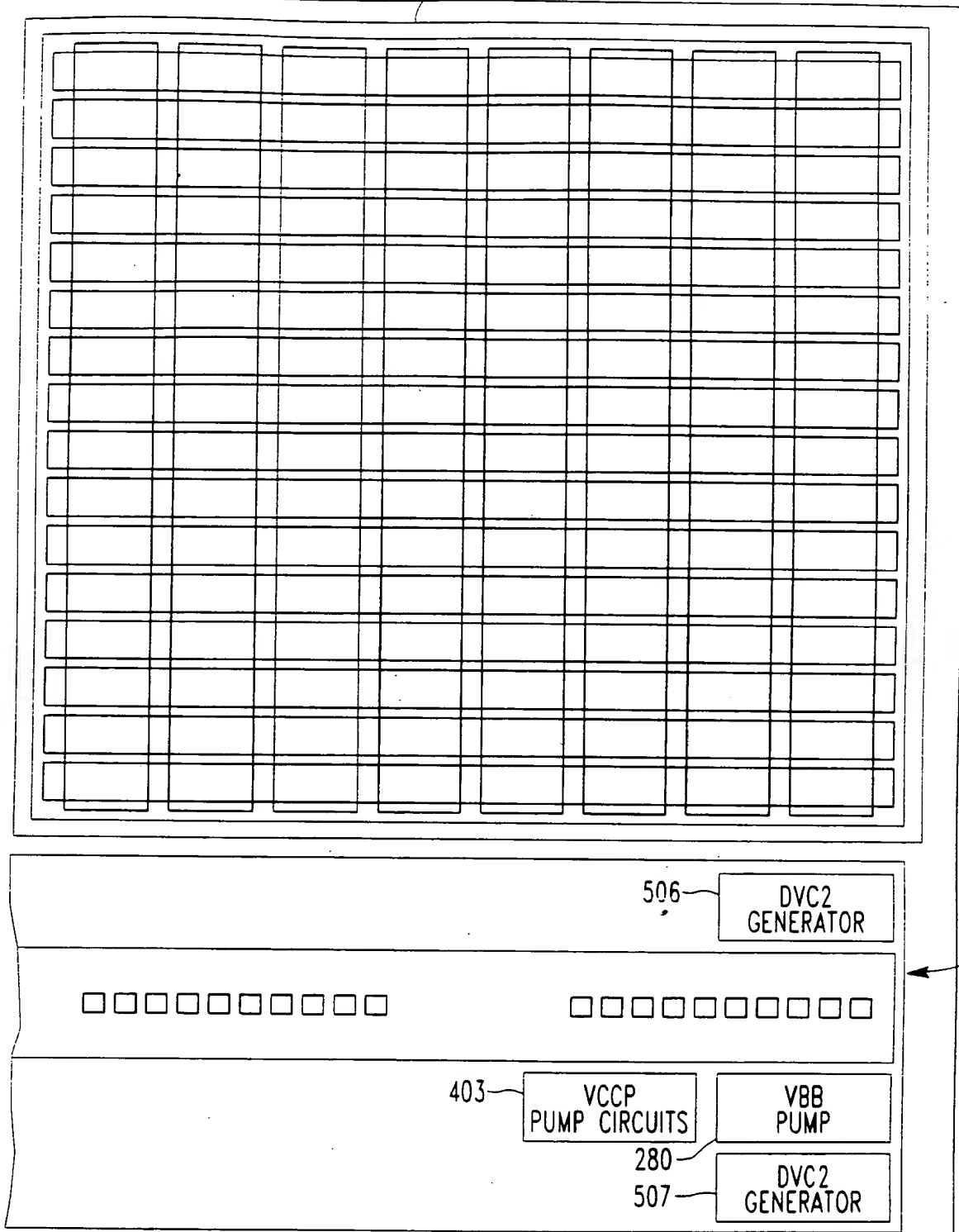
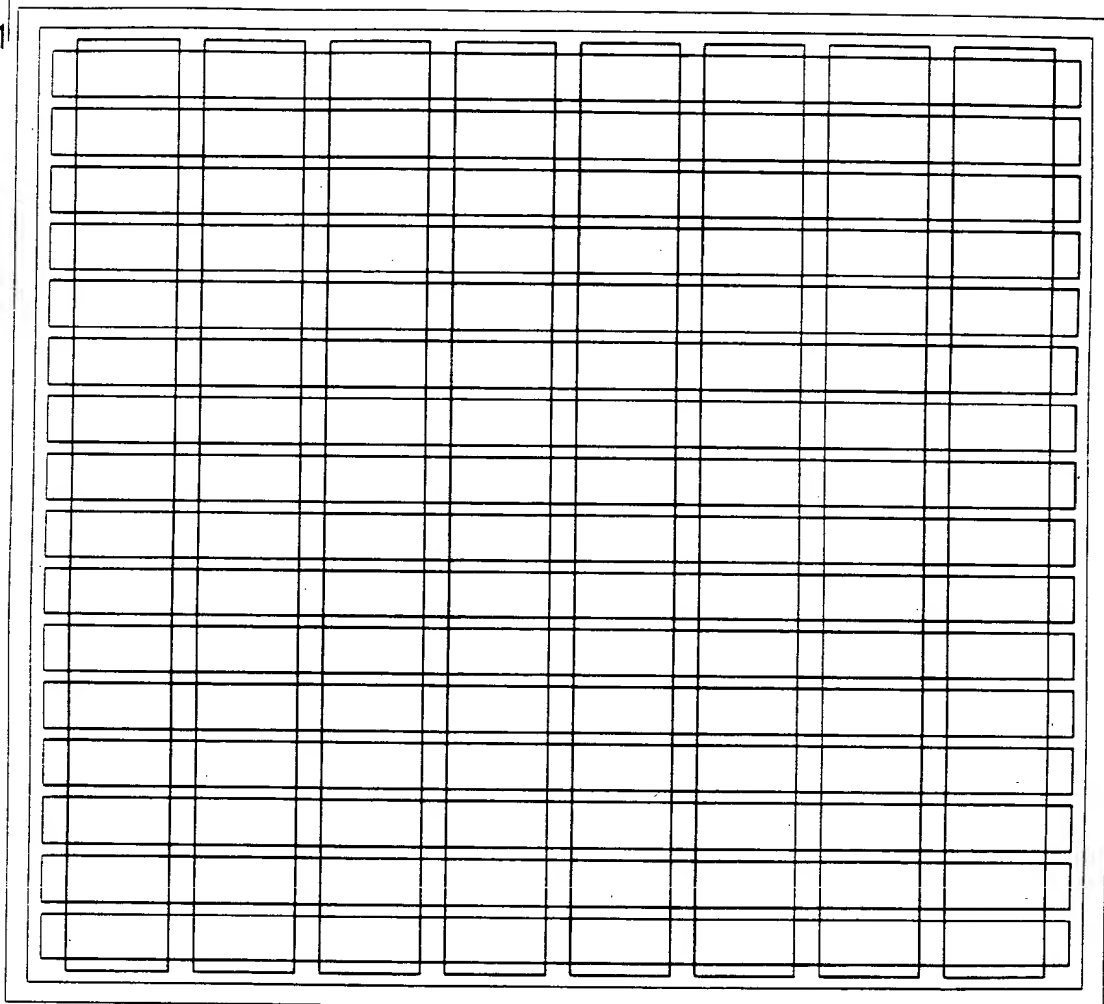


FIG. 33E2

TO
FIG.
33E4

FIG. 33E3

TO
FIG.
33E1

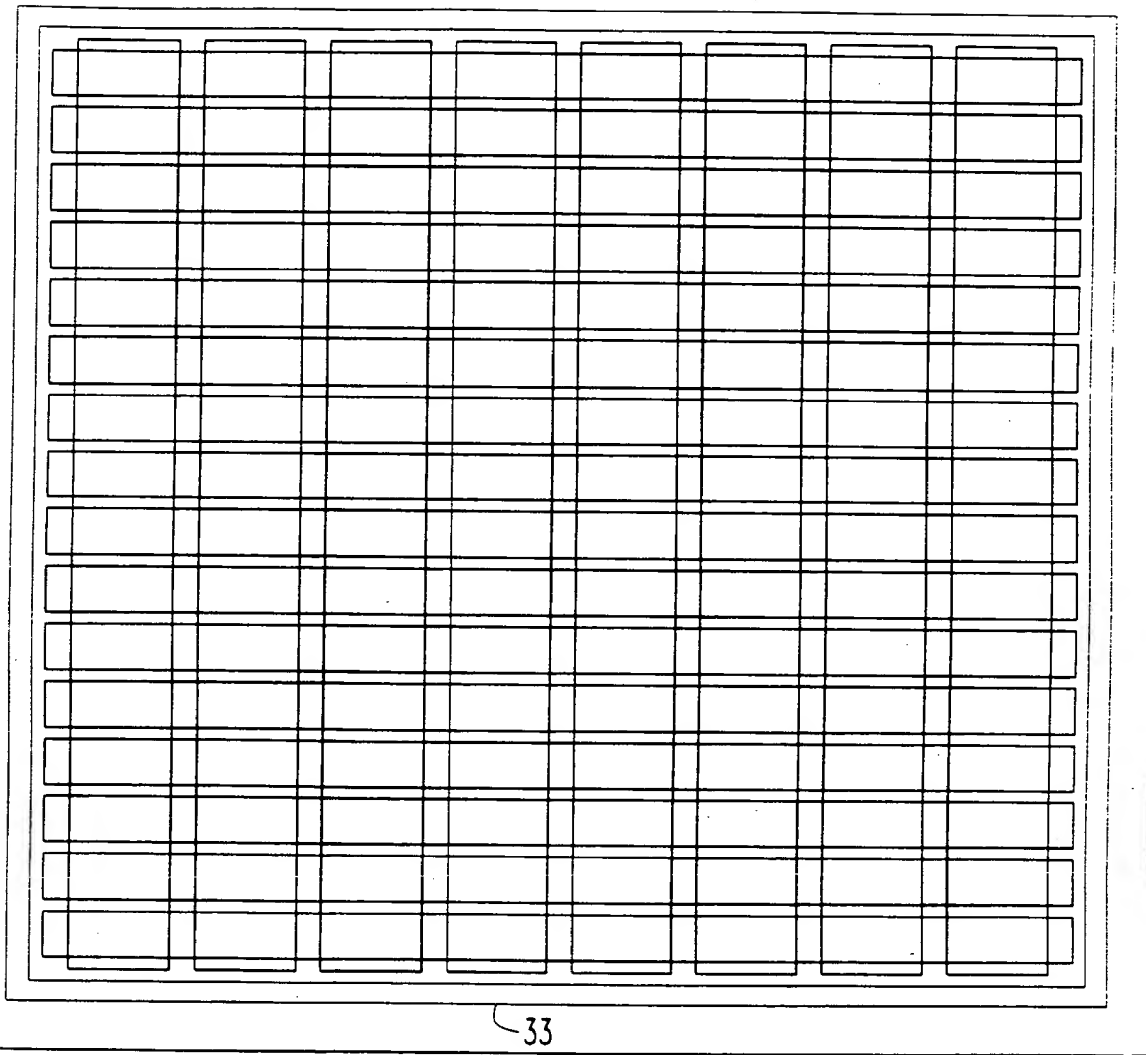


31

FIG. 33E3

TO FIG. 33E4

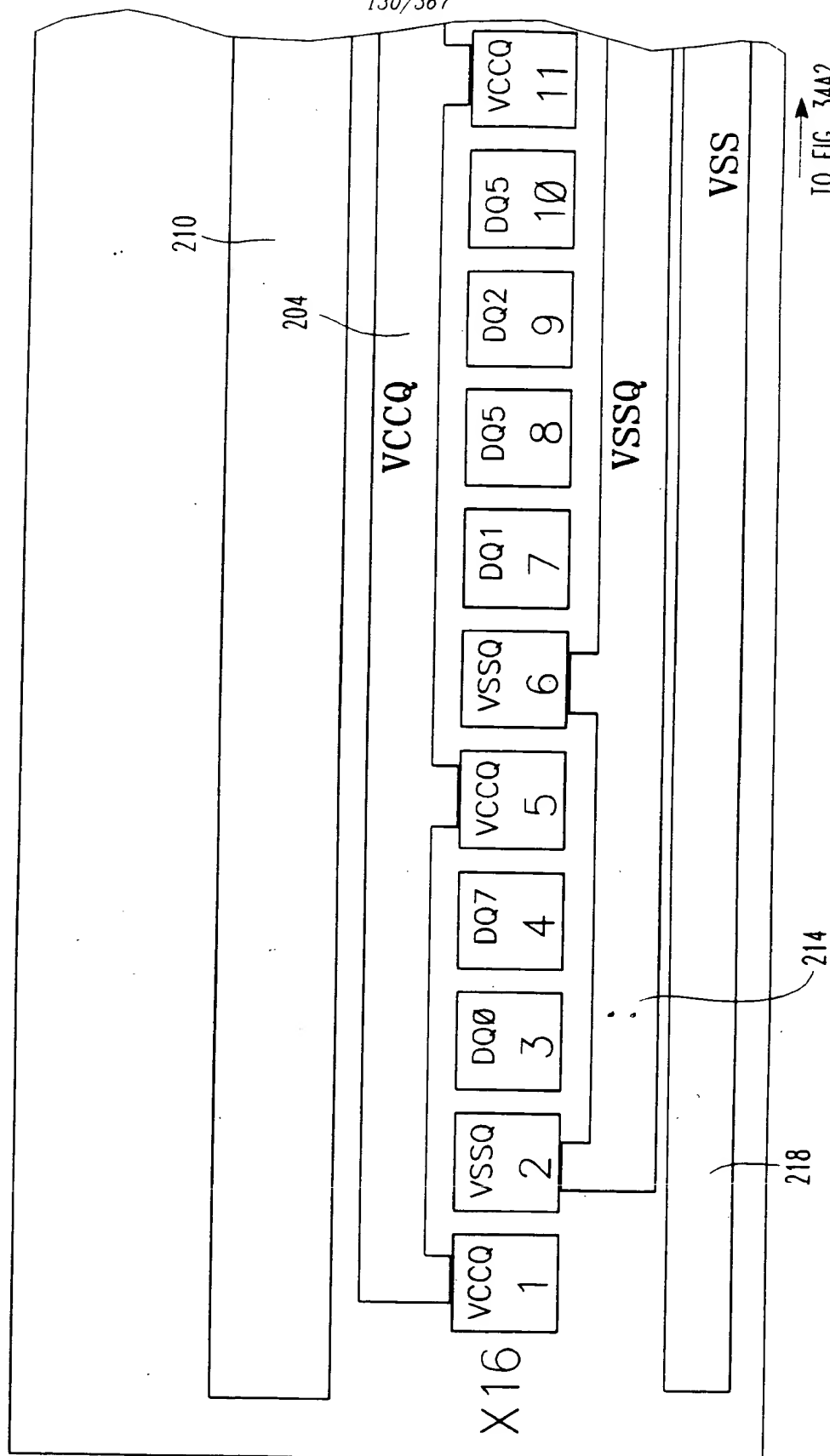
FIG. 33E4



TO FIG. 33E3

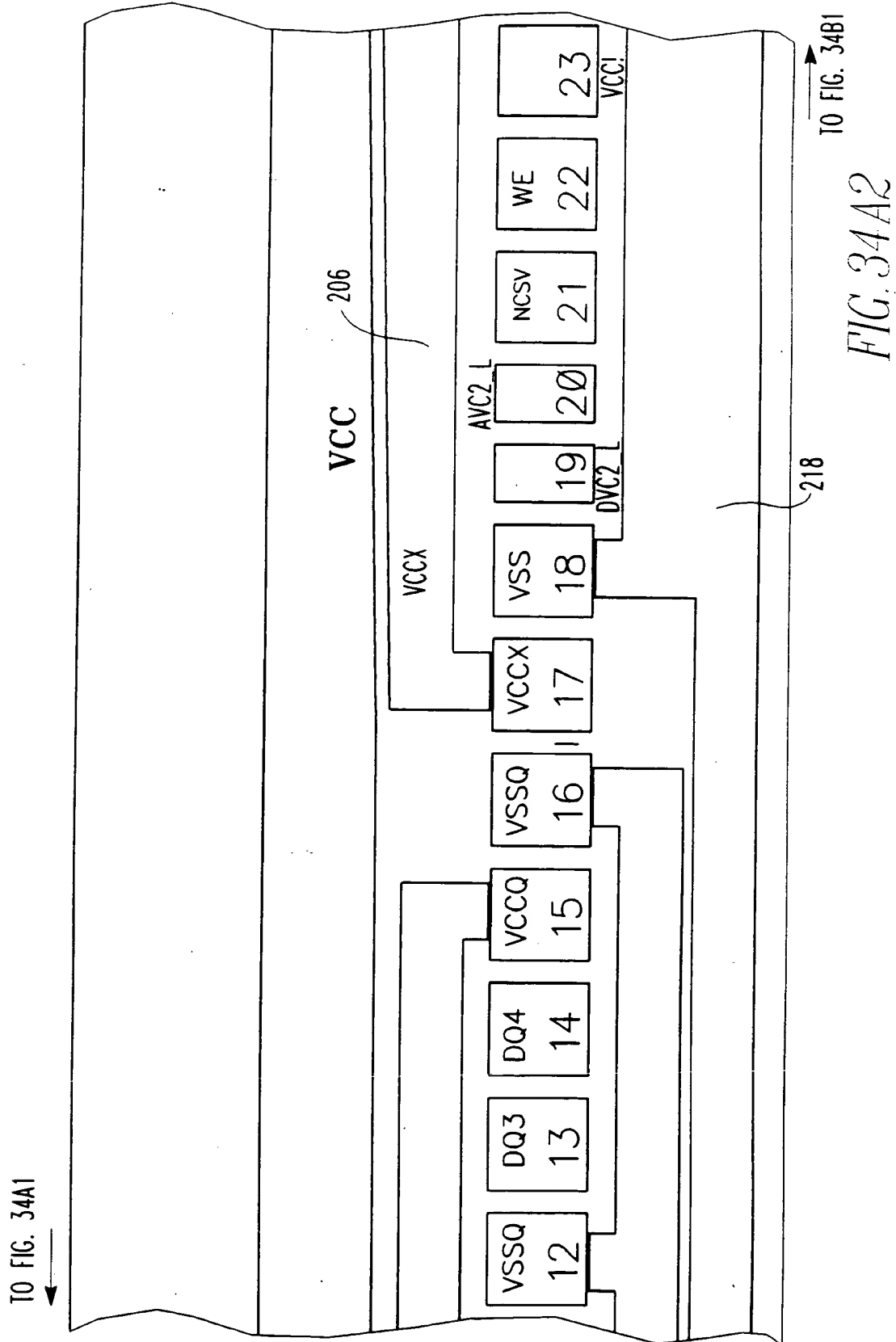
FIG. 33E4

TO
FIG.
33E2

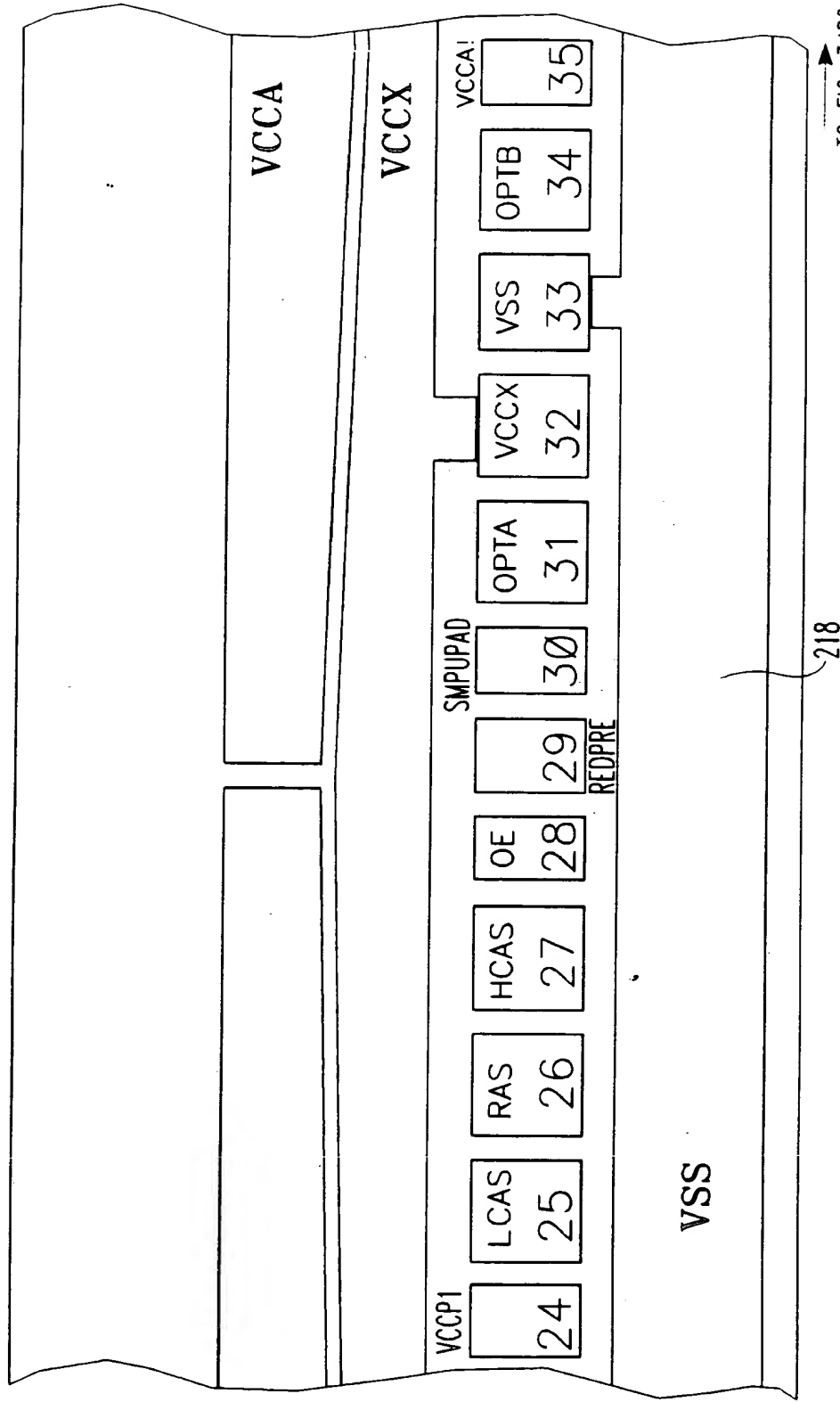


TO FIG. 34A2

FIG. 34A1



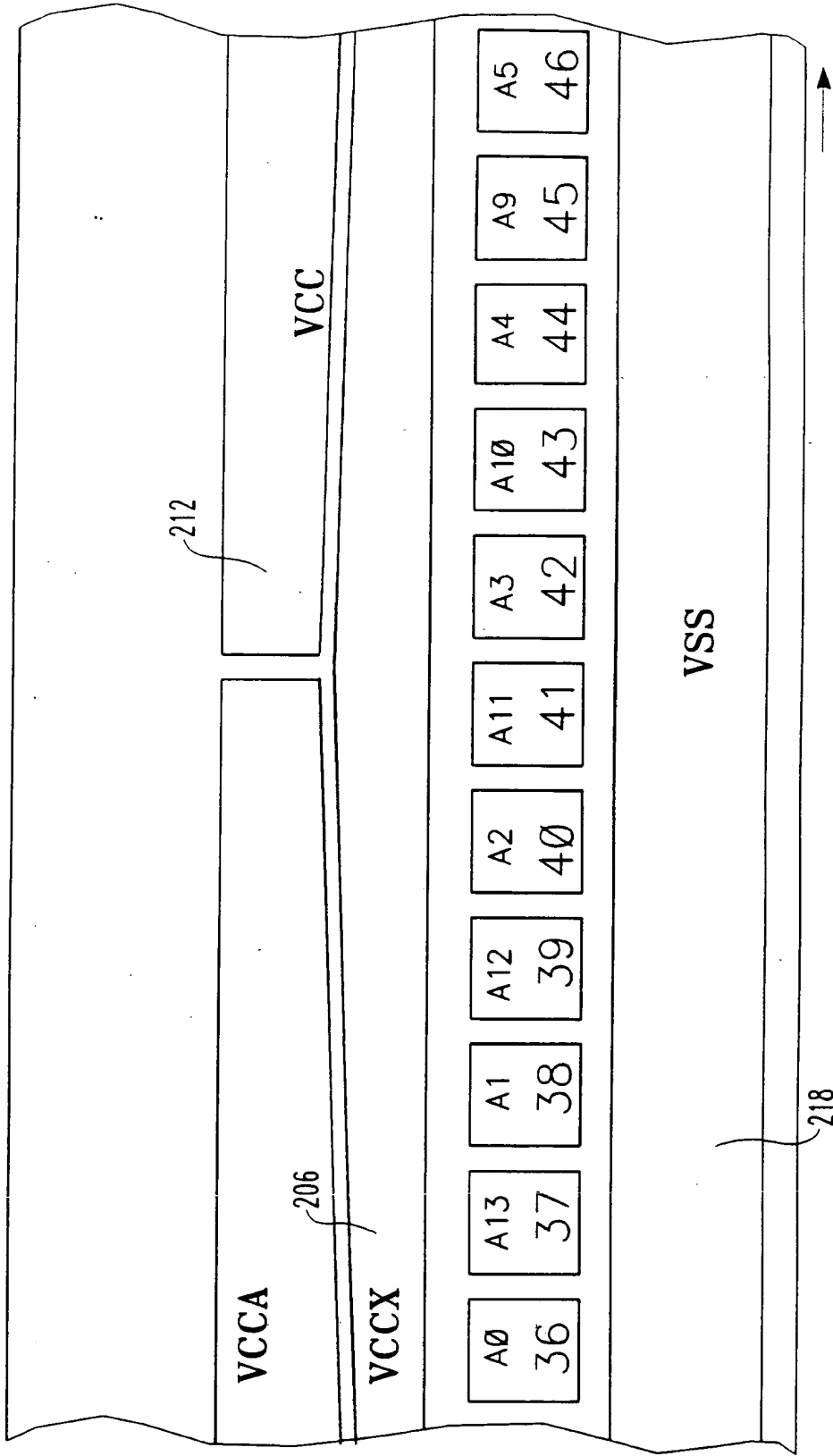
TO FIG. 34A2
↓



TO FIG. 34B2
↑

FIG. 34B1

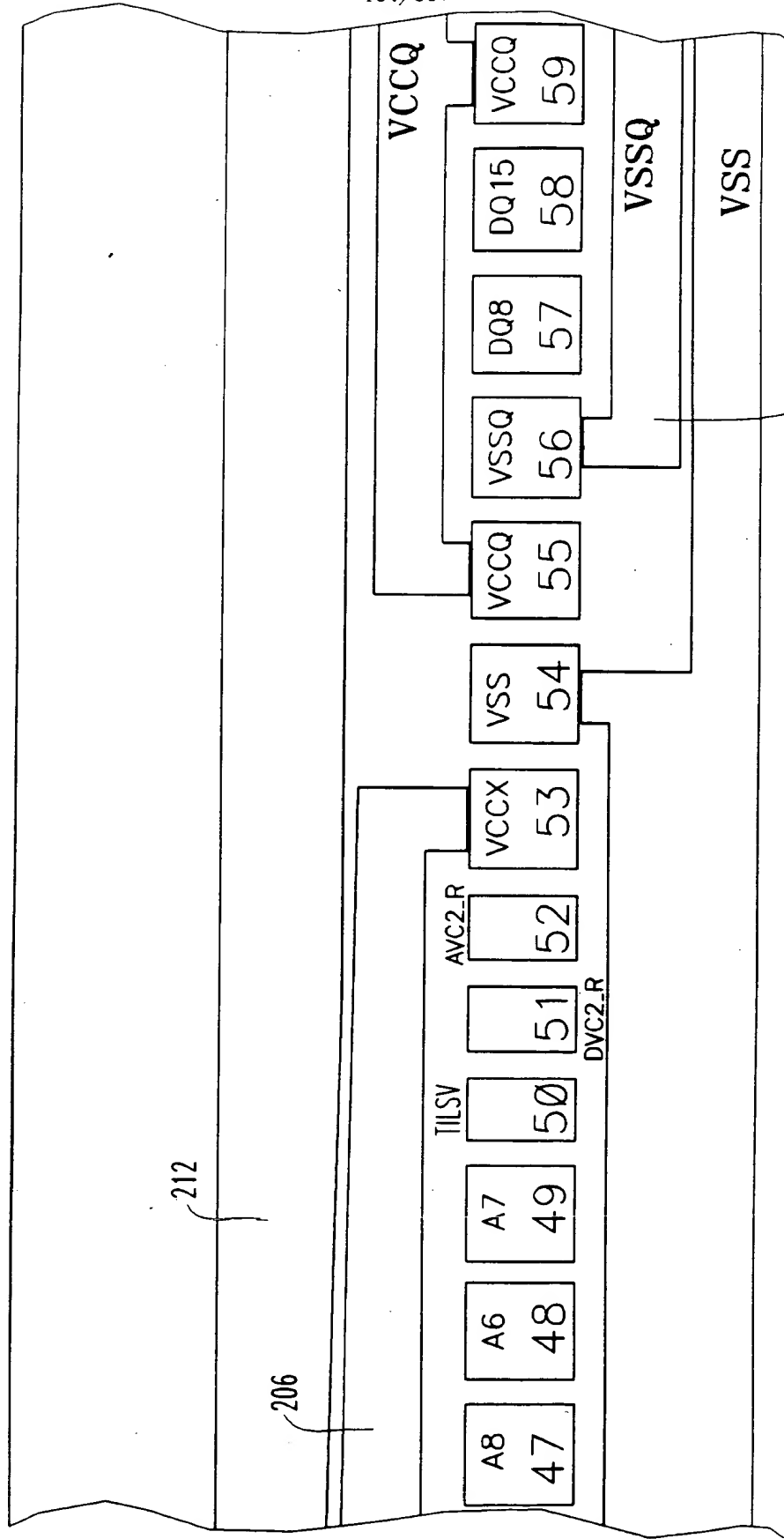
TO FIG. 34B1



TO FIG. 34C1

FIG. 34B2

TO FIG. 34B2



TO FIG. 34C2

FIG. 34C1

216

TO FIG. 34C1

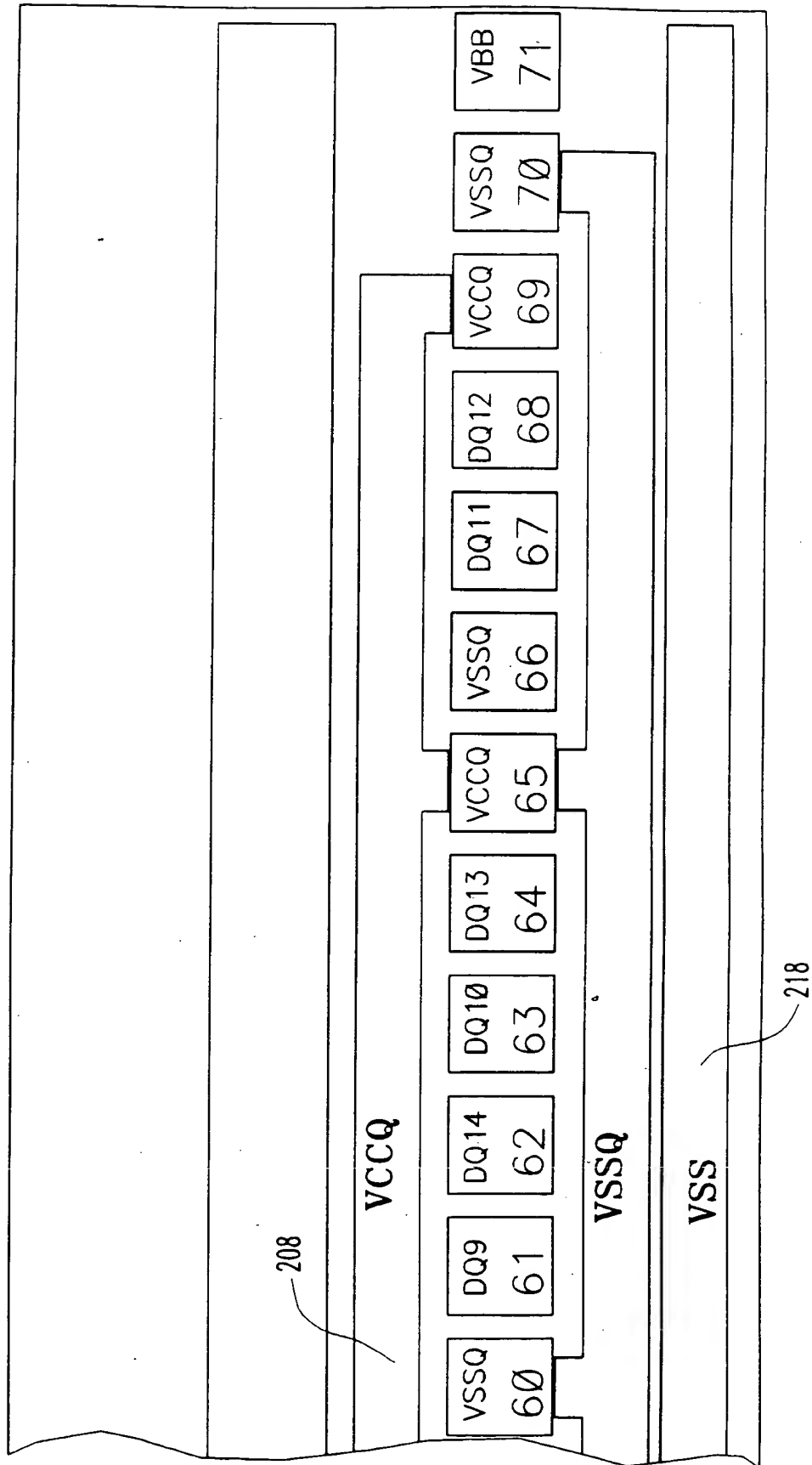


FIG. 34C2

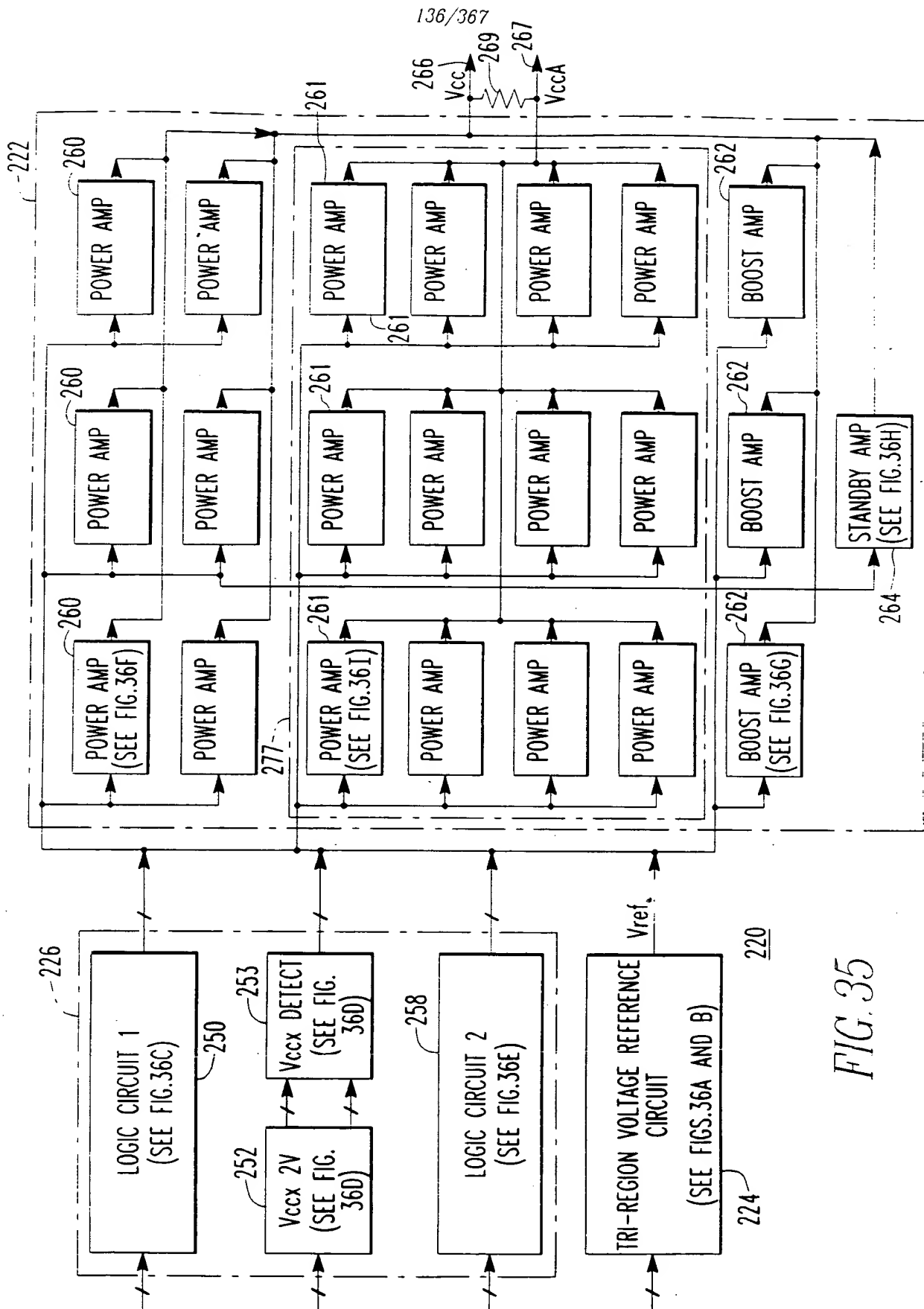


FIG. 35

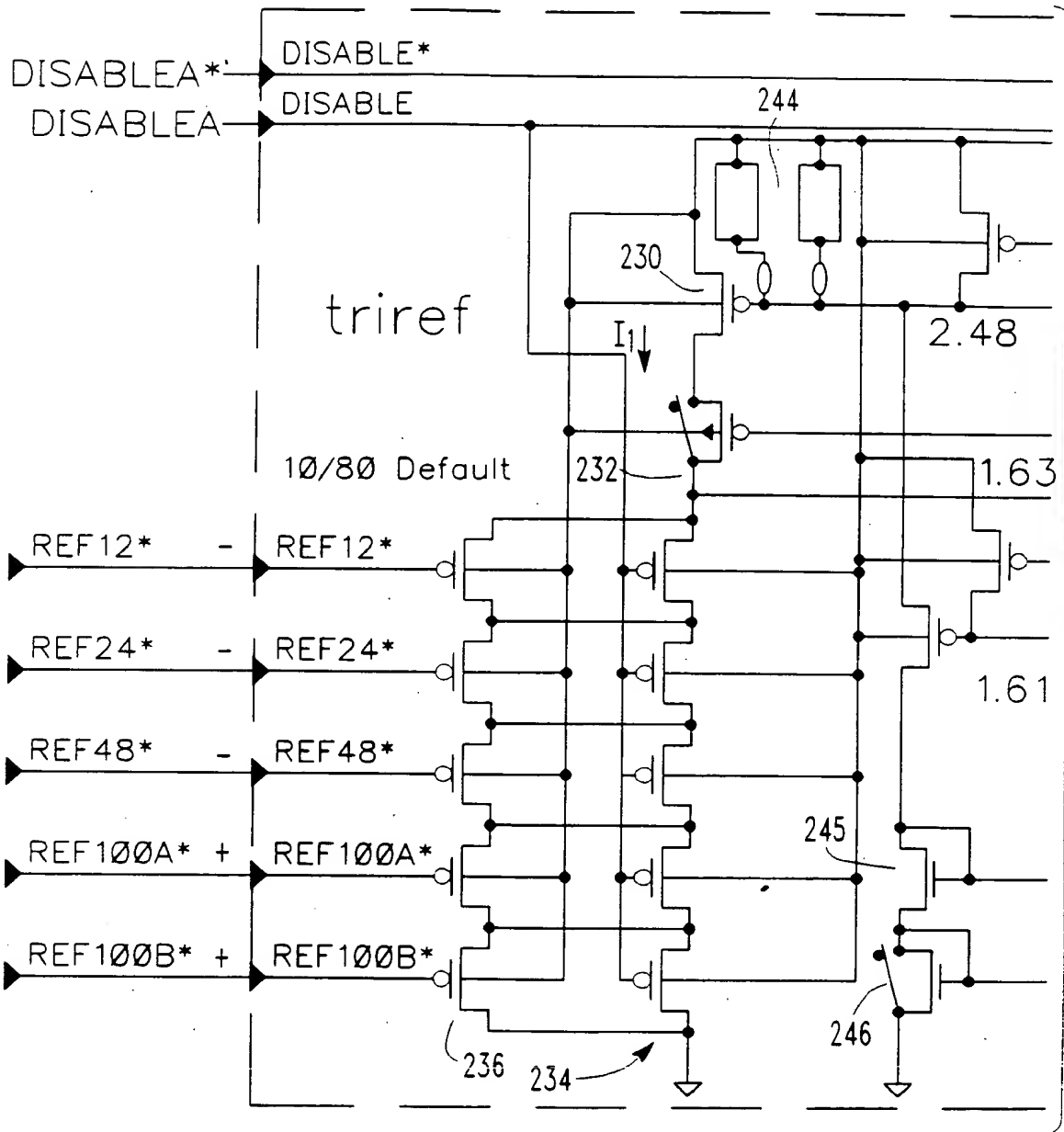


FIG. 36A1

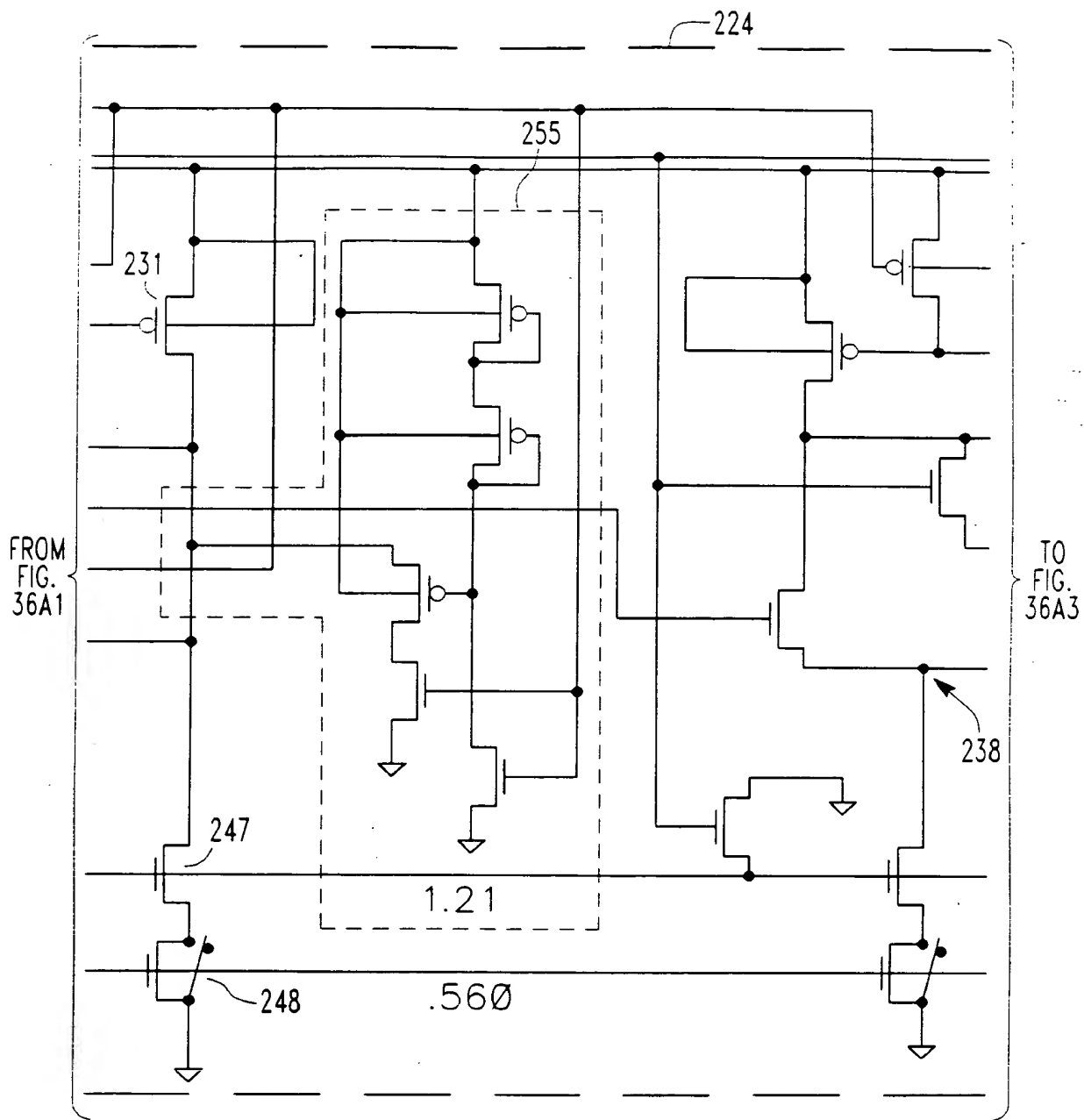


FIG. 36A2

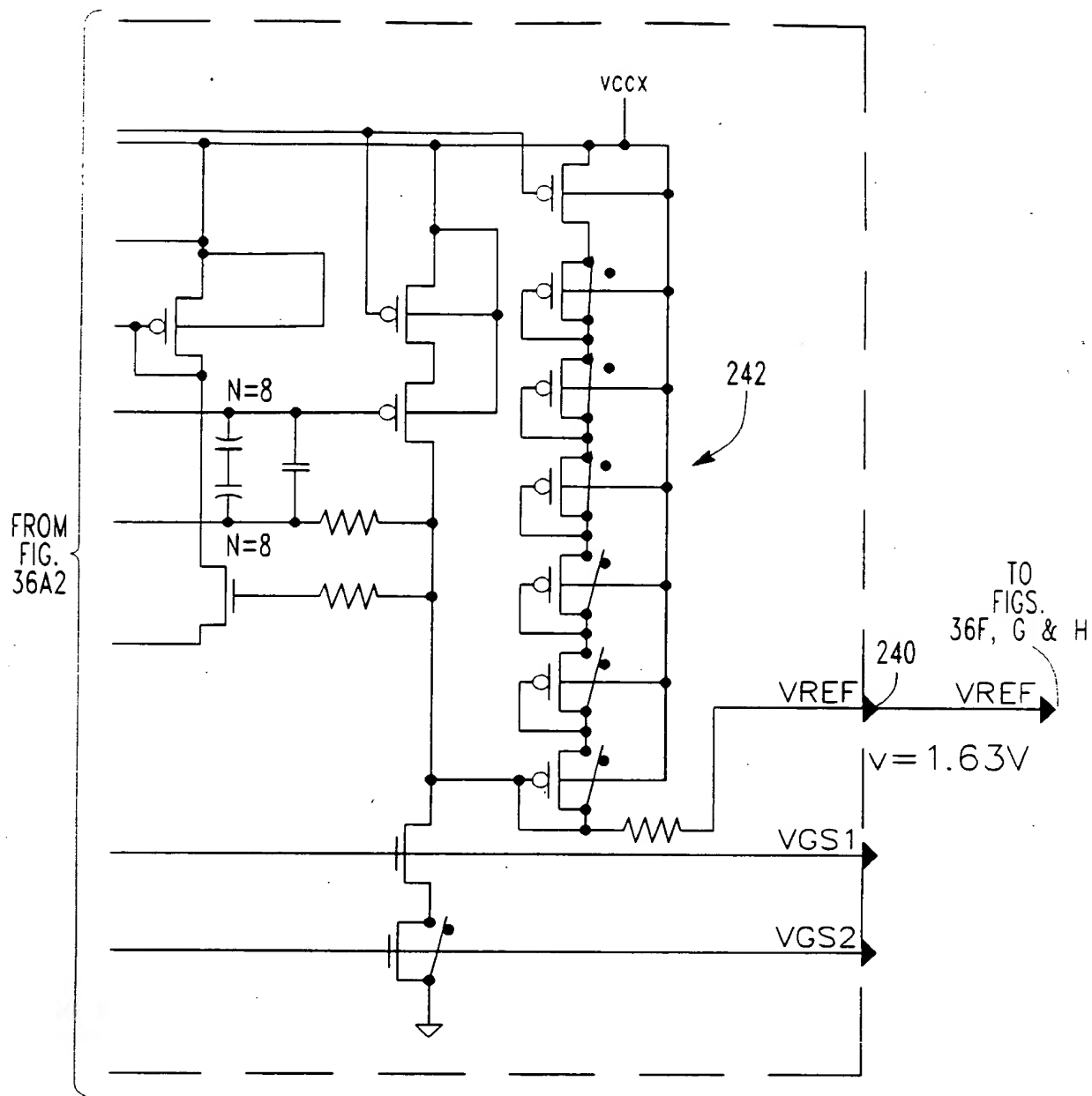


FIG. 36A3

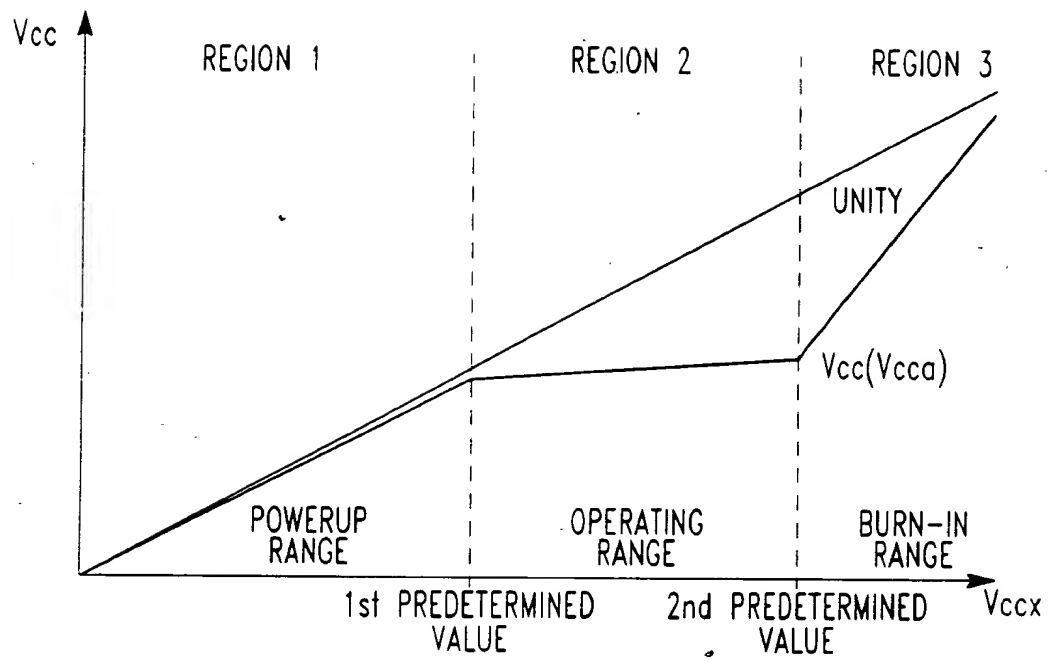


FIG. 36B

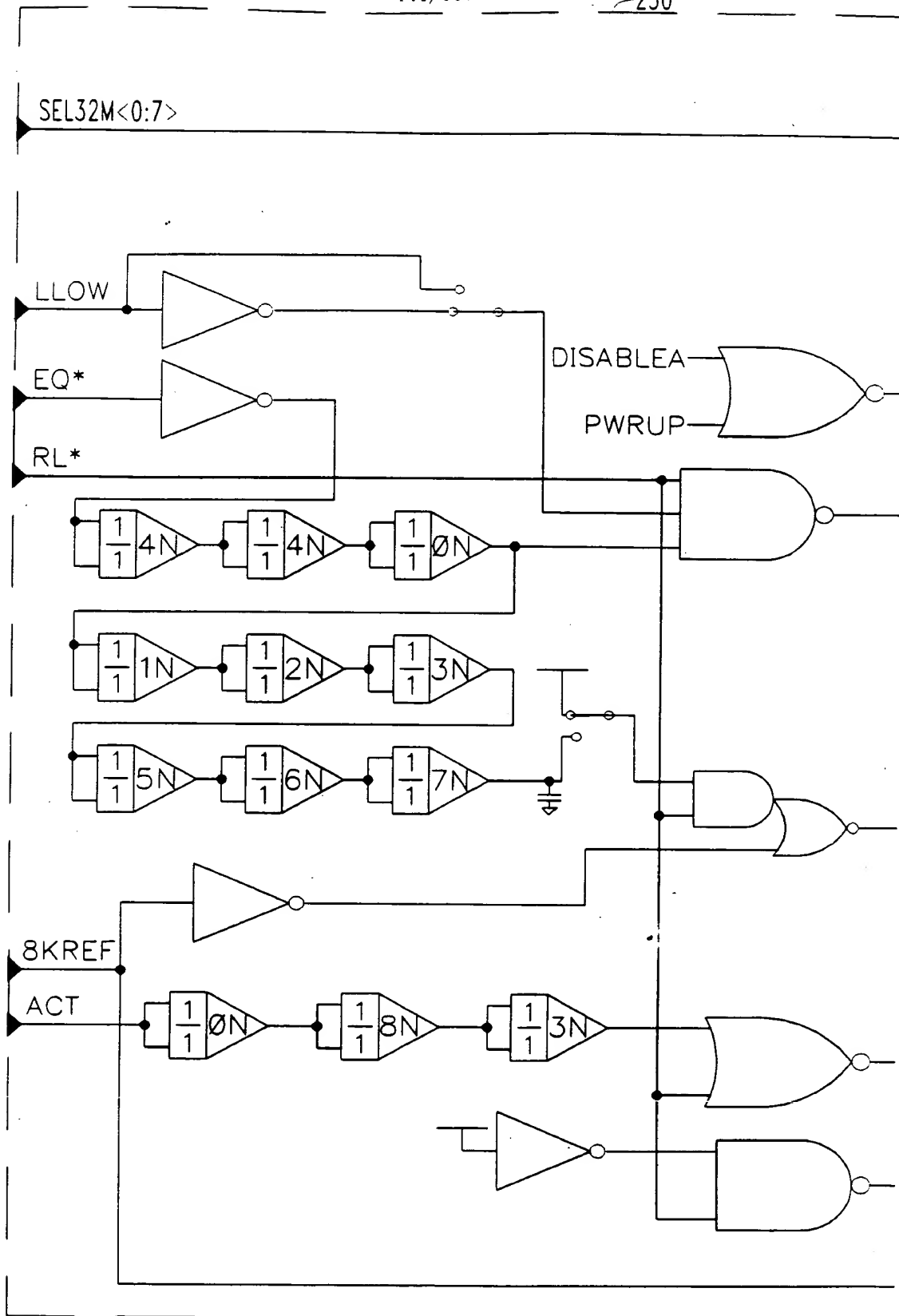
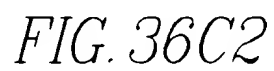
TO
FIG.
36C2

FIG. 36C1



FROM
FIG.
36C2

TO
FIG.
36F

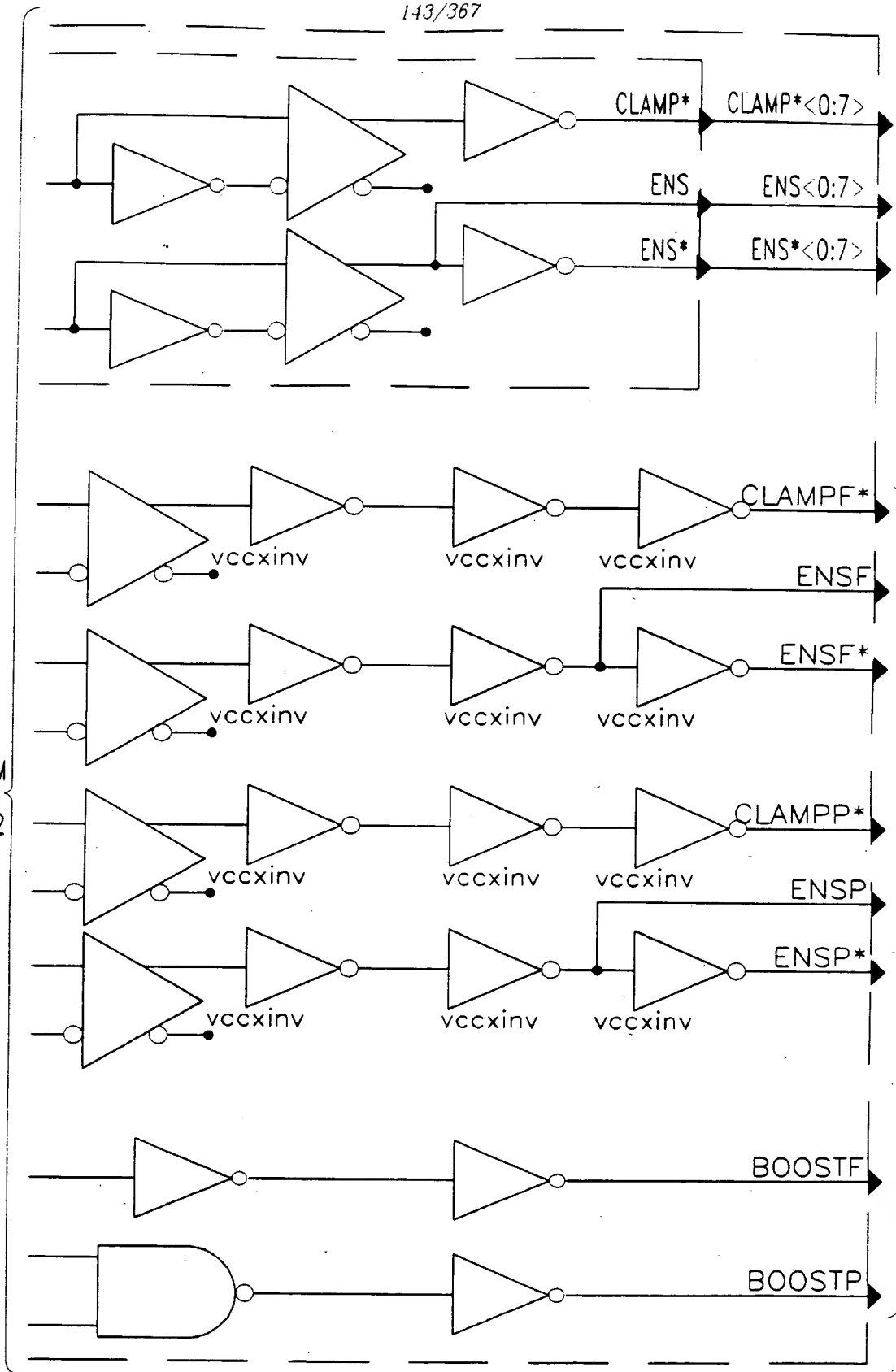


FIG. 36C3

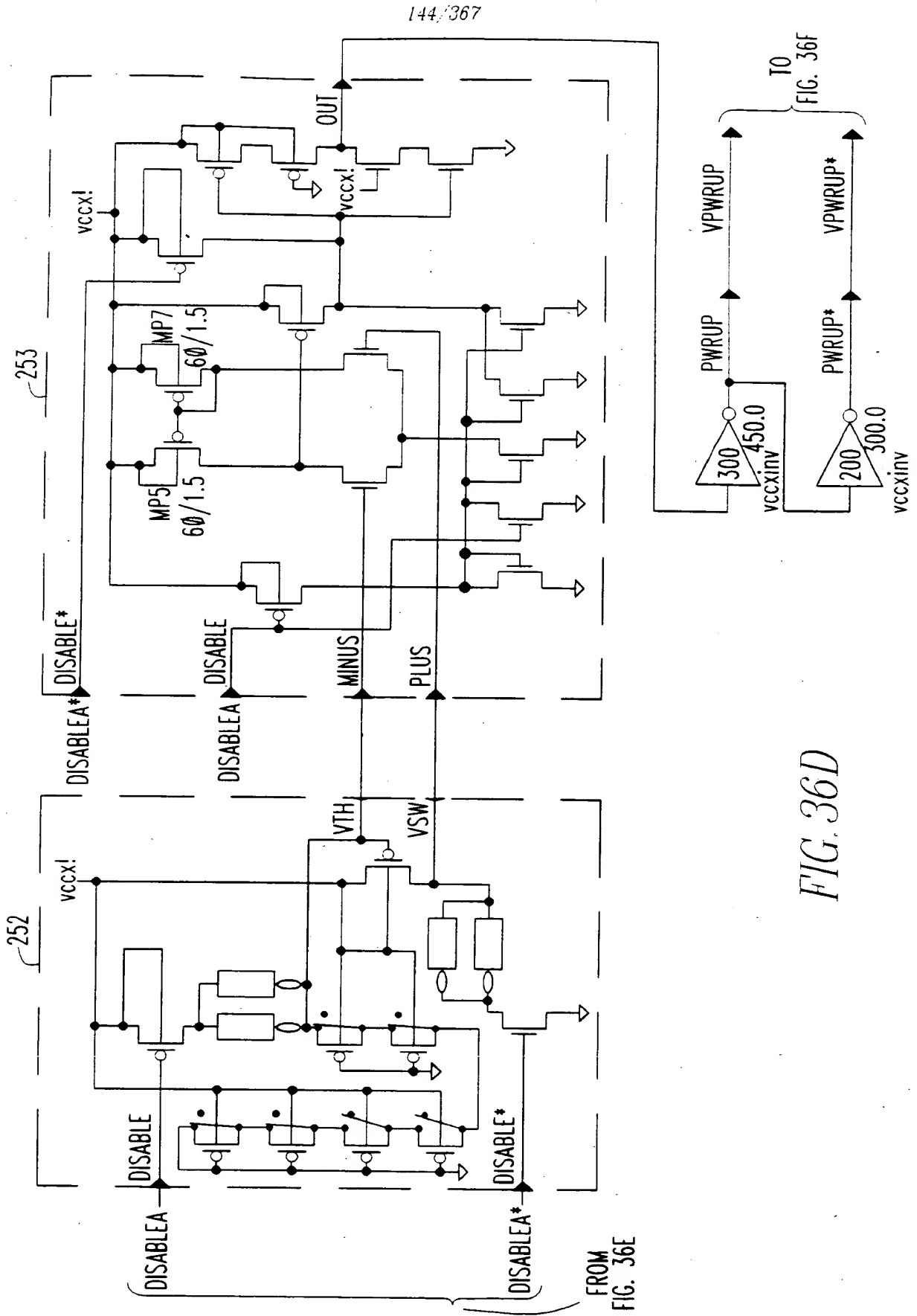


FIG. 36D

FIG. 36F

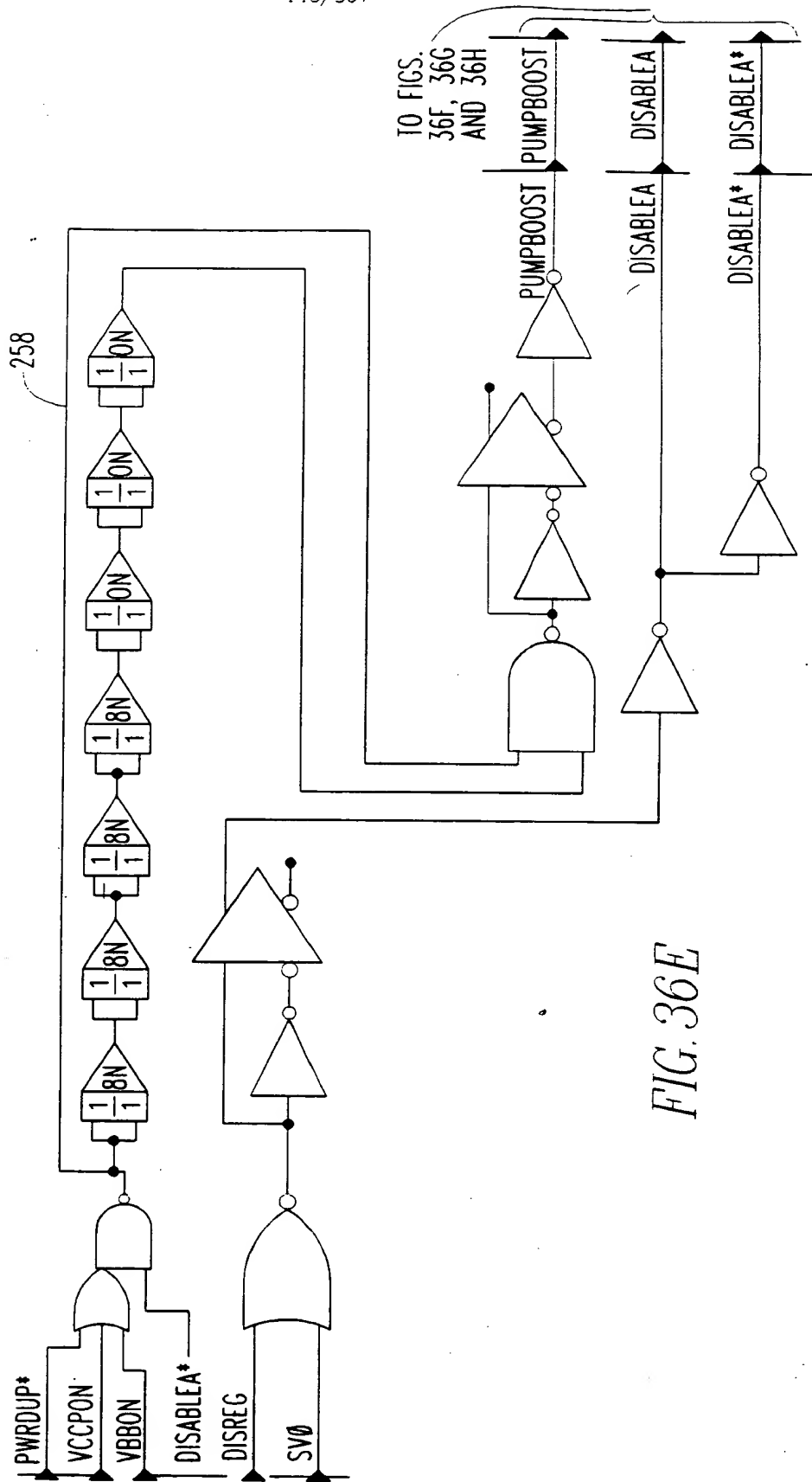
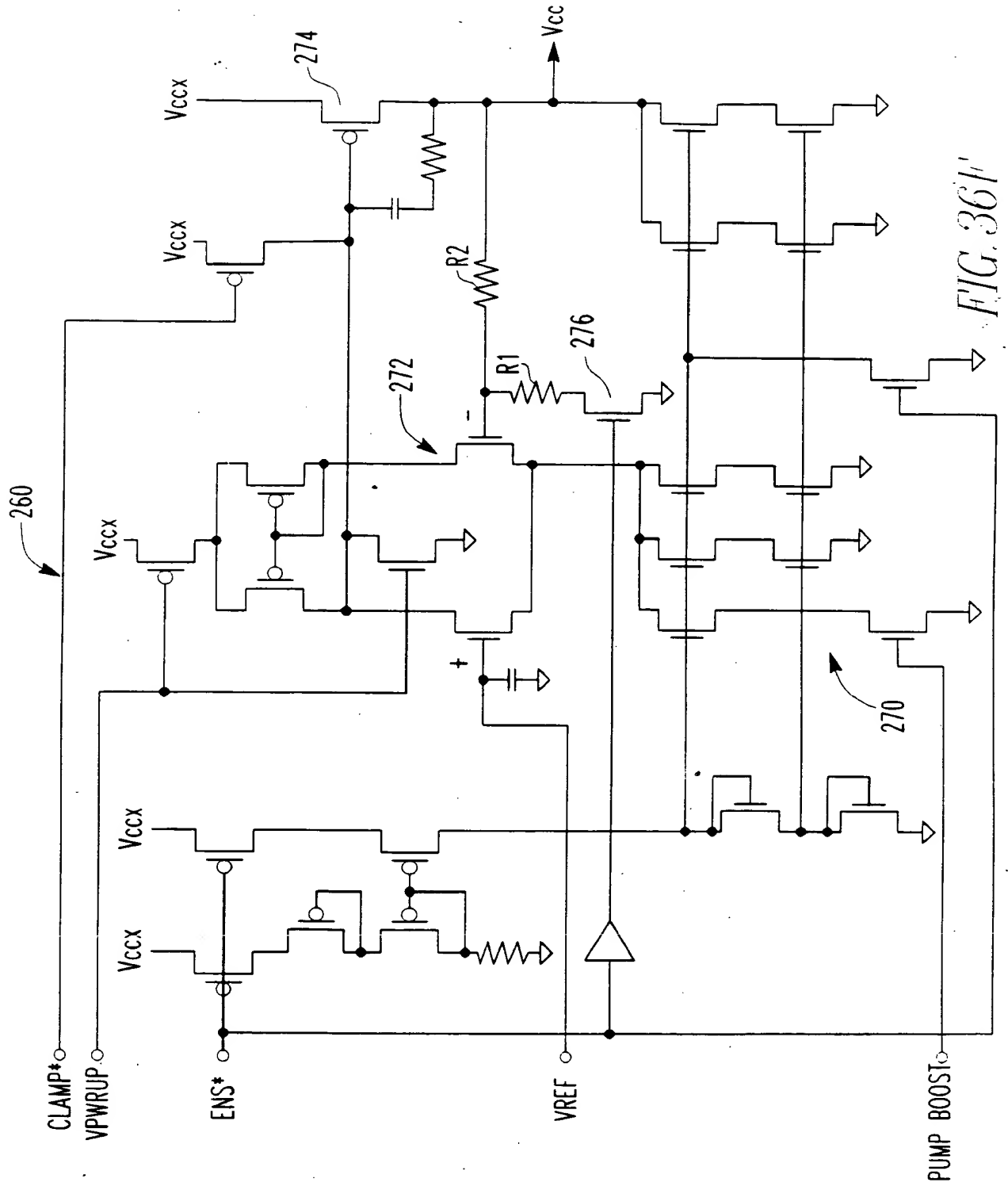


FIG. 36E

TO FIGS.
36F, 36G
AND 36H



262

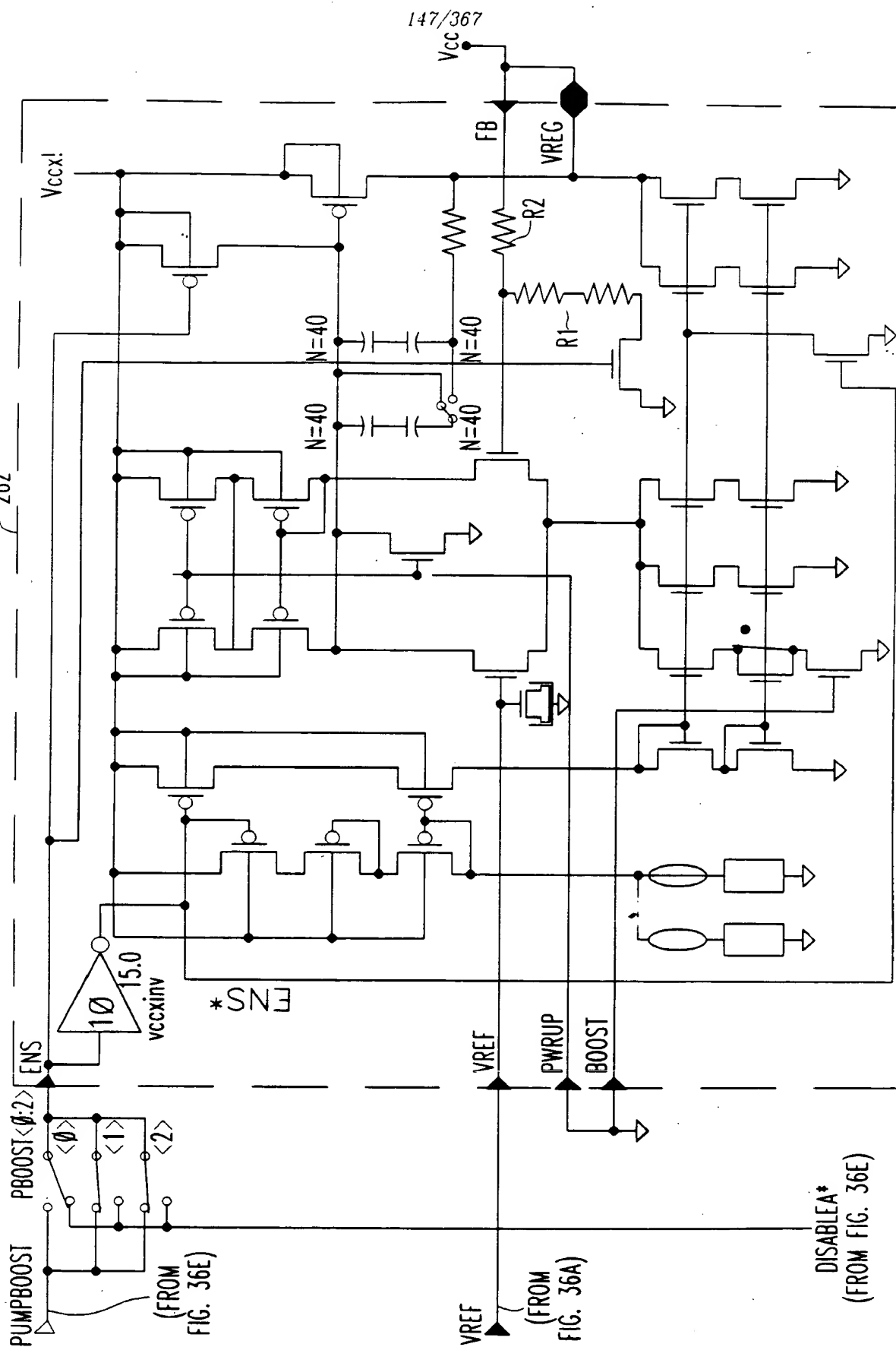
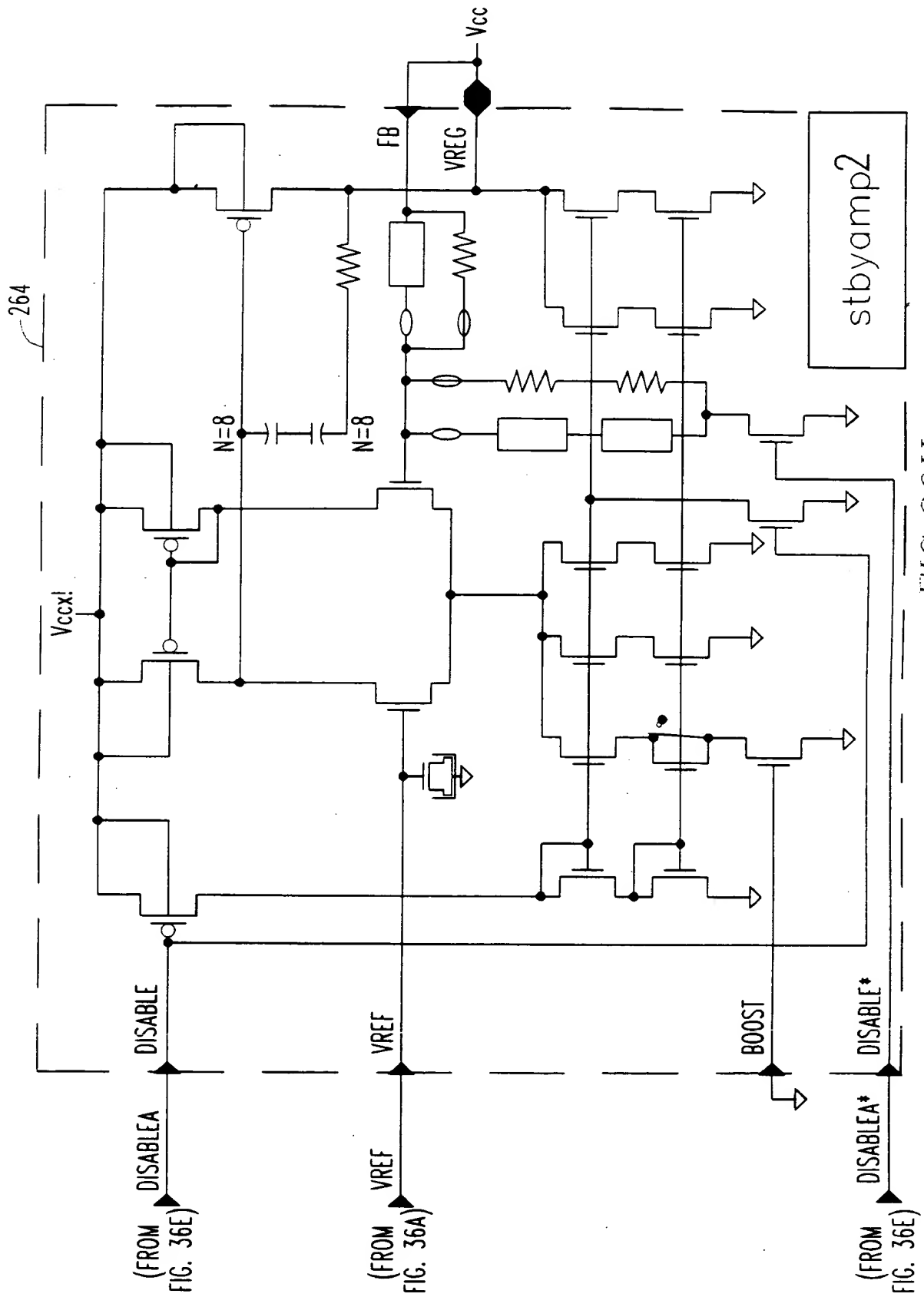


FIG. 36G



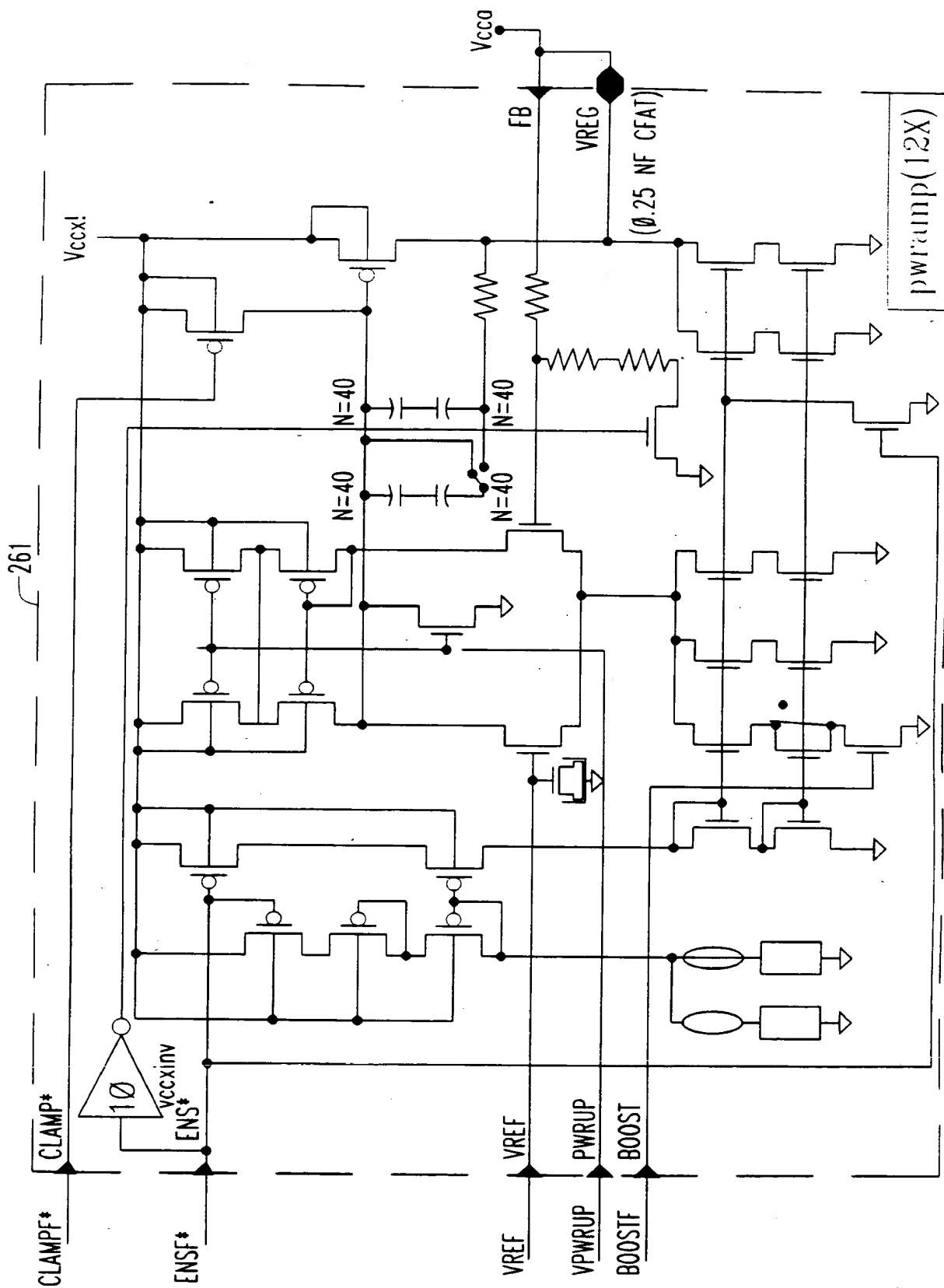


FIG. 361

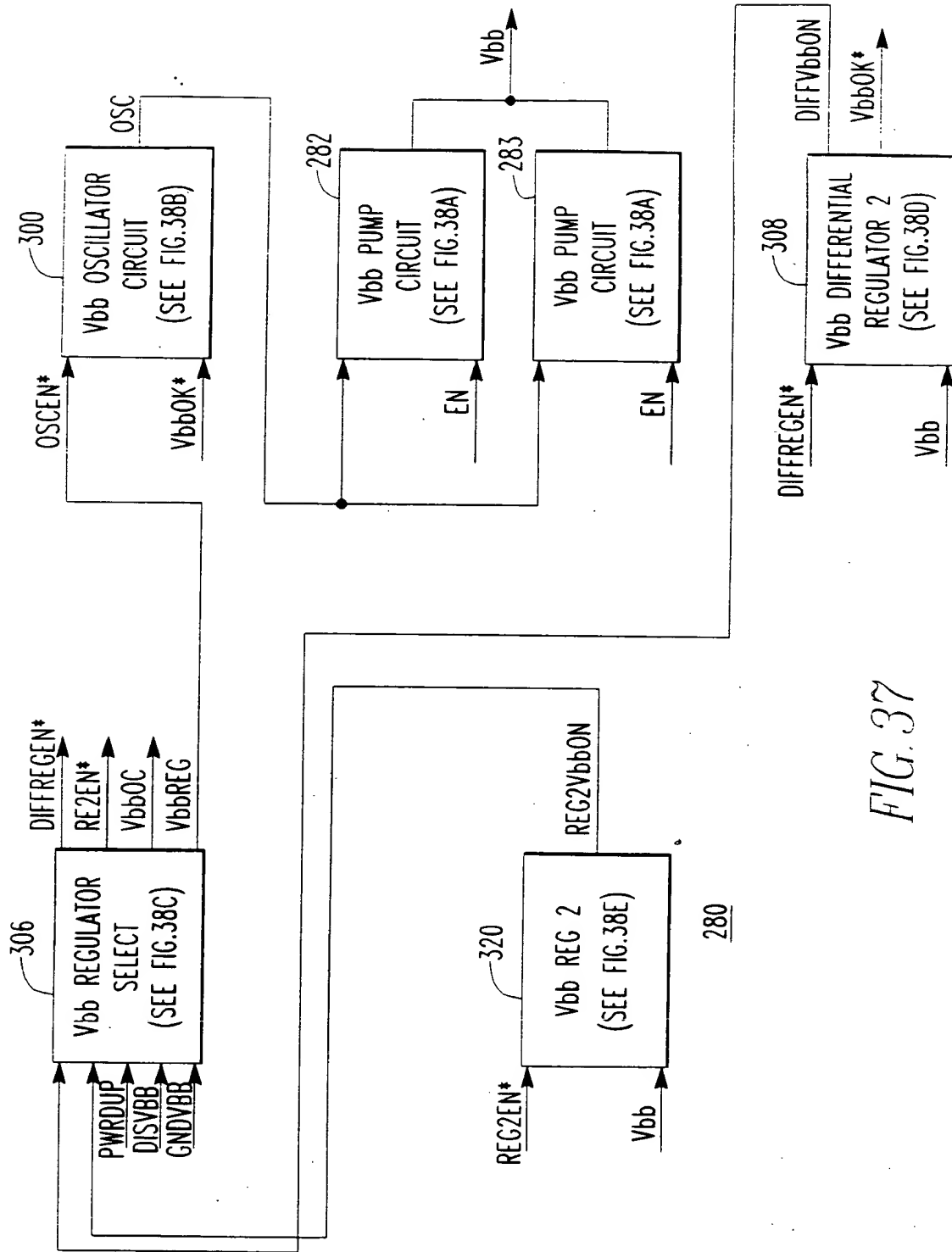


FIG. 37

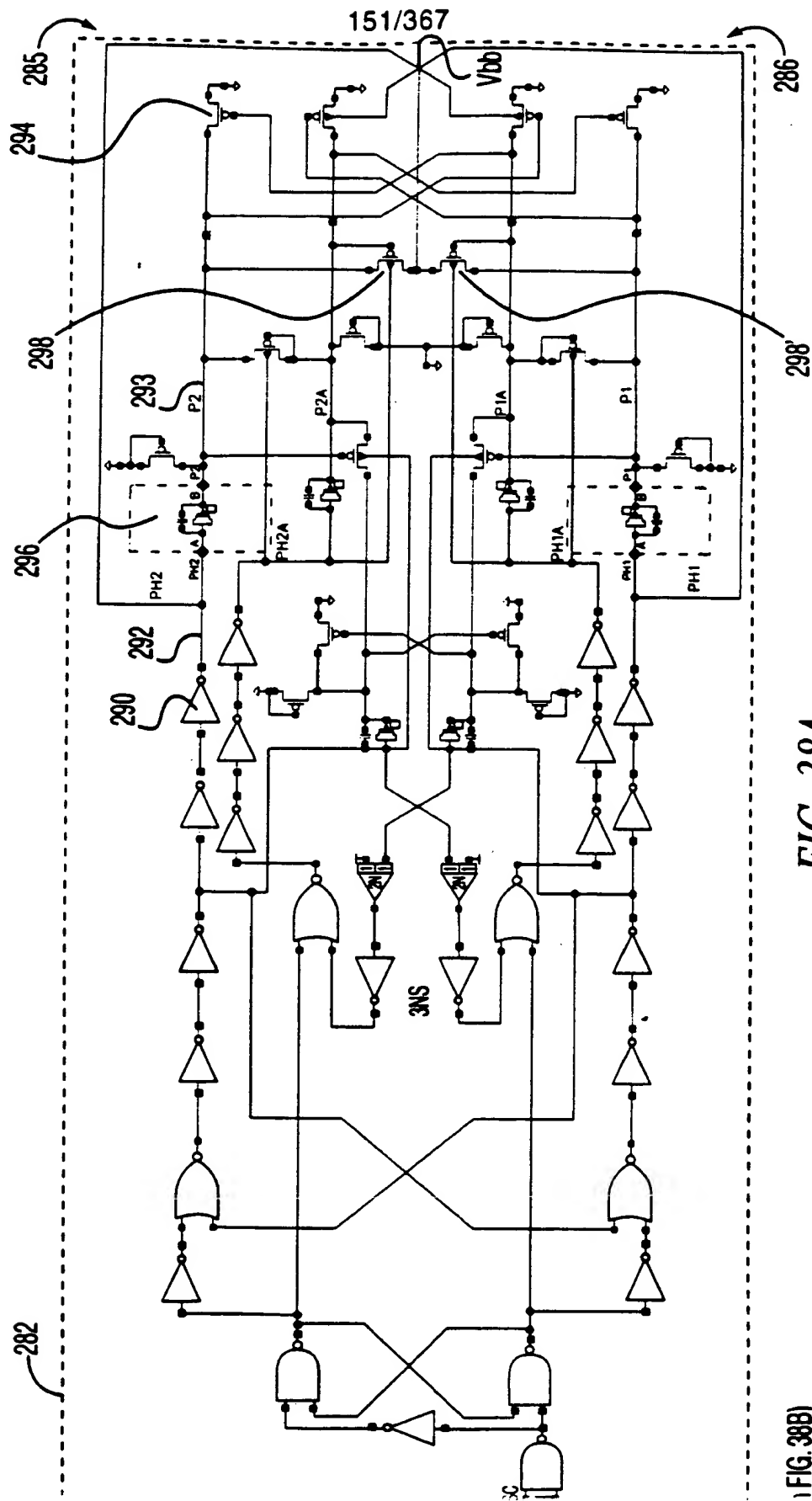
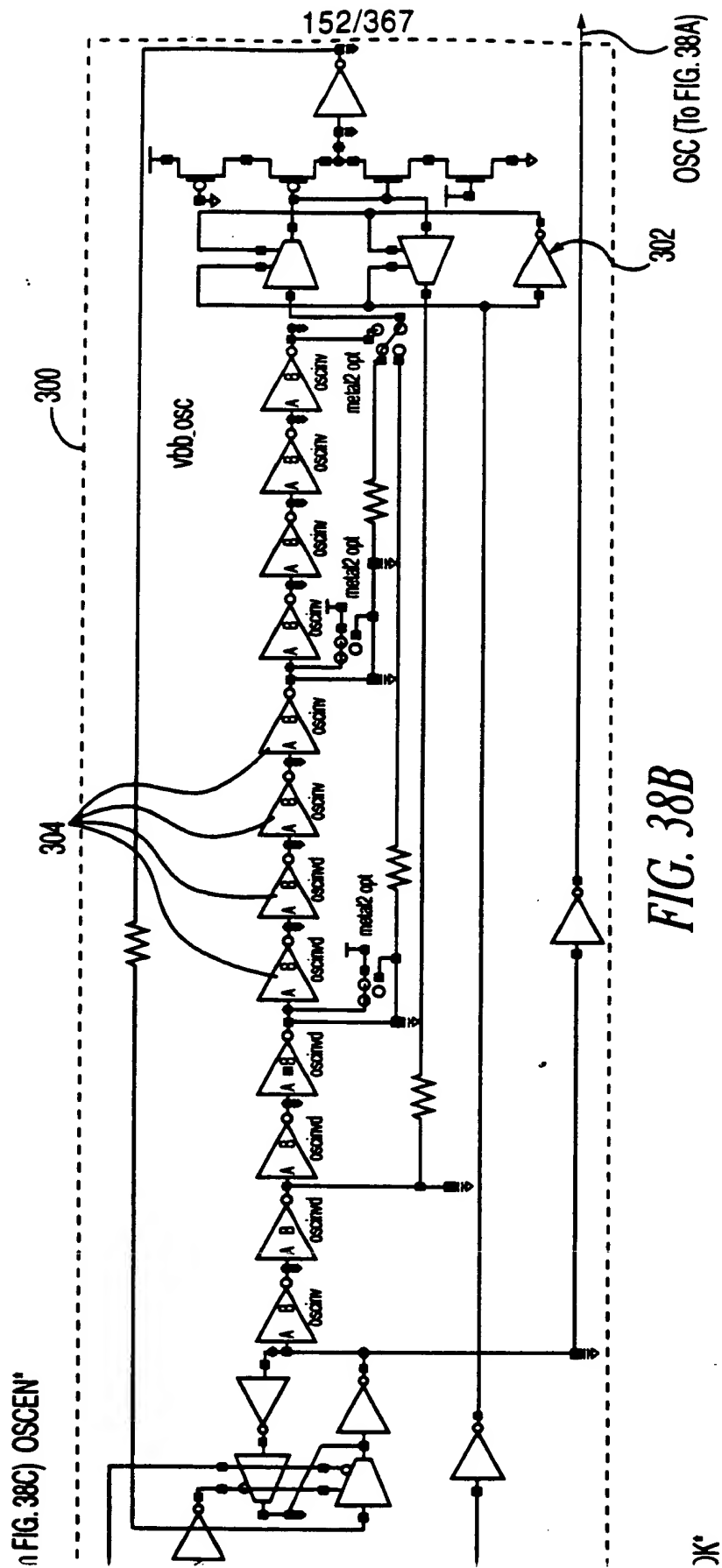


FIG. 38A

FIG. 38B



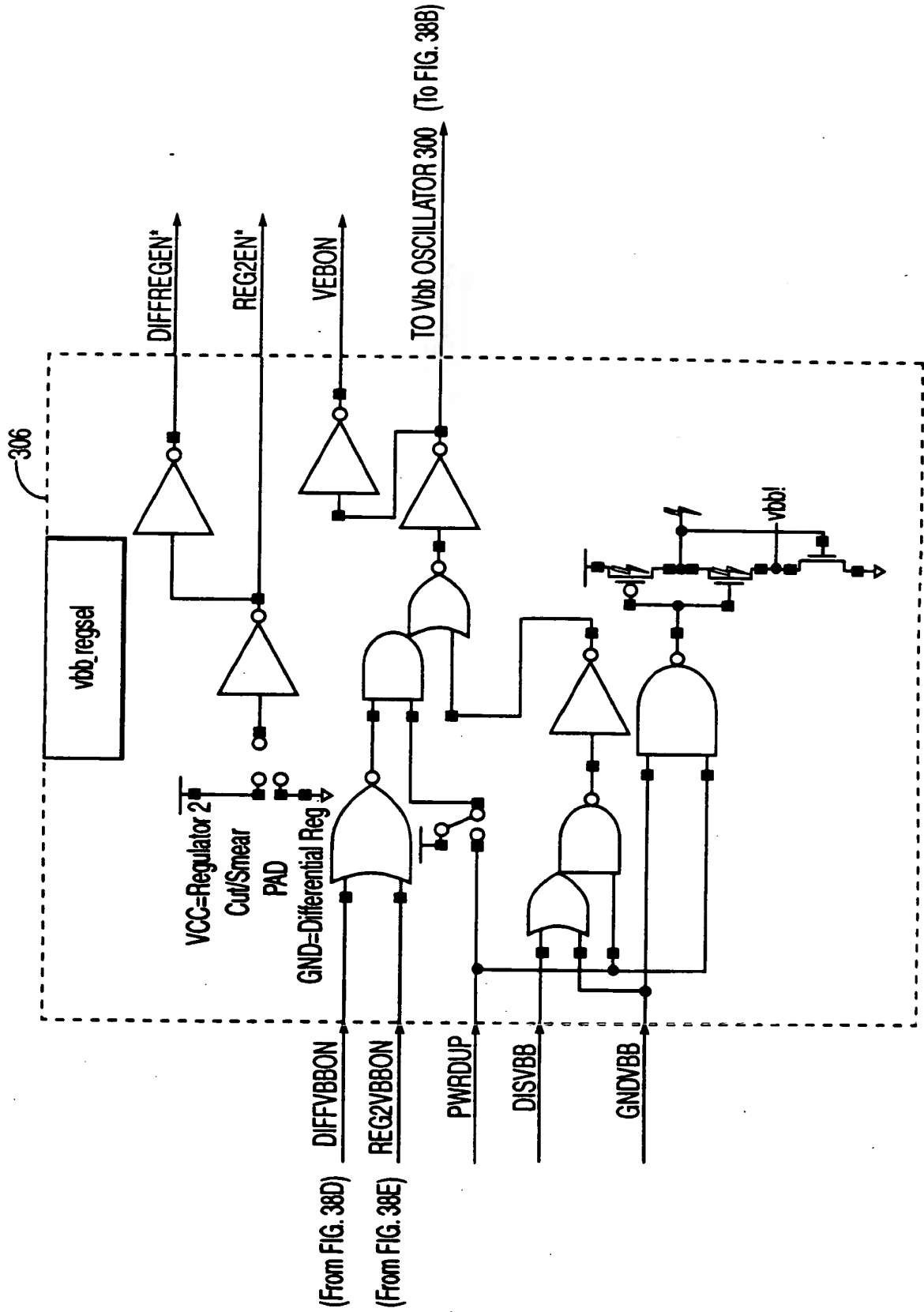


FIG. 38C

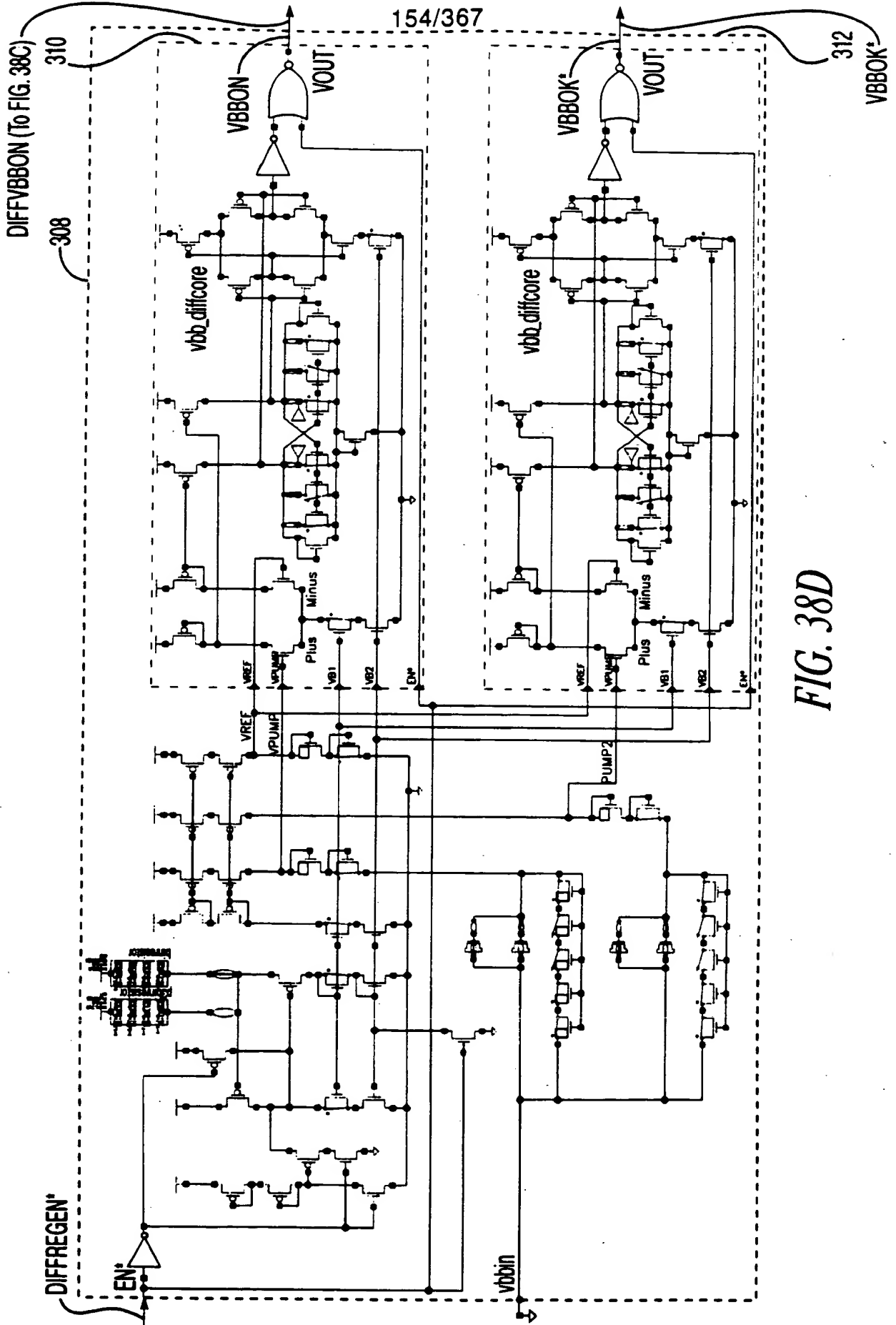


FIG. 38D

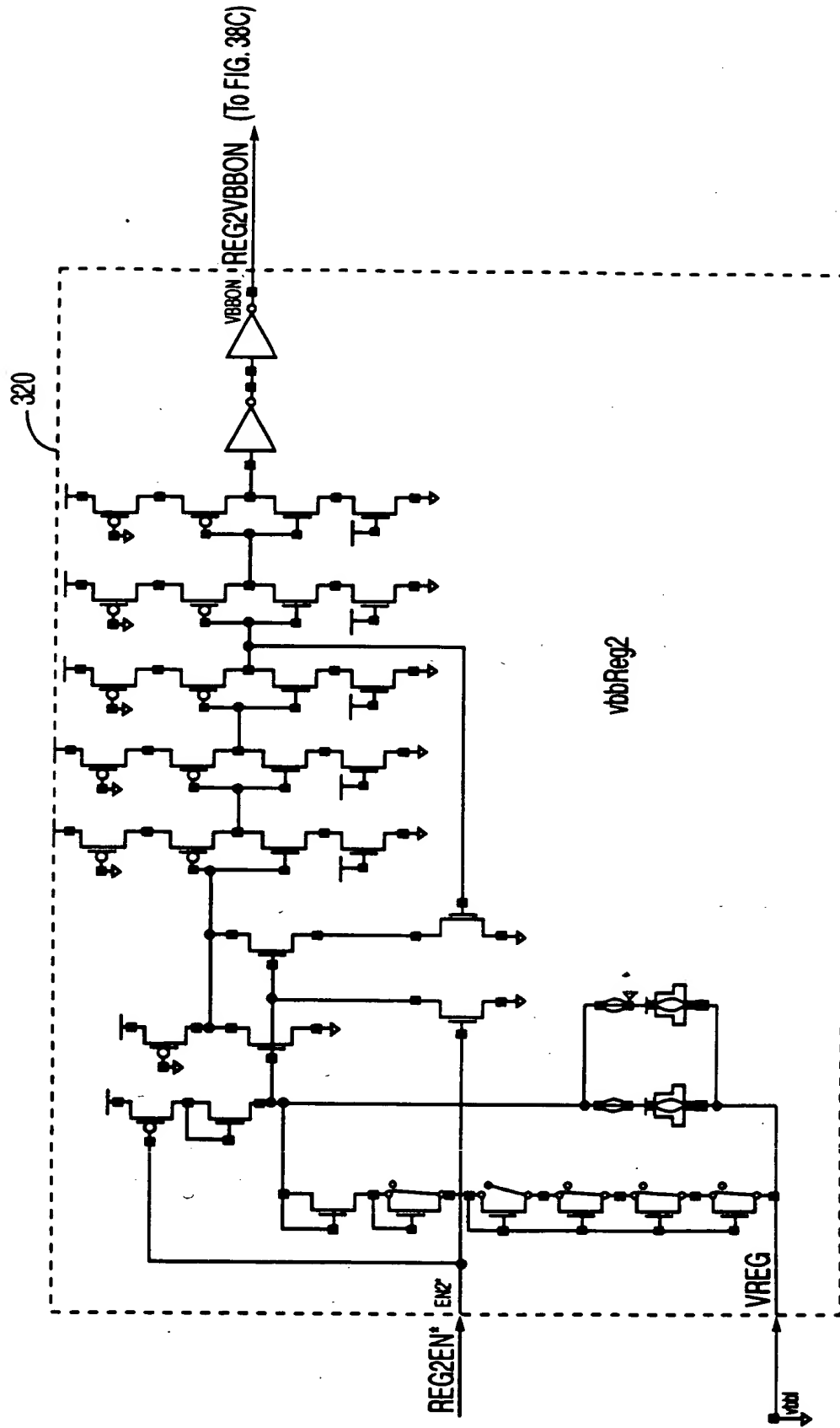


FIG. 38E

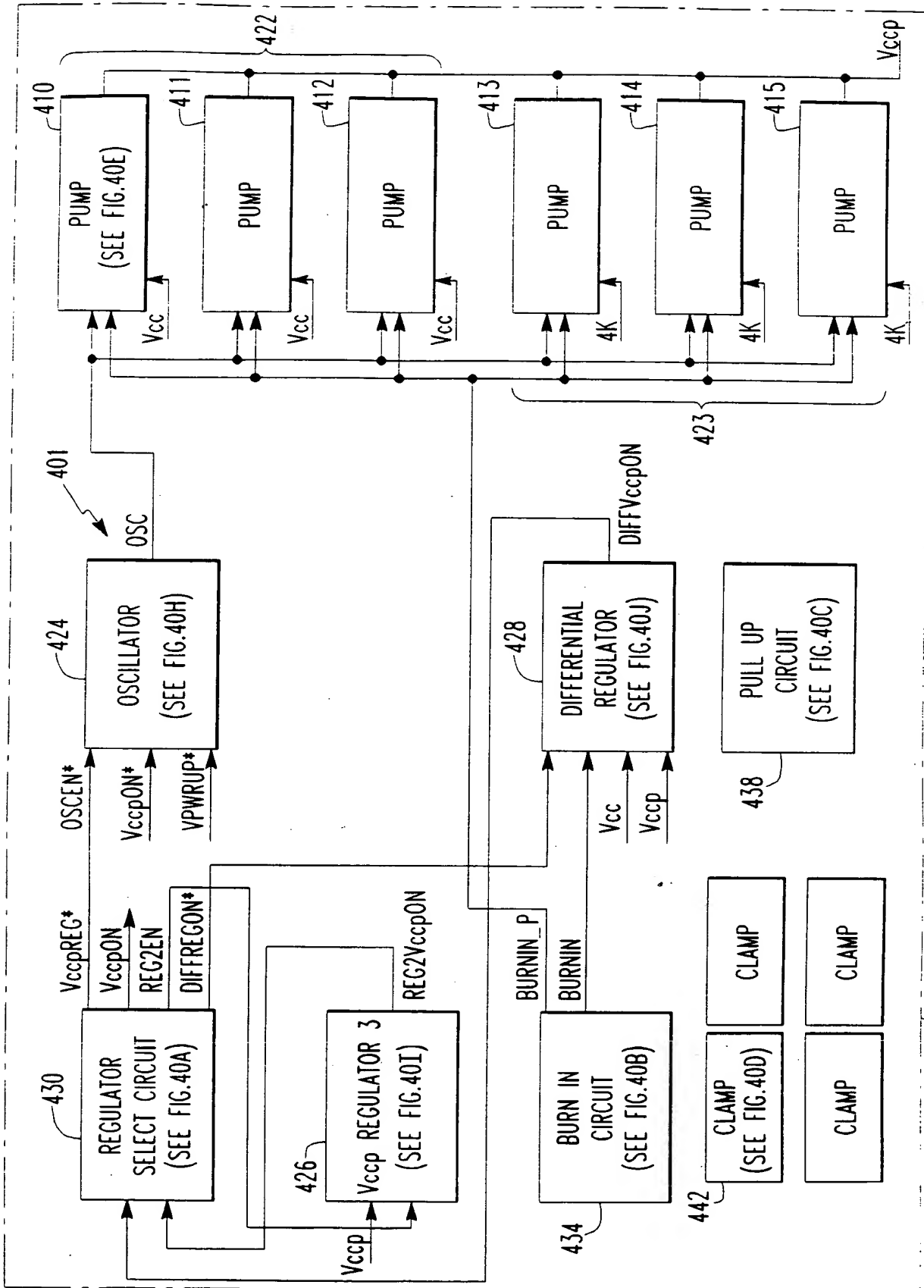


FIG. 39

FIG. 40B

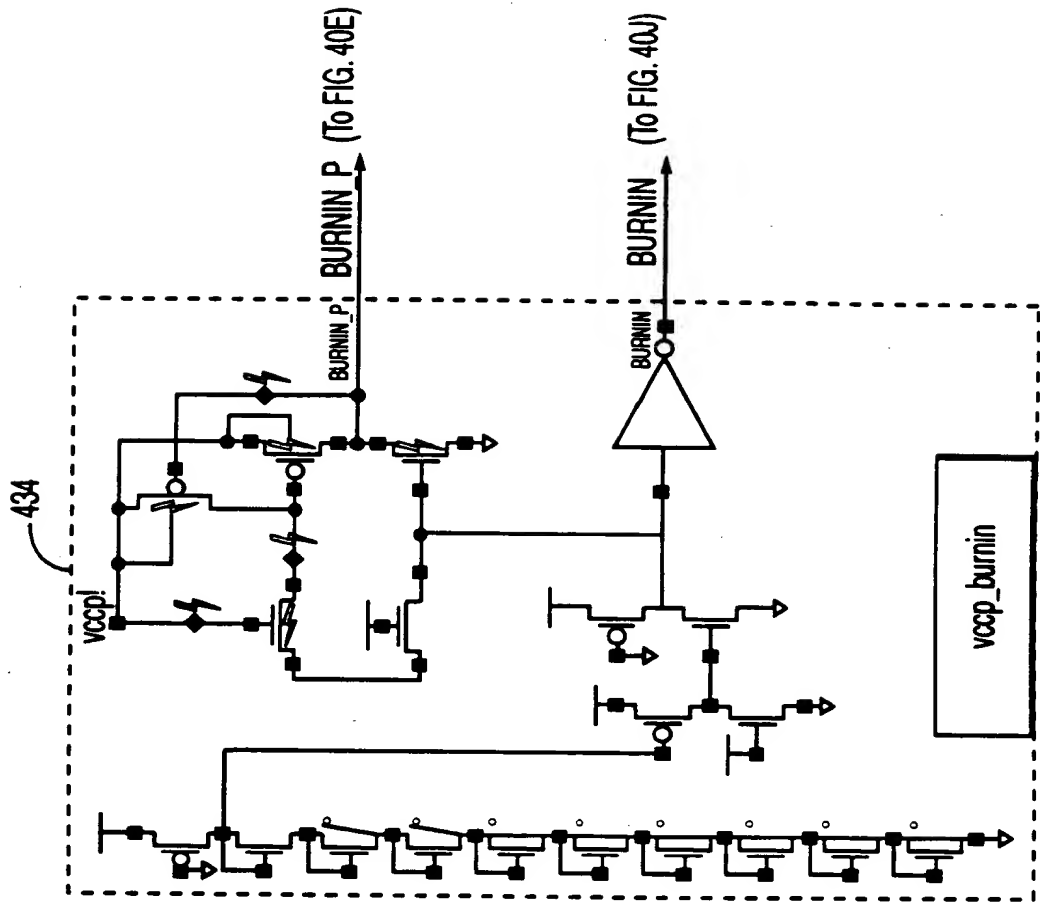


FIG. 40B

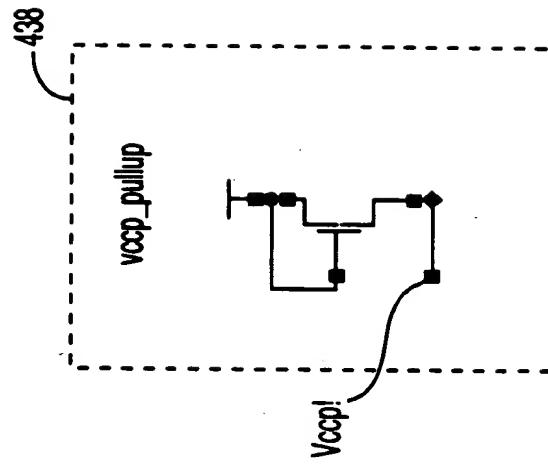


FIG. 40C

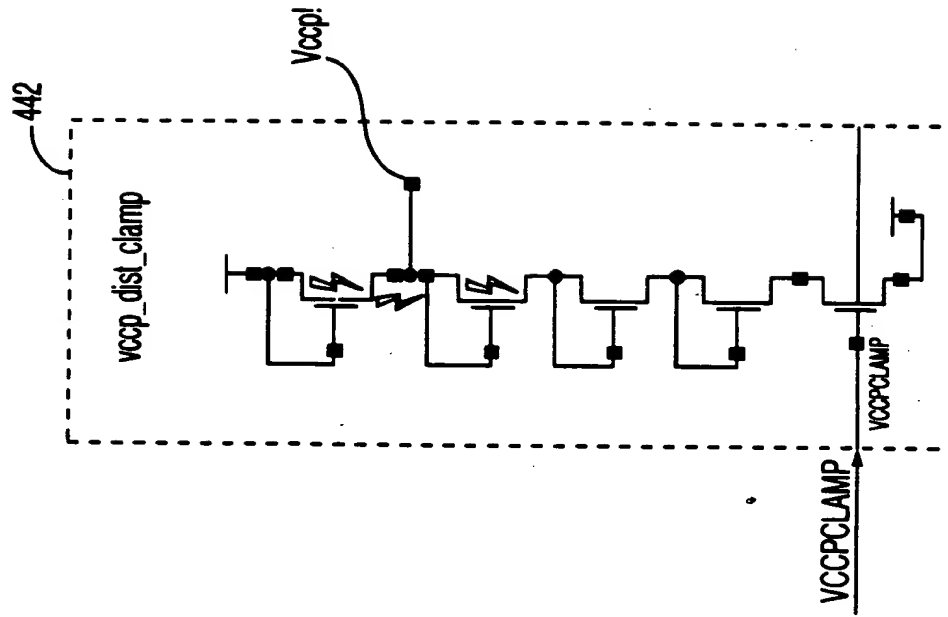
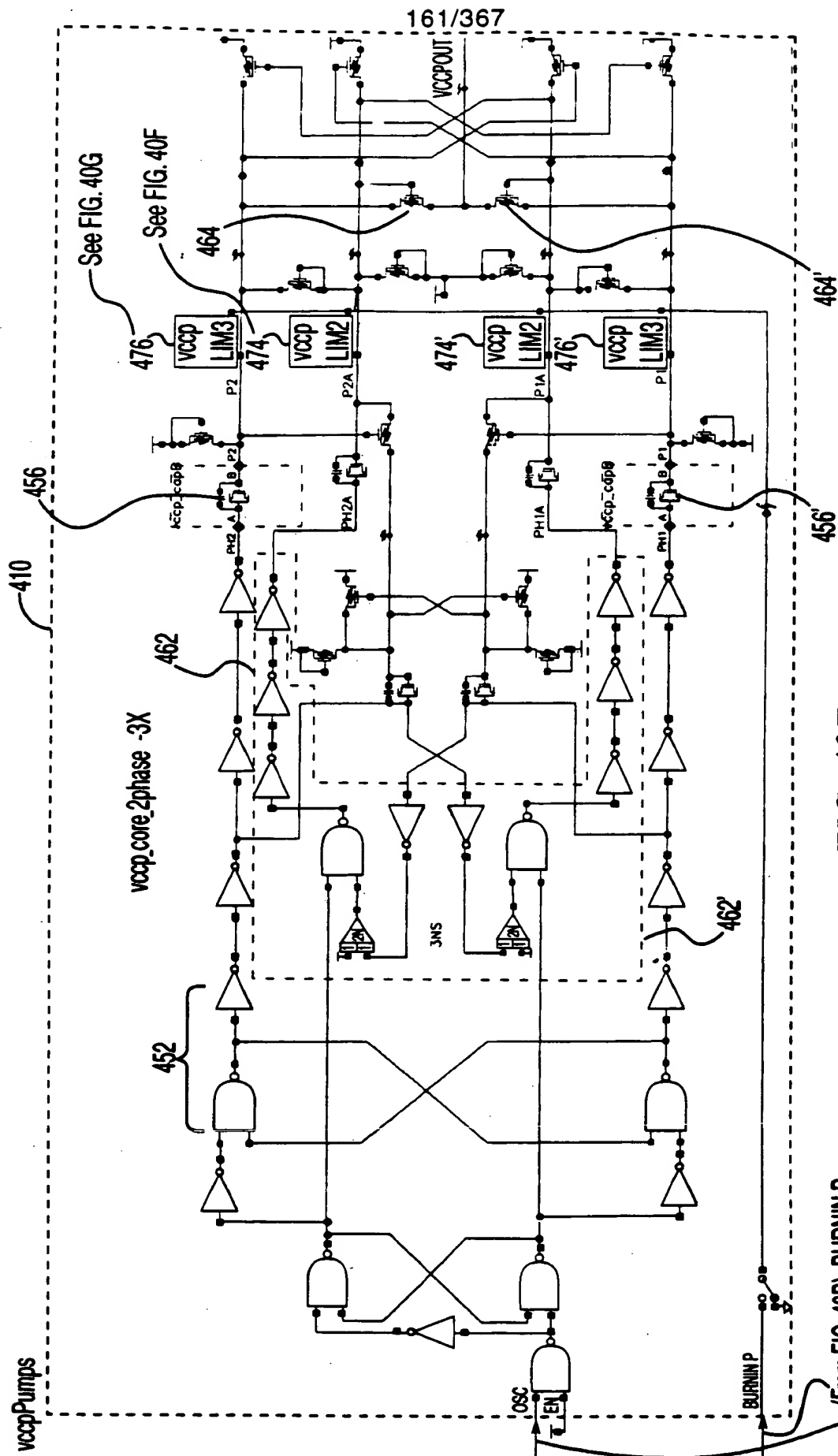


FIG. 40D



(From FIG. 40B) BURNIN P

(From FIG. 40H) VCCPOSC

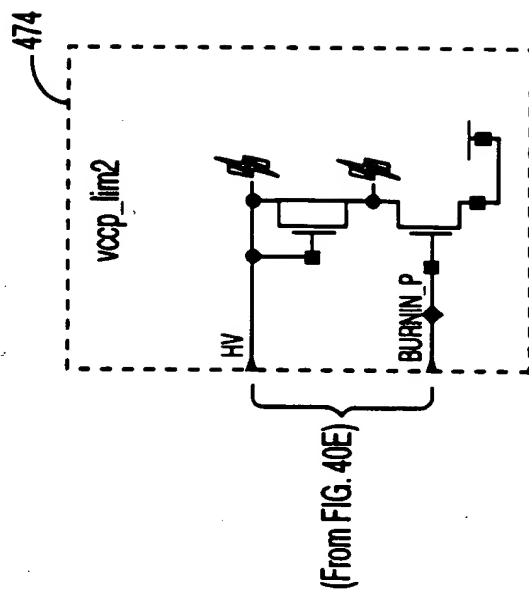


FIG. 40F

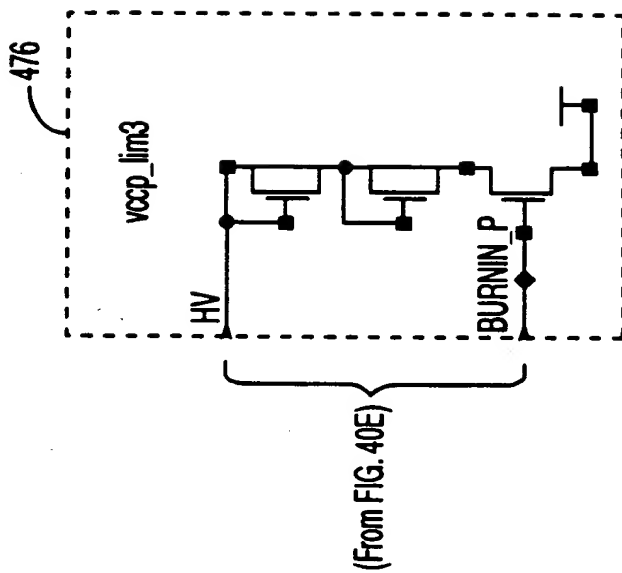
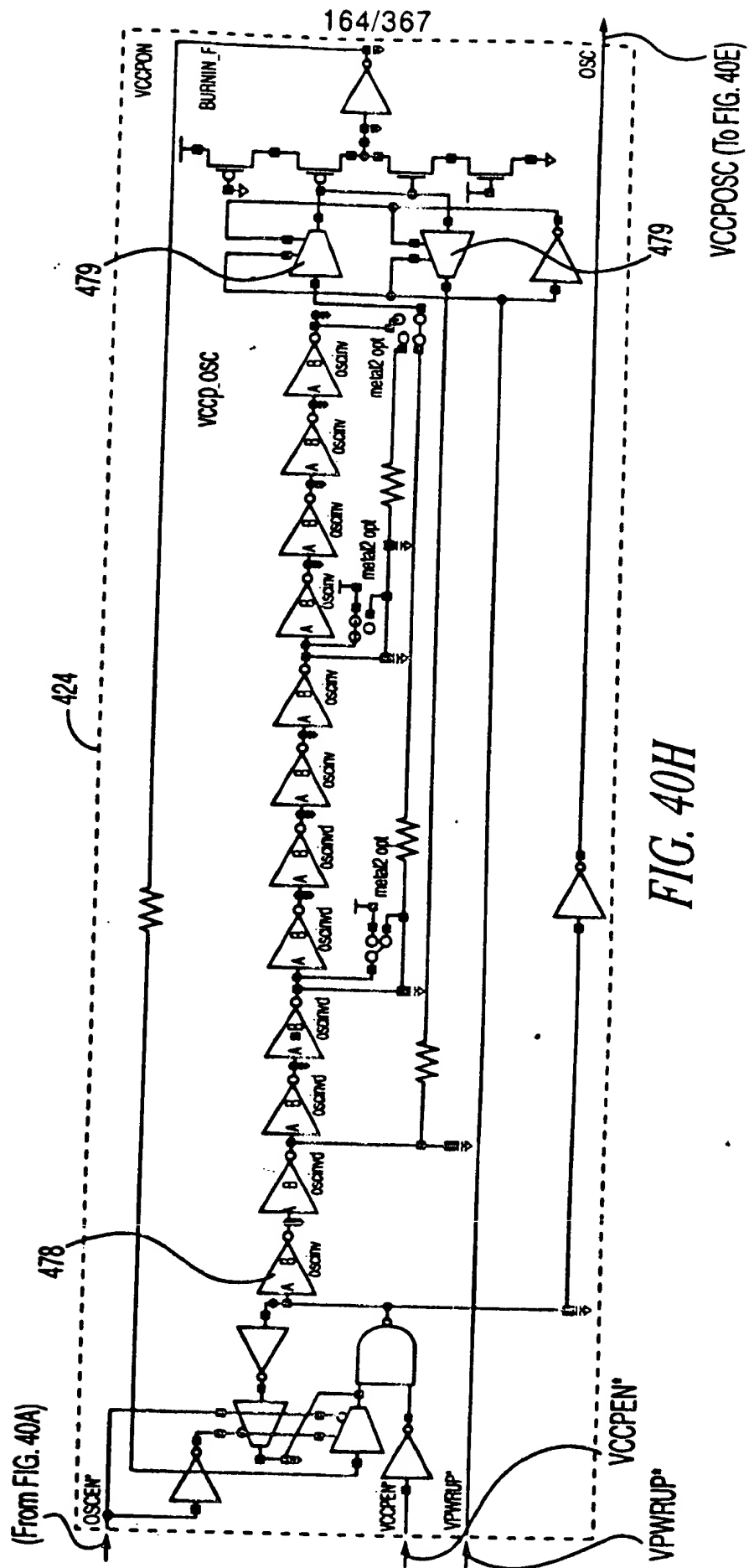


FIG. 40G



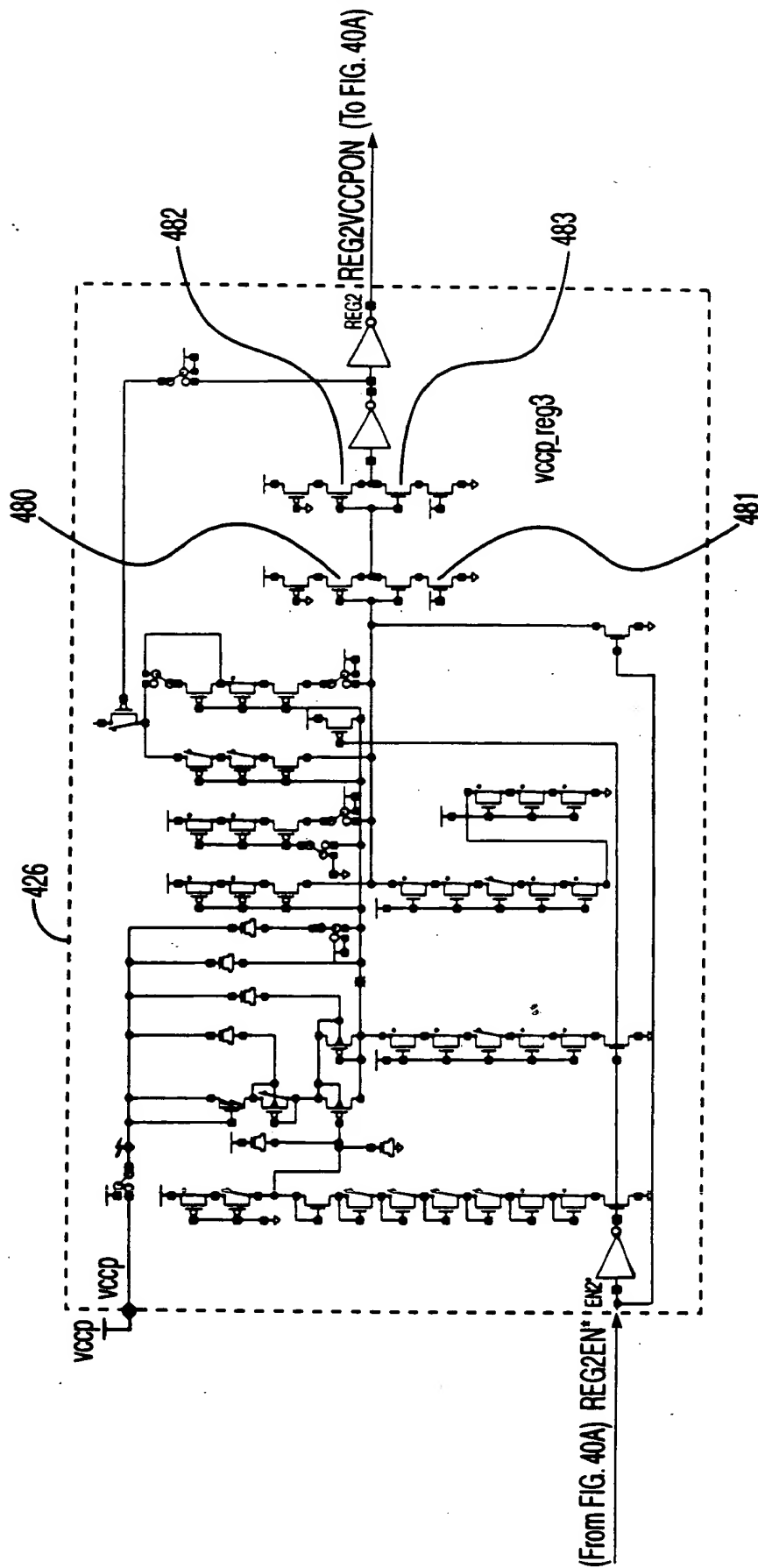


FIG. 401

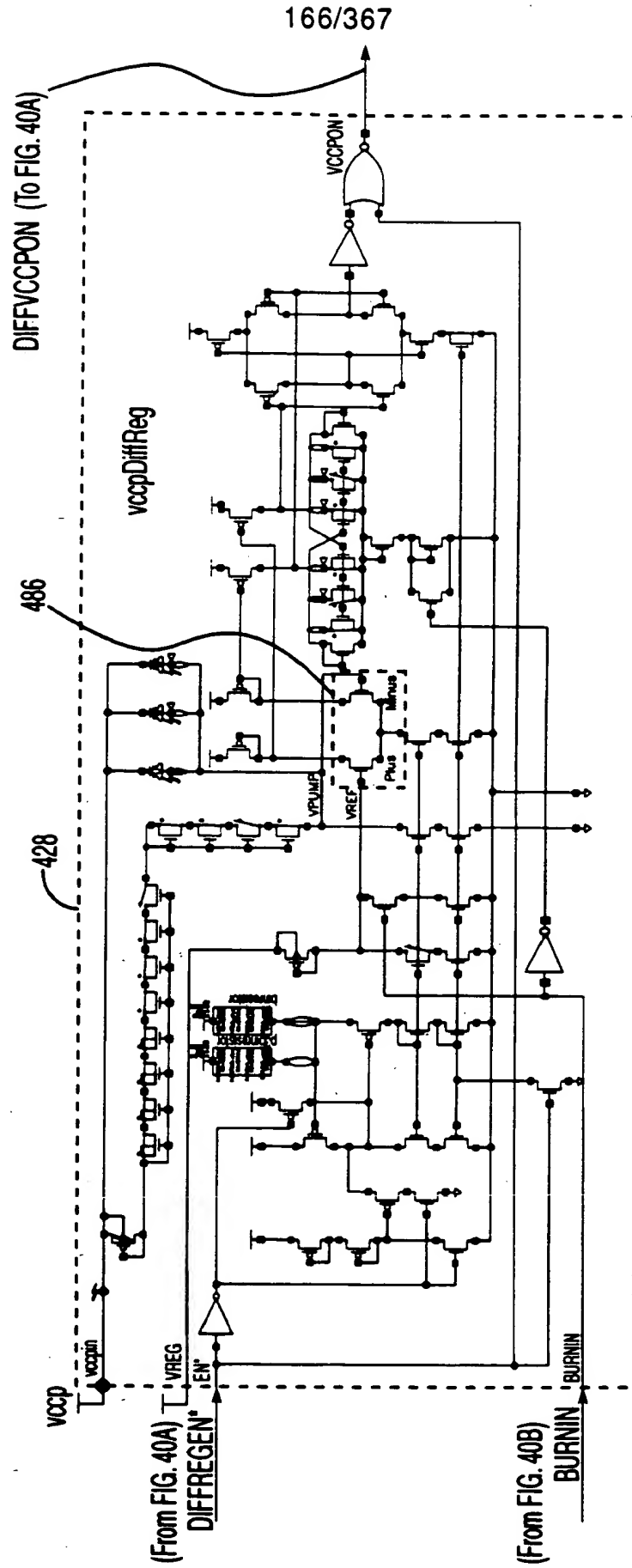
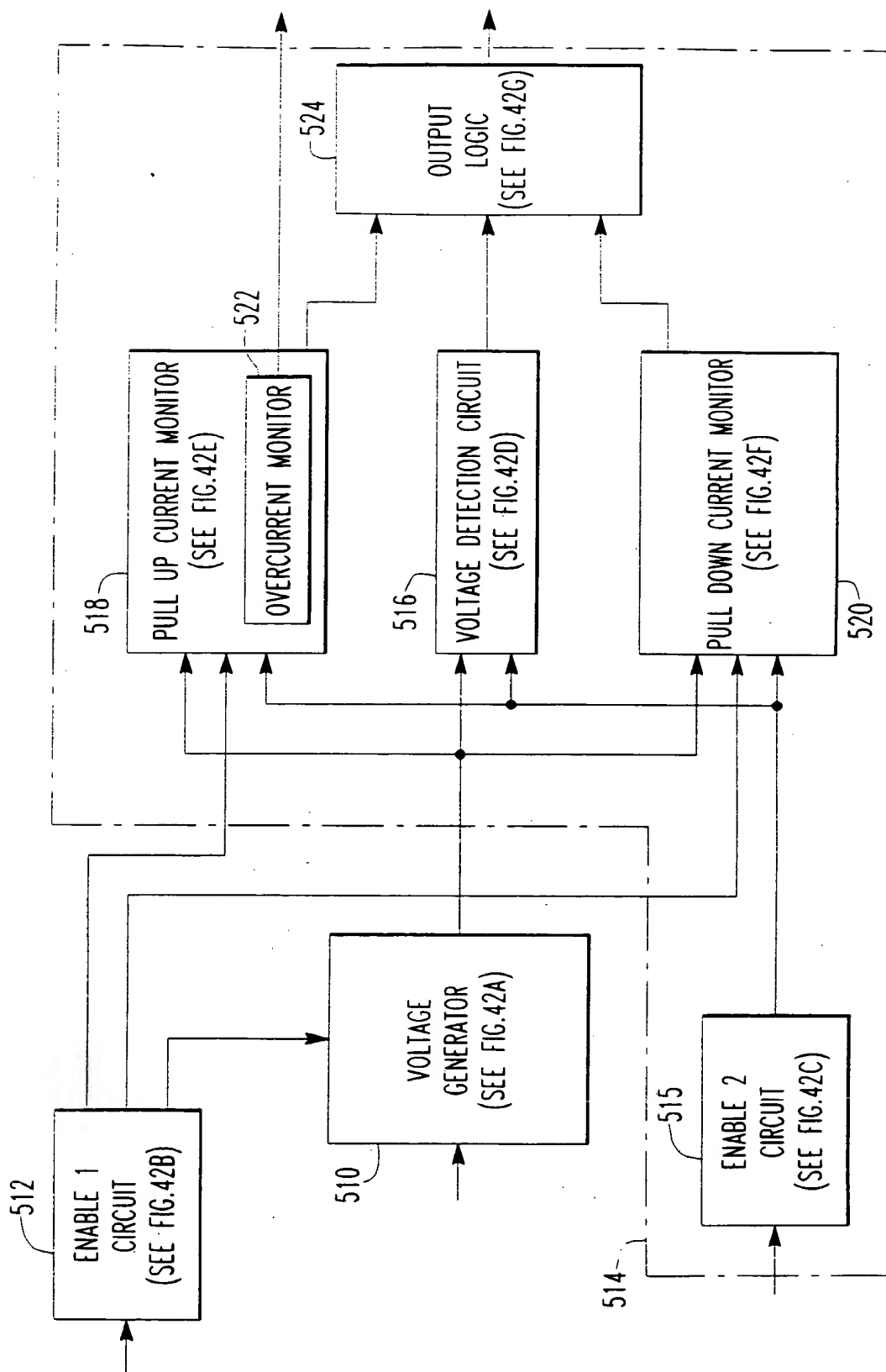


FIG. 40J



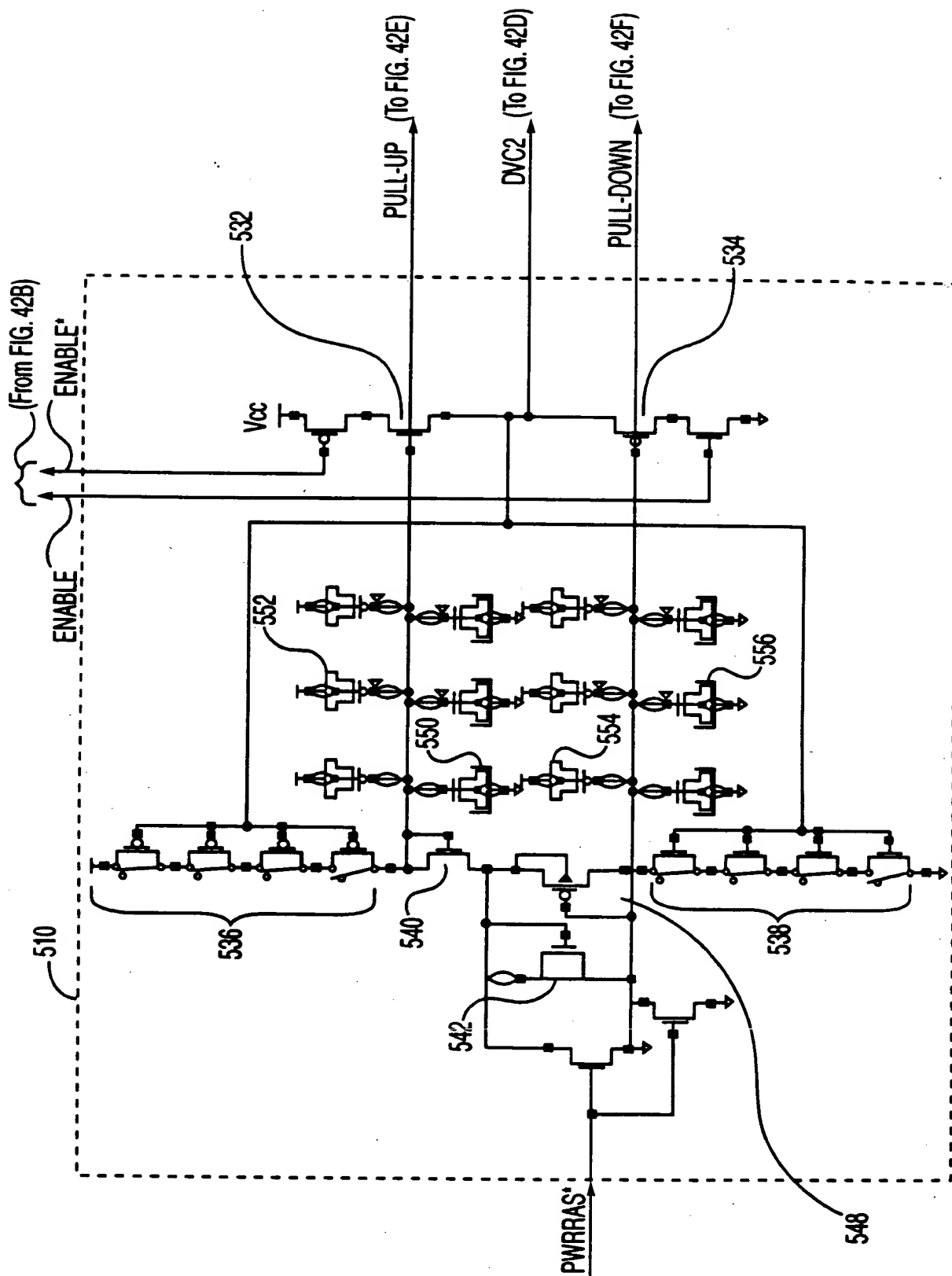


FIG. 42A

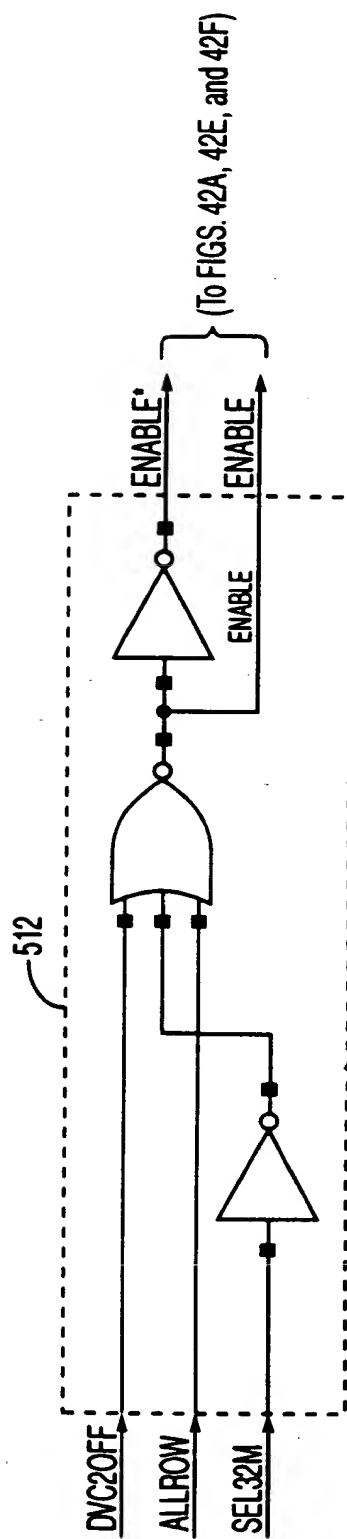


FIG. 42B

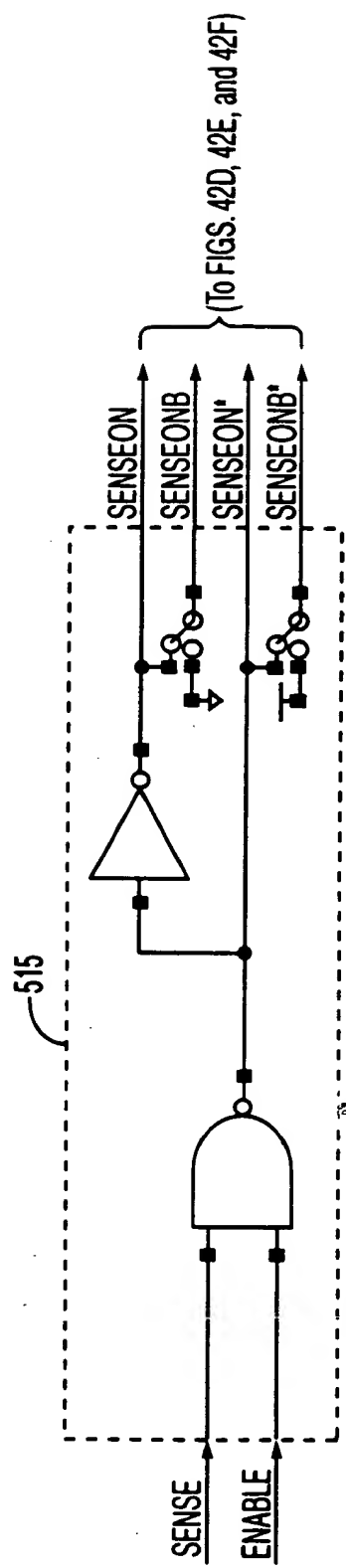


FIG. 42C

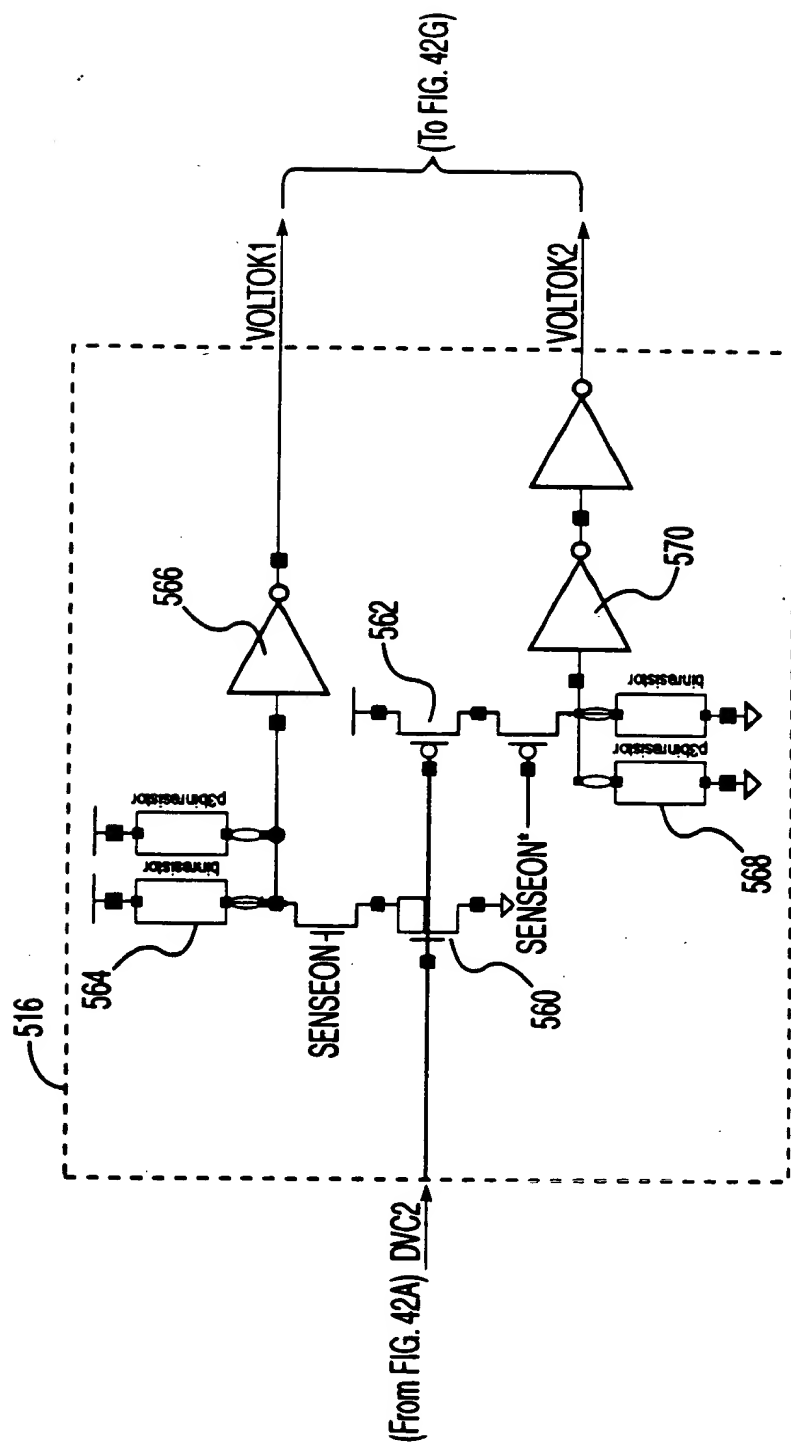


FIG. 42D

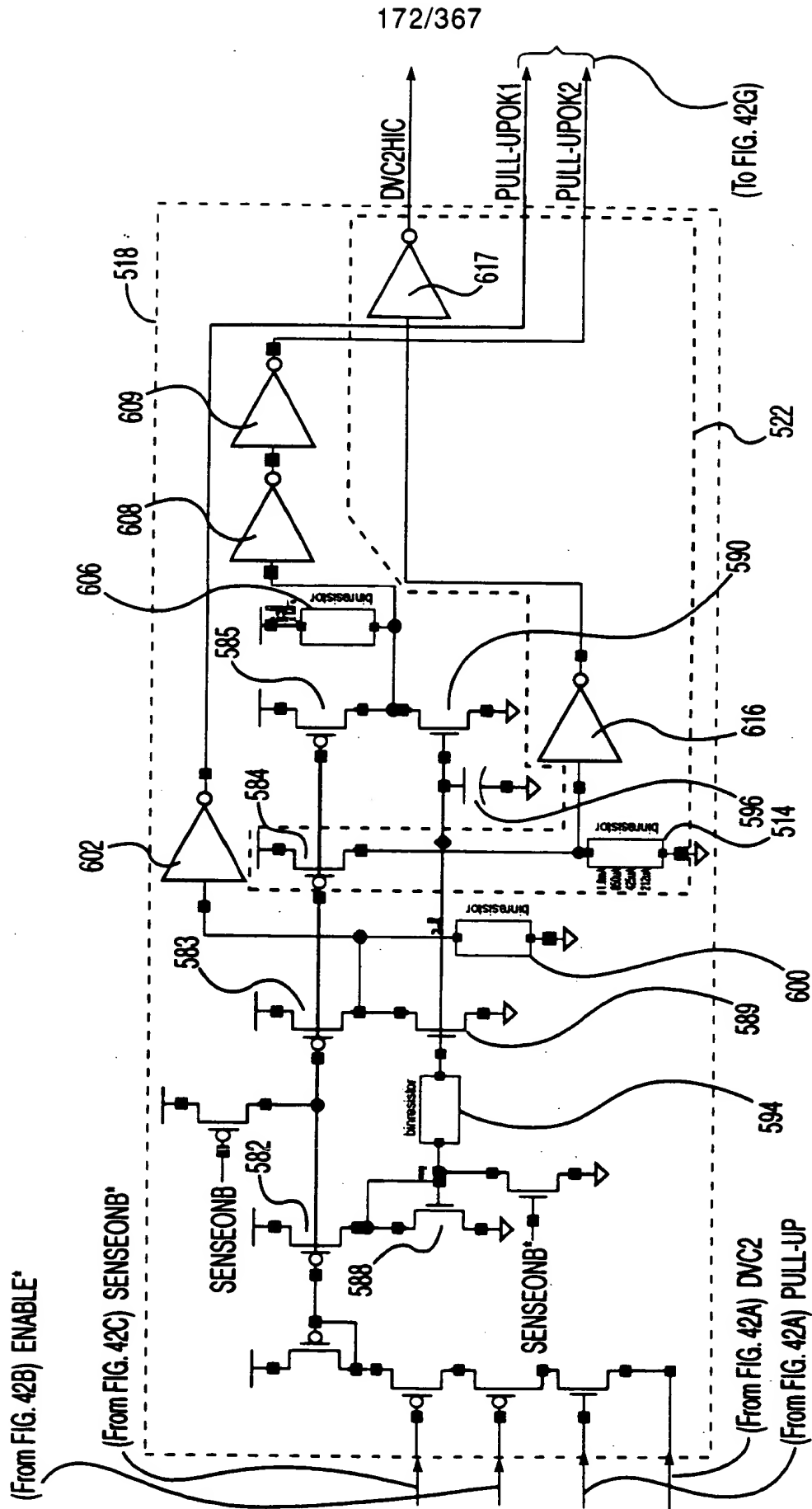


FIG. 42E

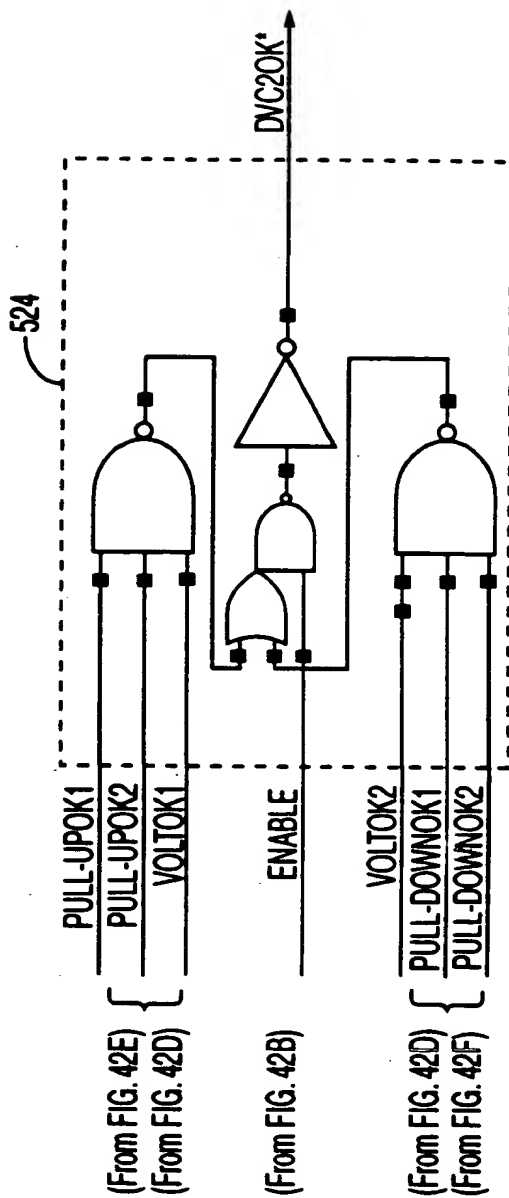


FIG. 42G

23

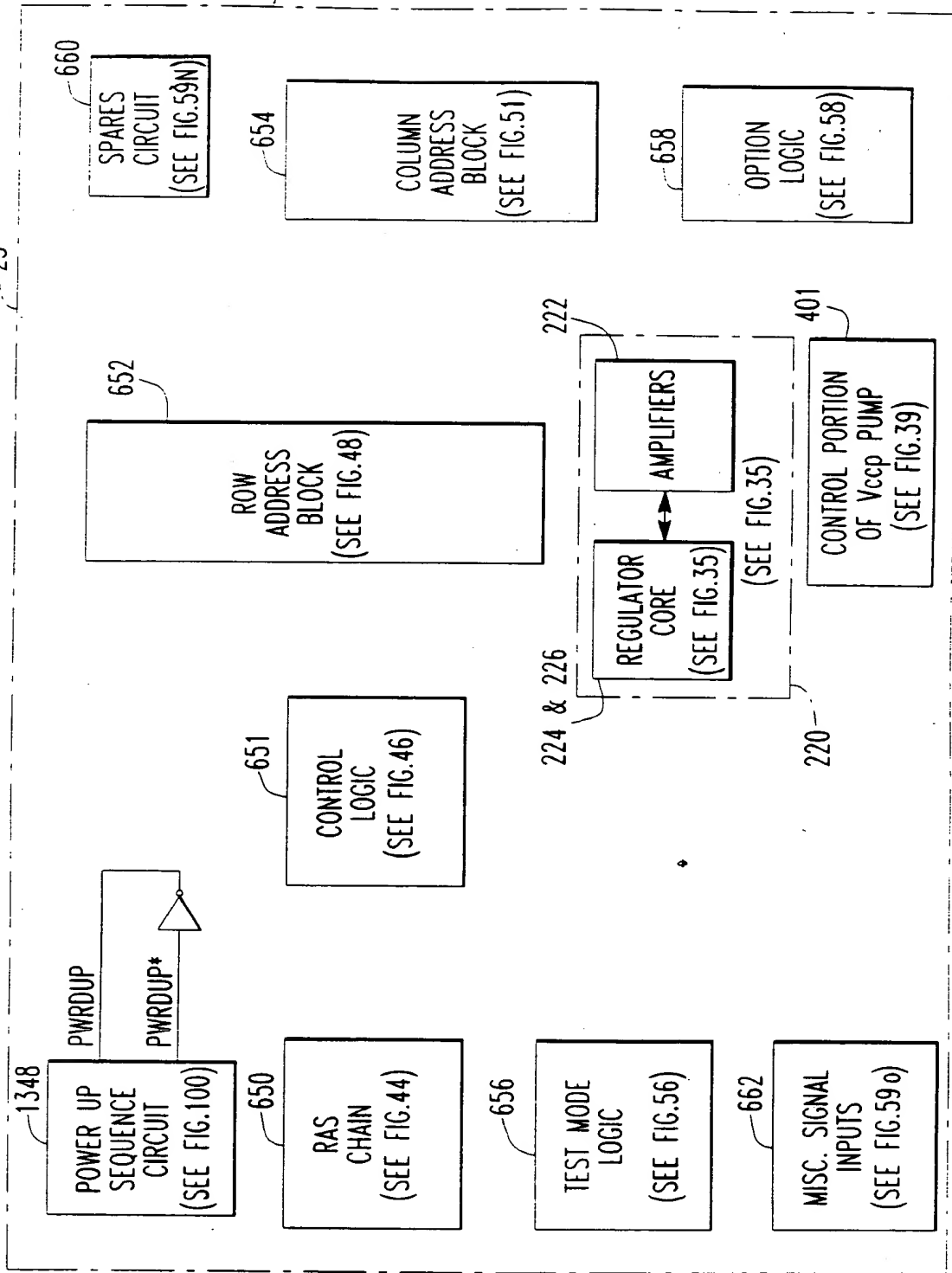


FIG. 43

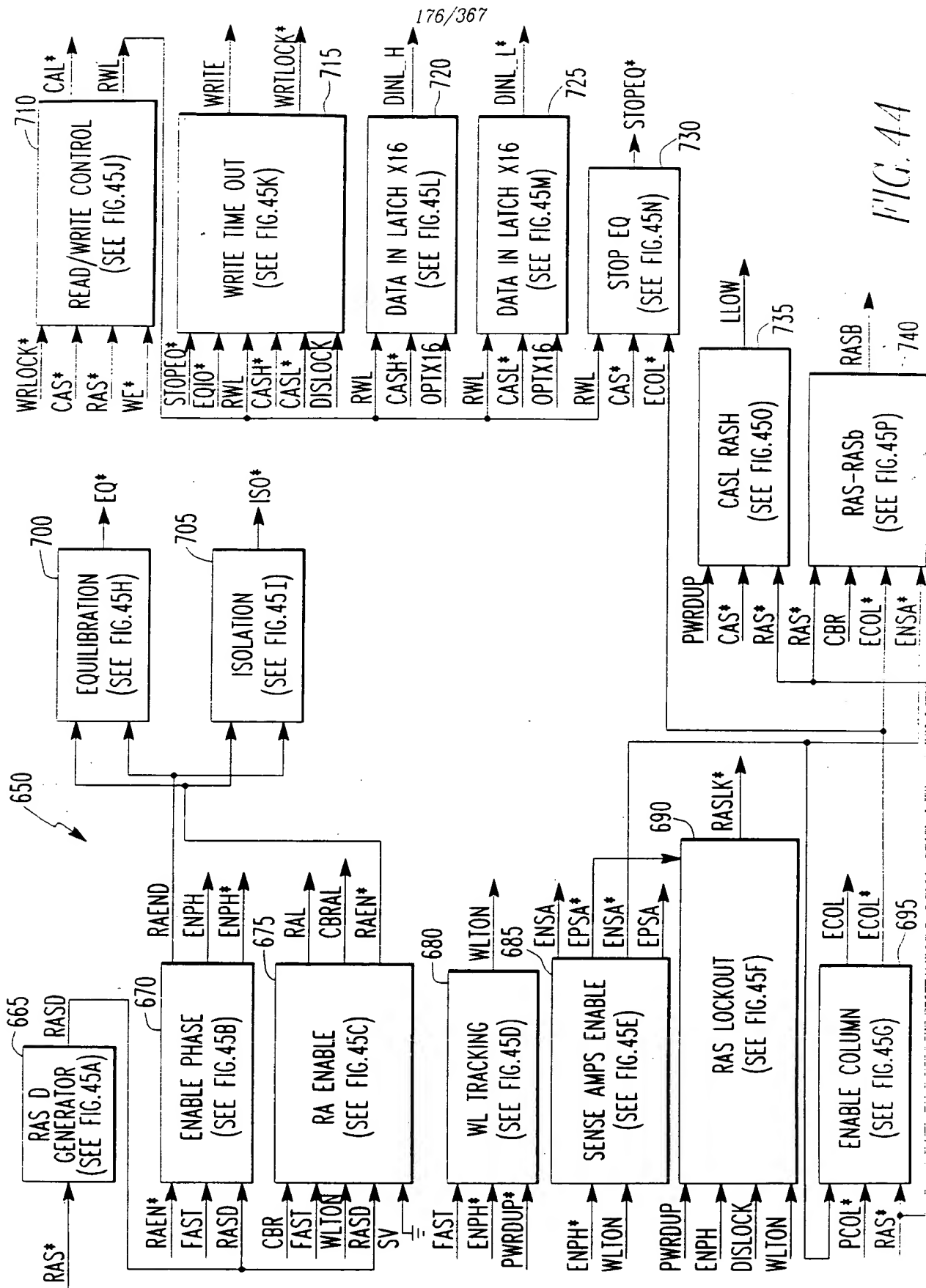


FIG. 44

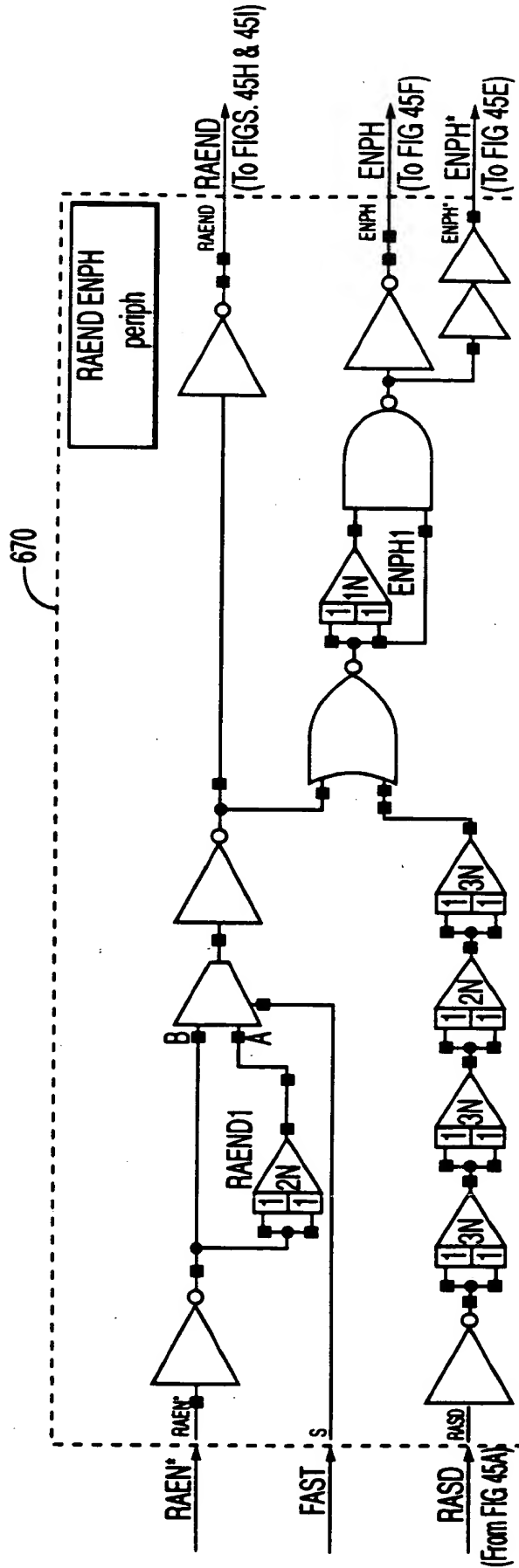
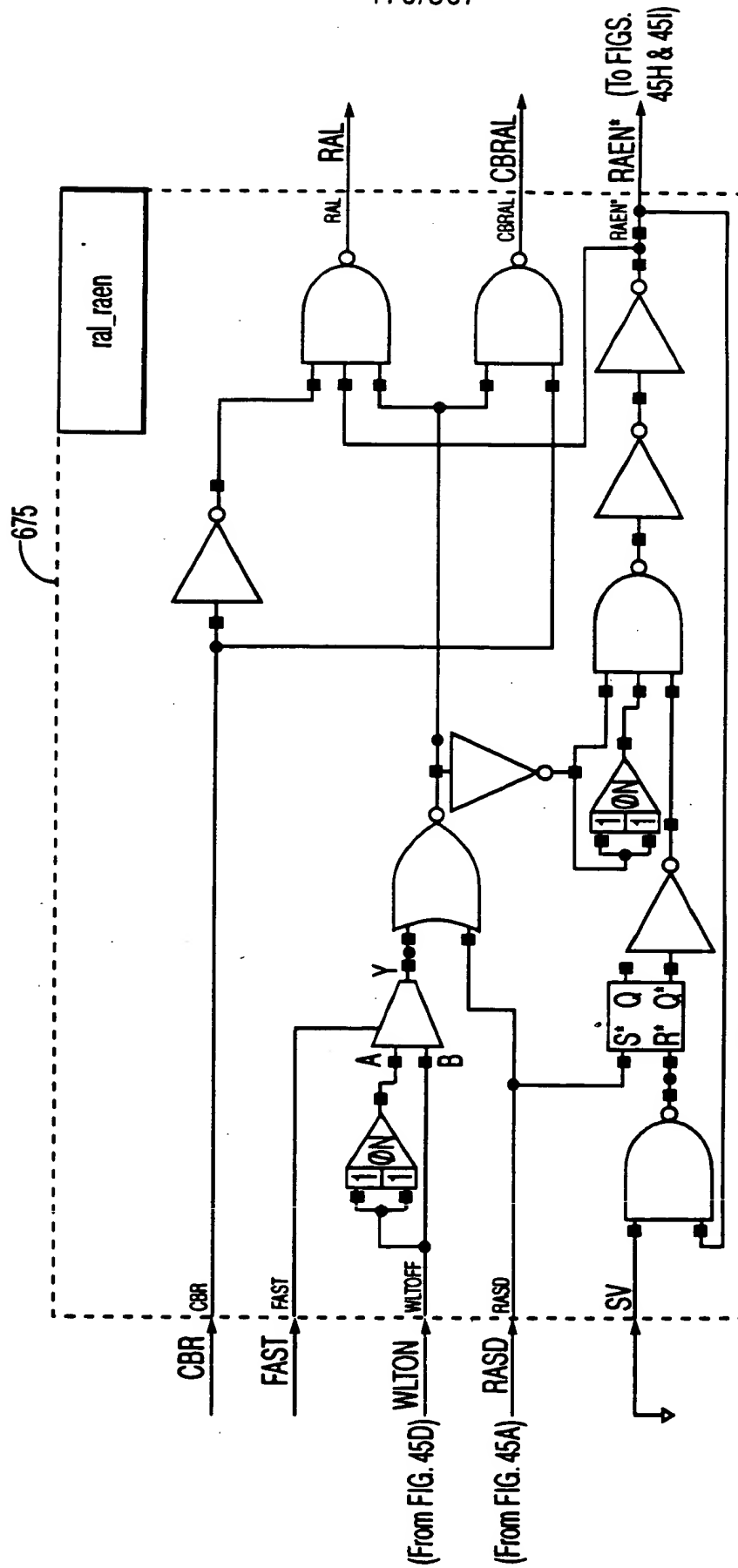


FIG. 45B



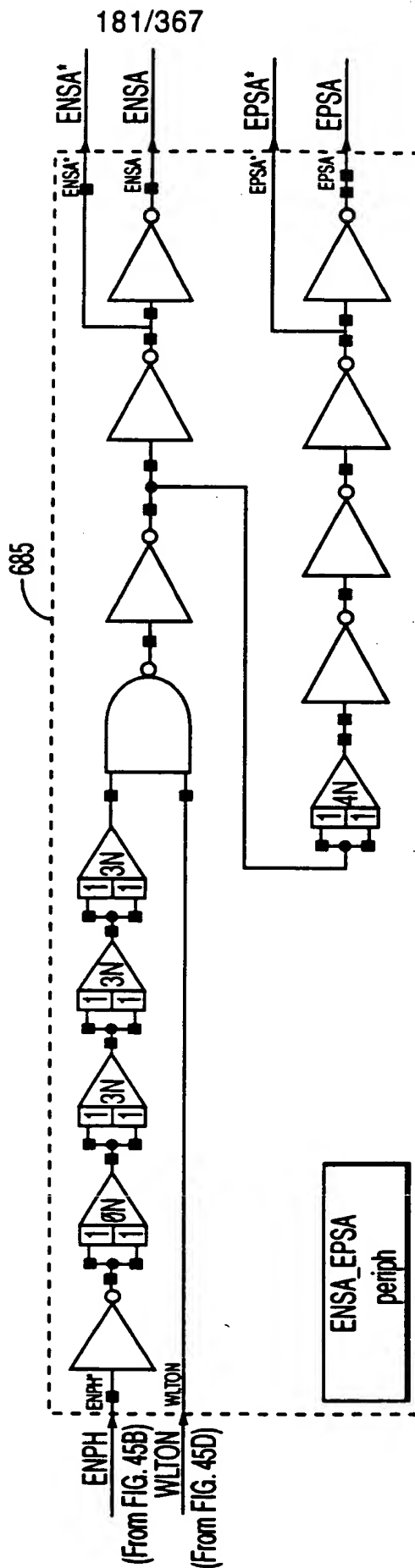


FIG. 45E

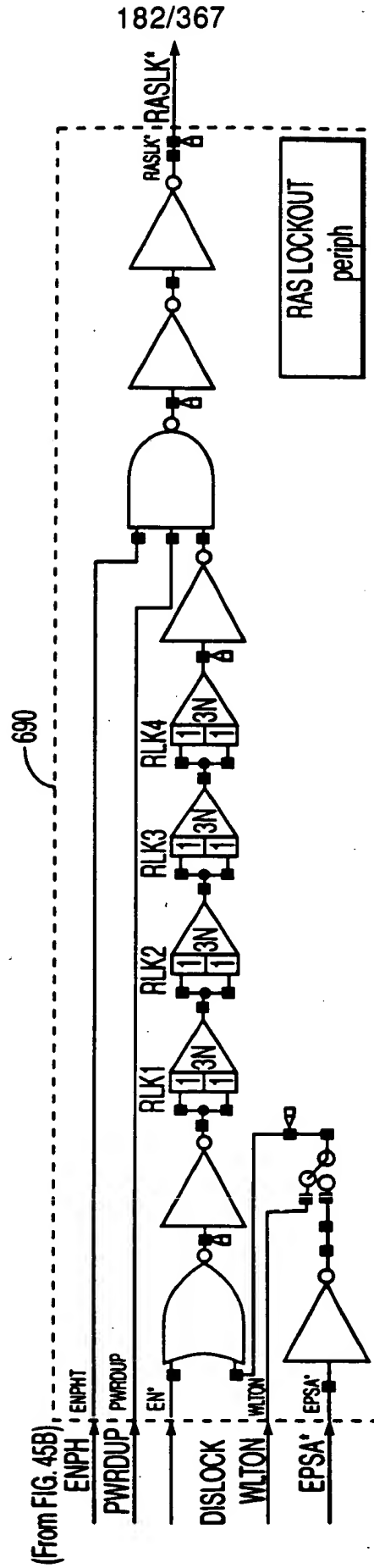


FIG. 45F

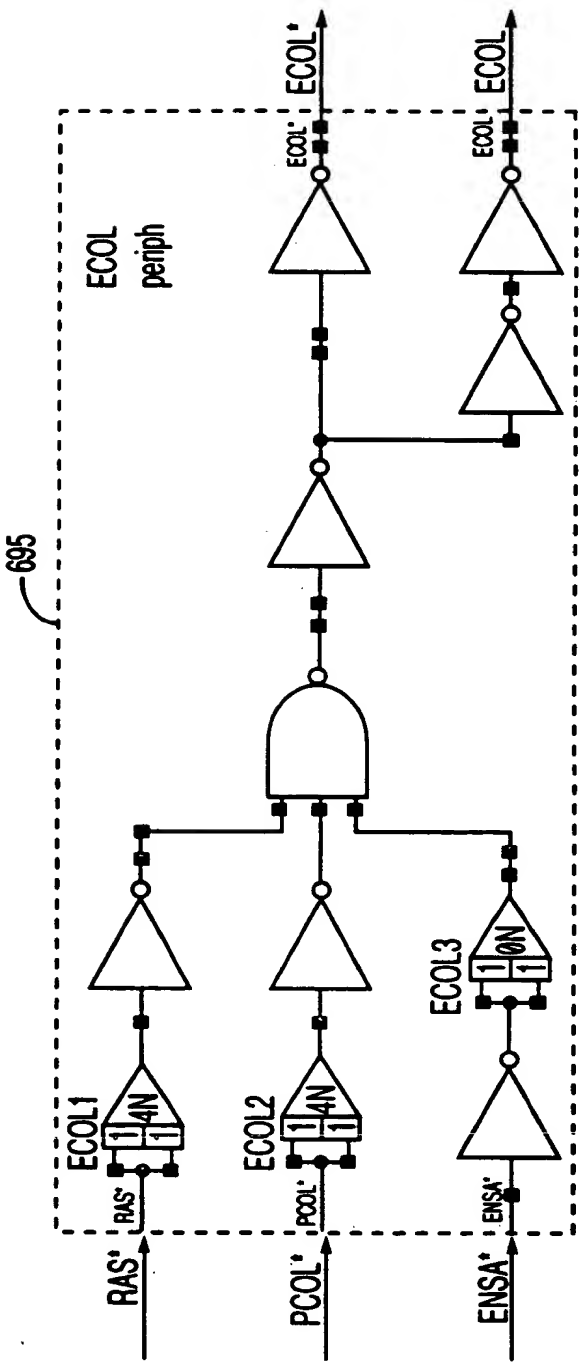


FIG. 45G

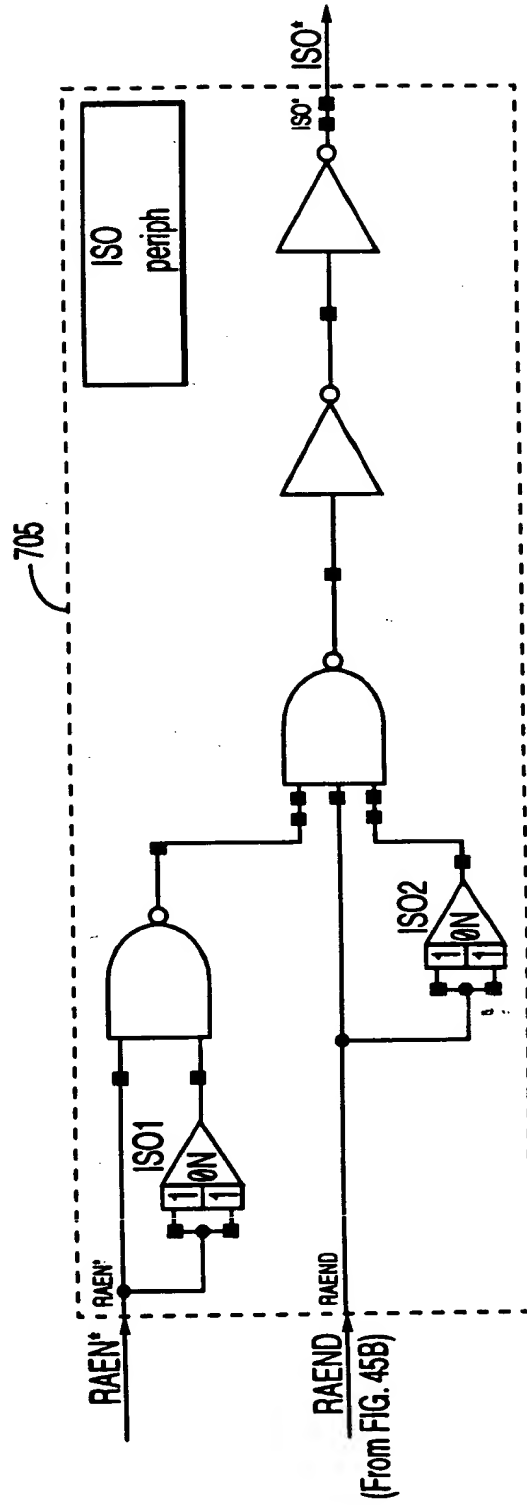


FIG. 45I



715

187/367

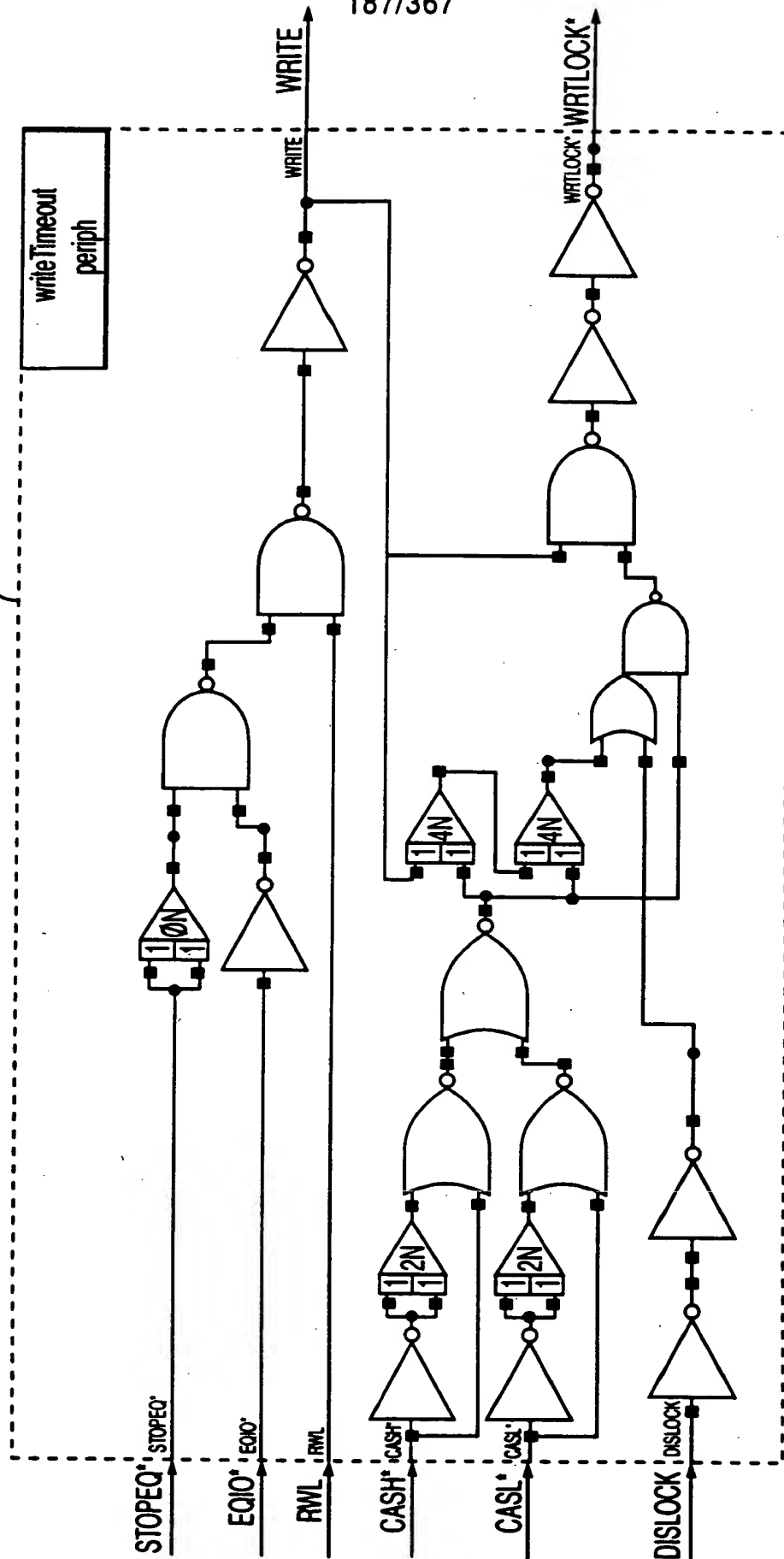


FIG. 45K

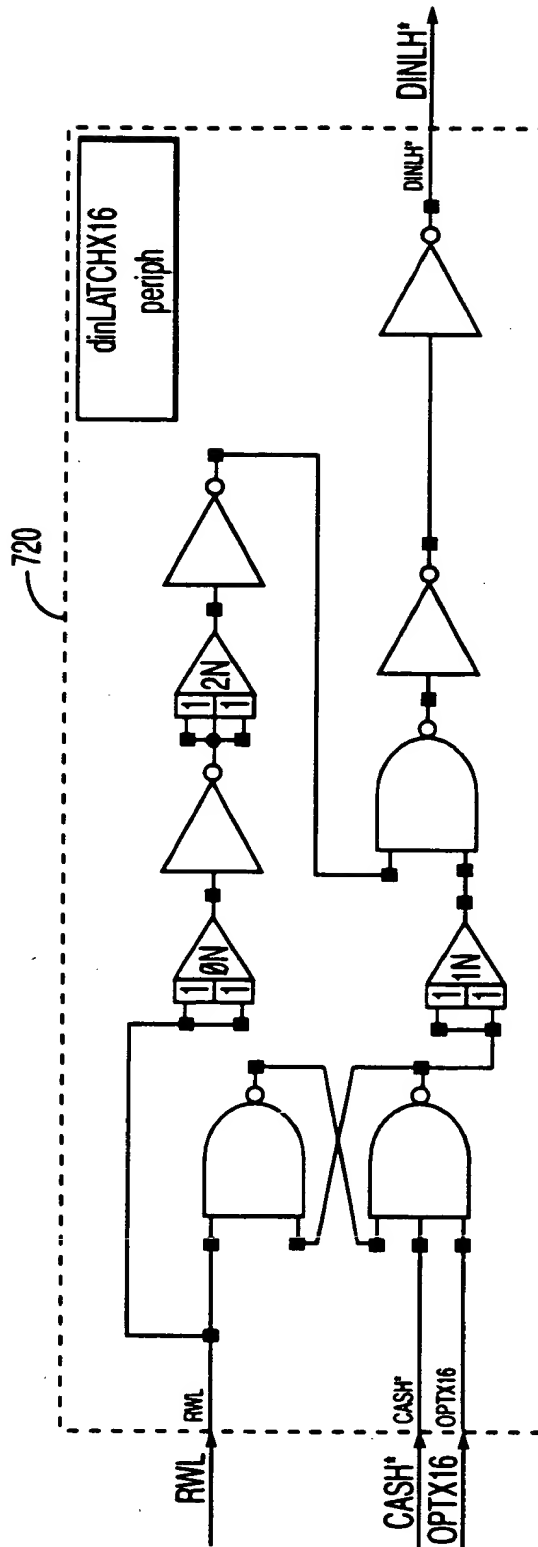


FIG. 45L

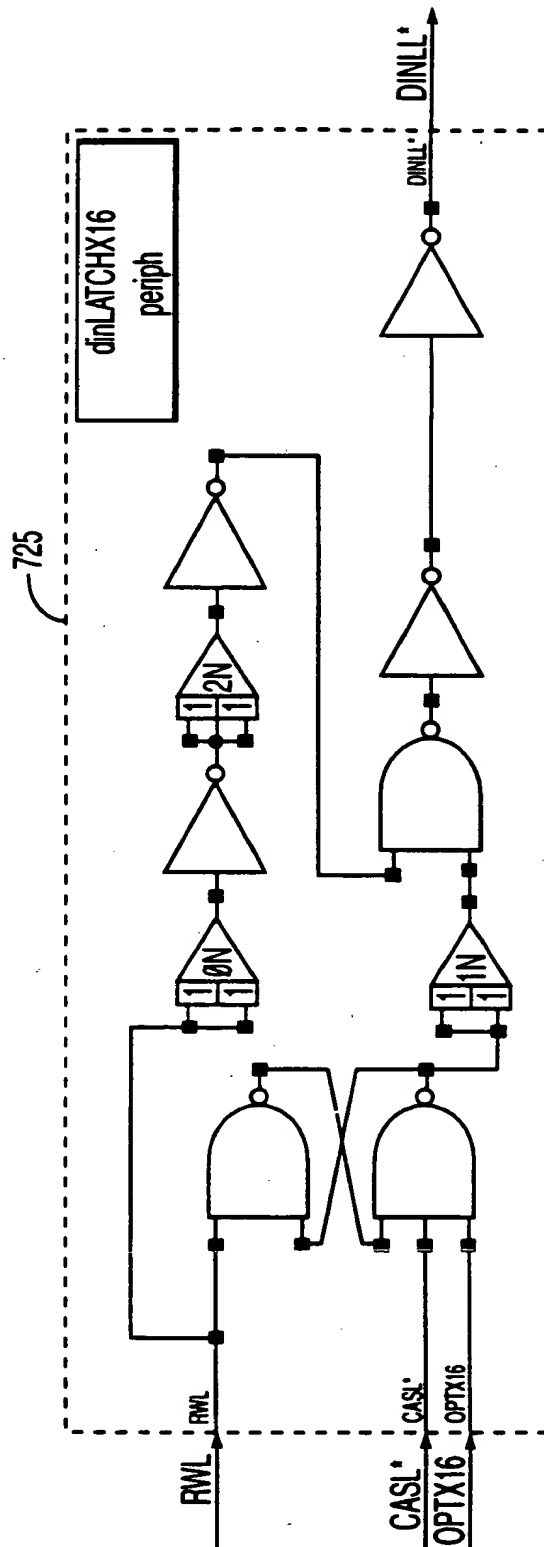


FIG. 45M

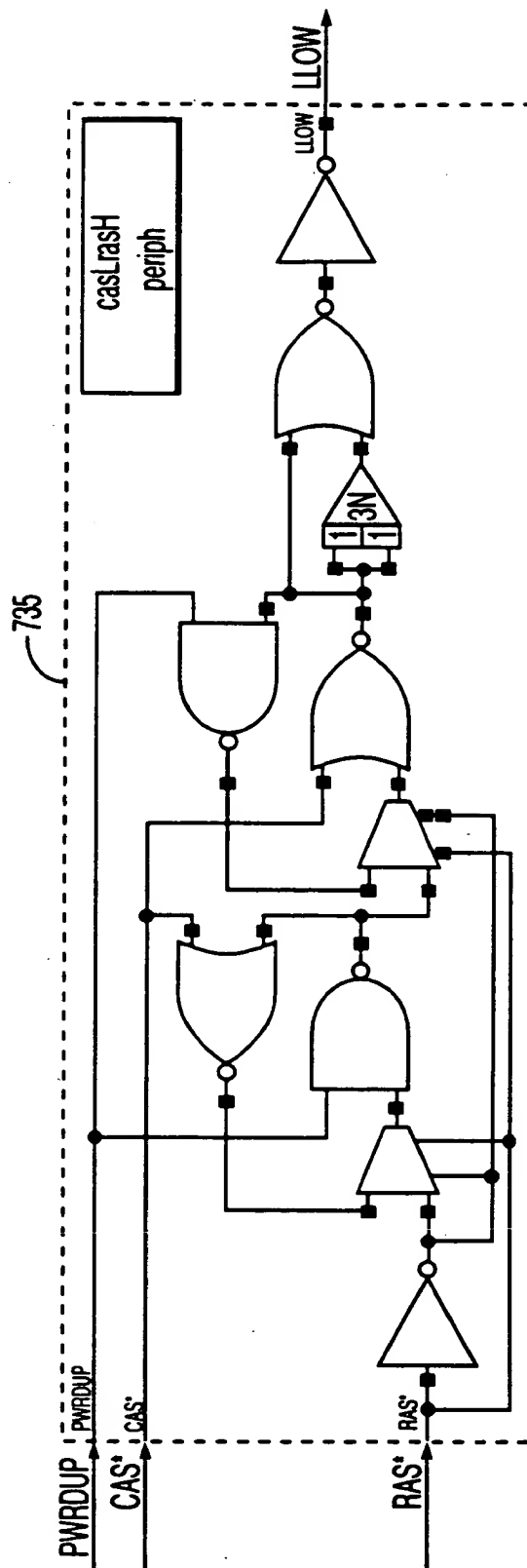


FIG. 450

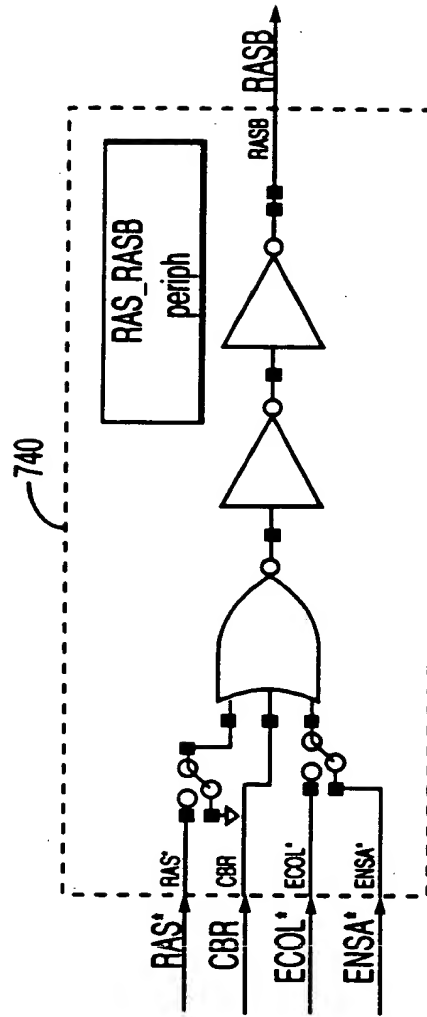


FIG. 45P

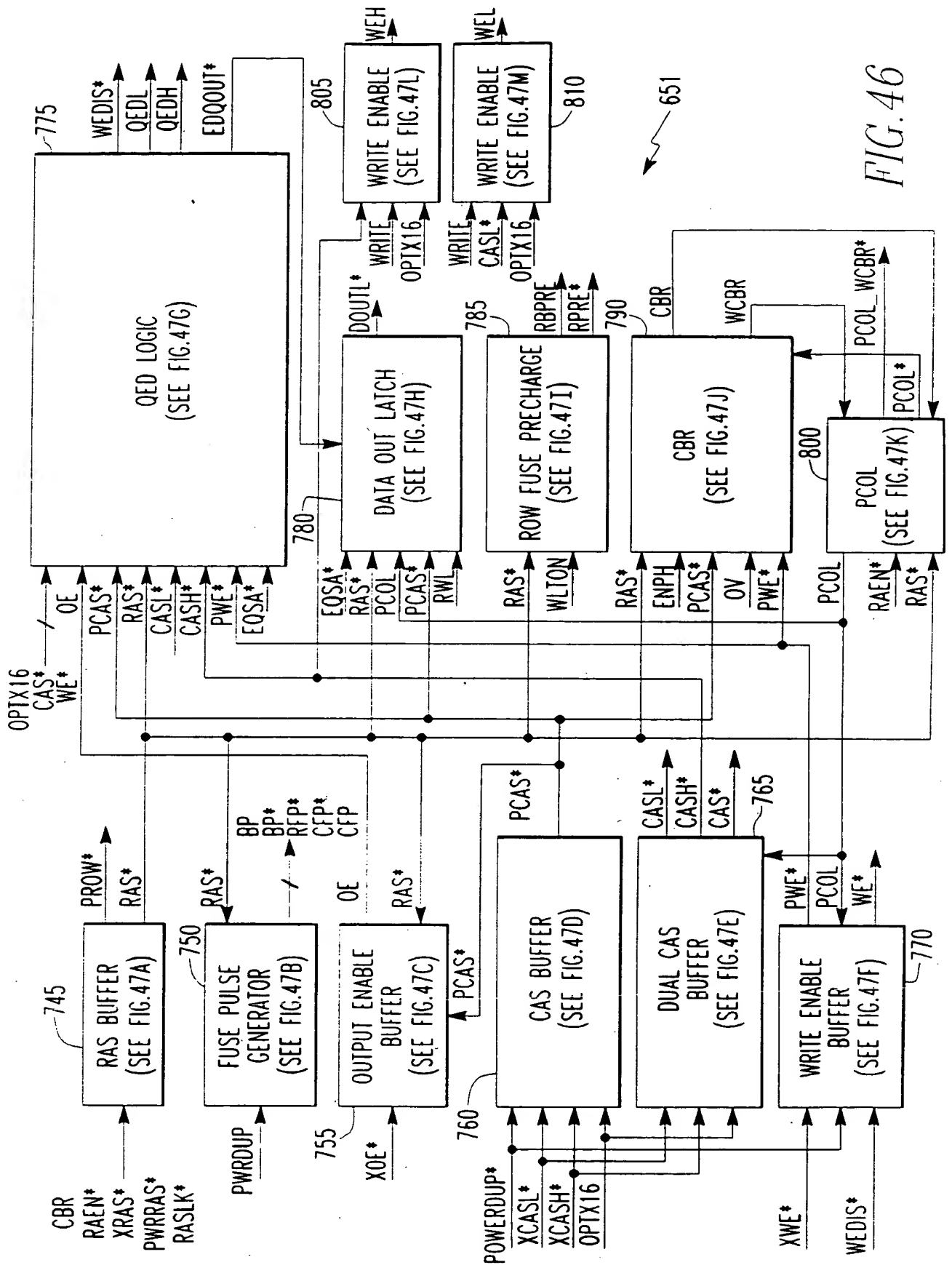


FIG. 46



FIG. 47A

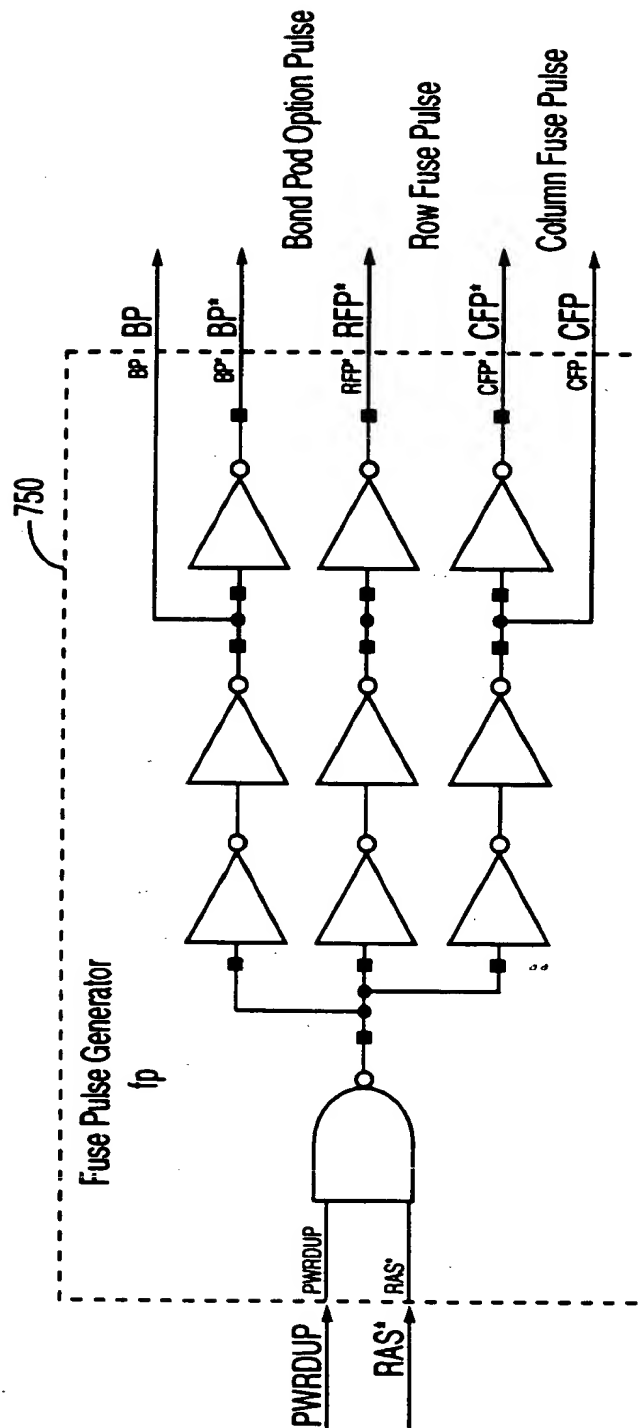
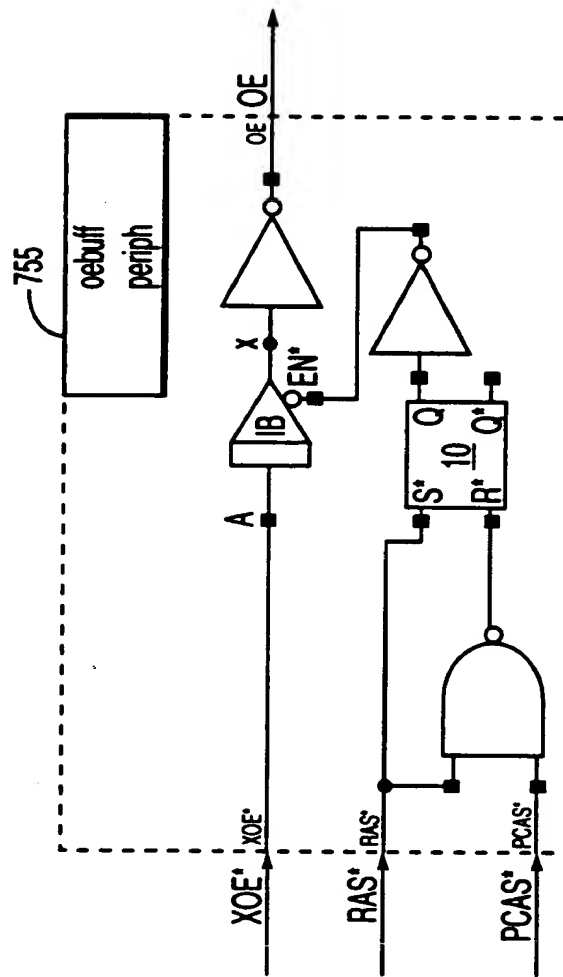


FIG. 47B



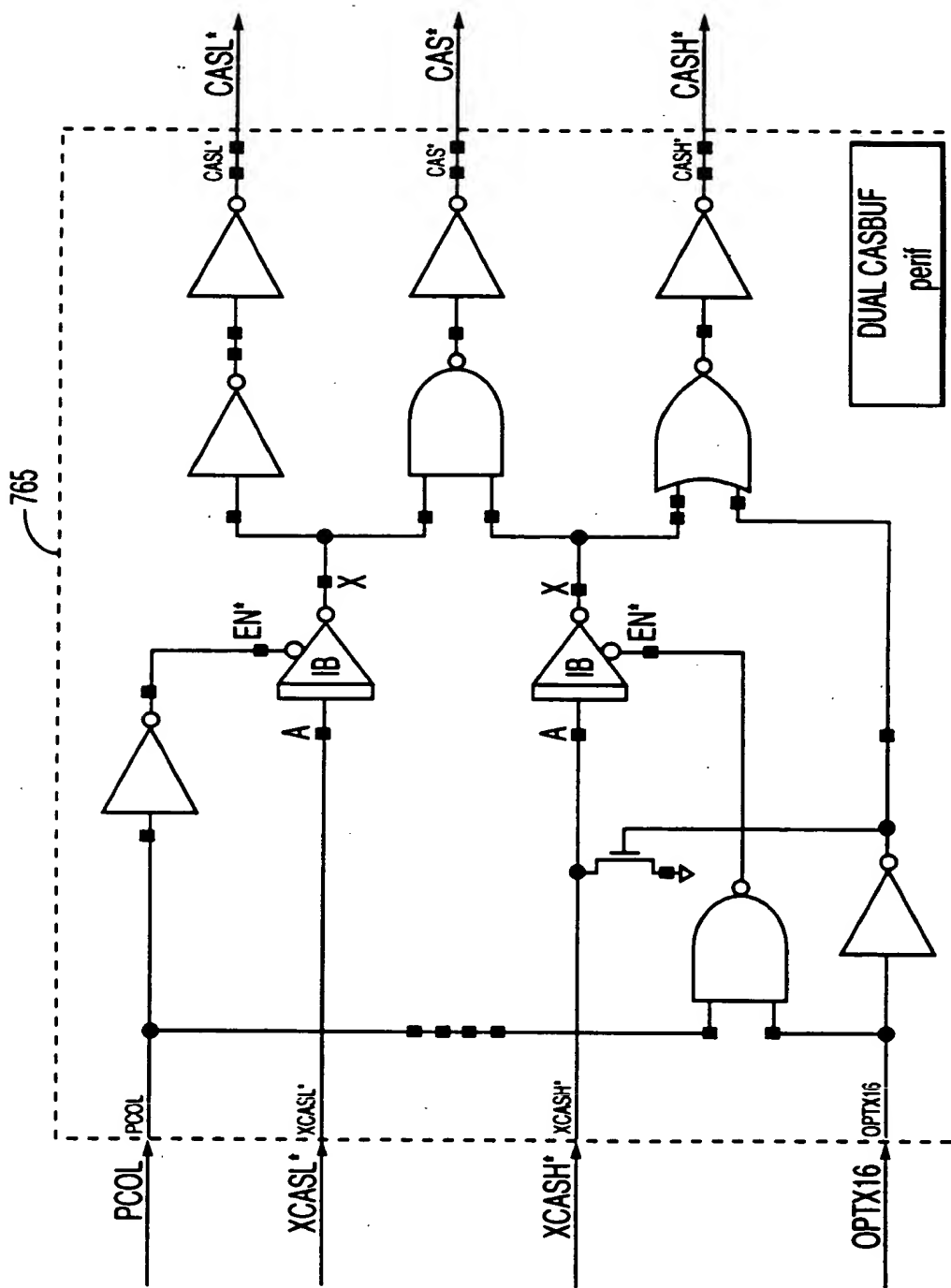


FIG. 47E

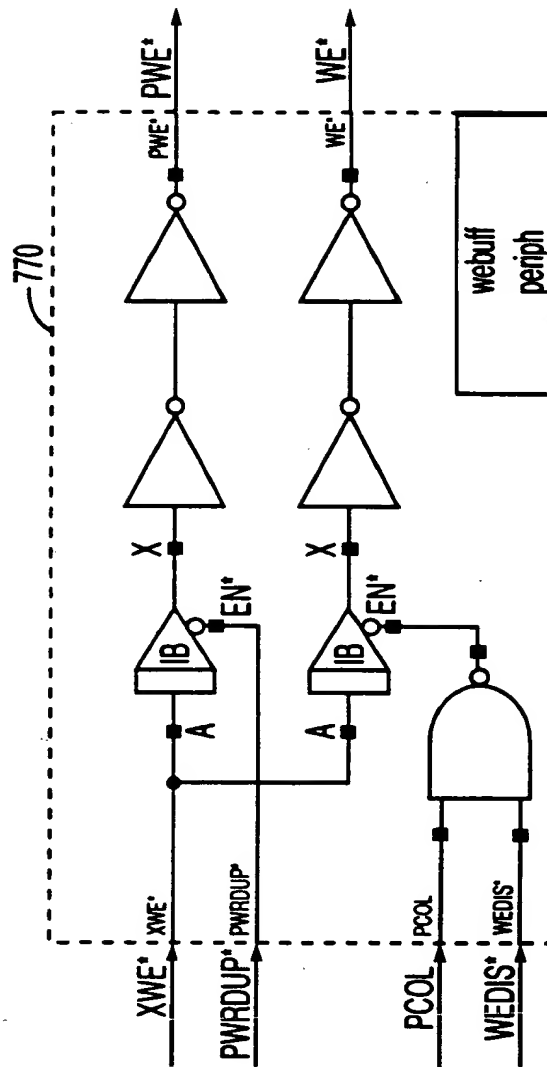


FIG. 47F

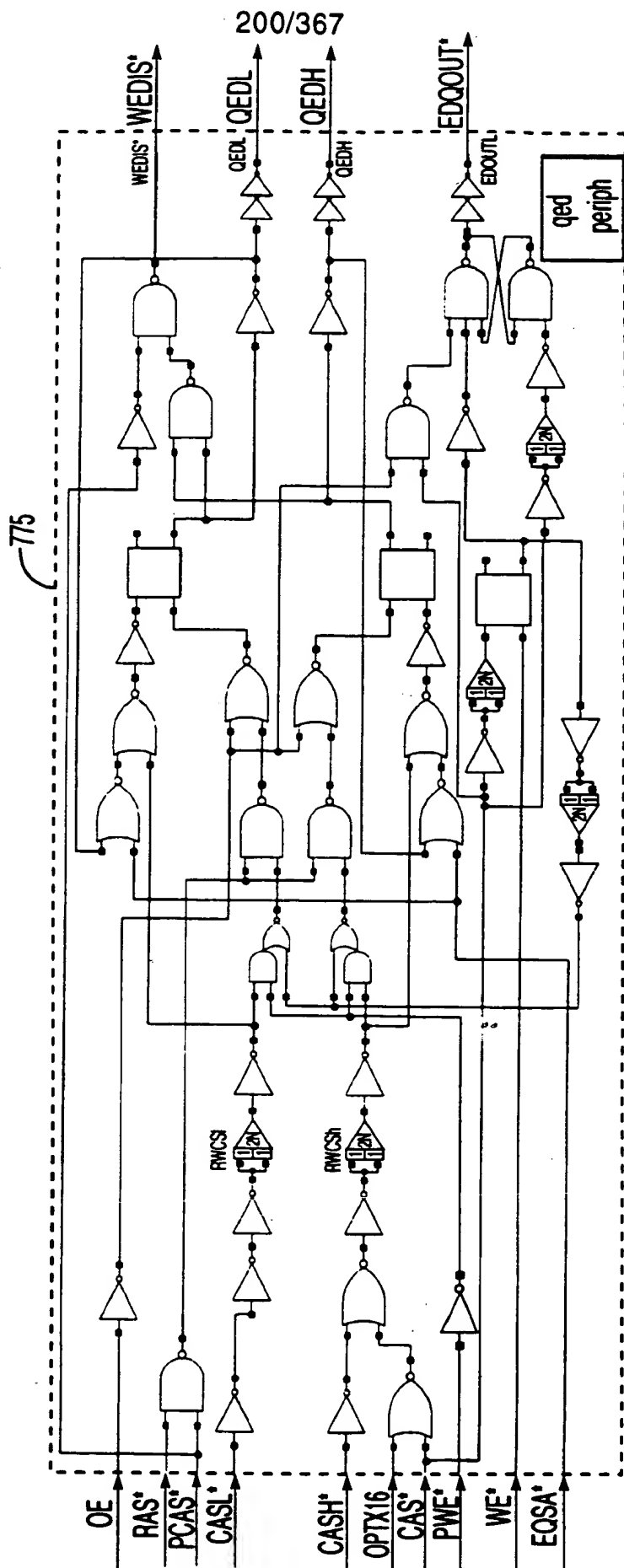


FIG. 47G

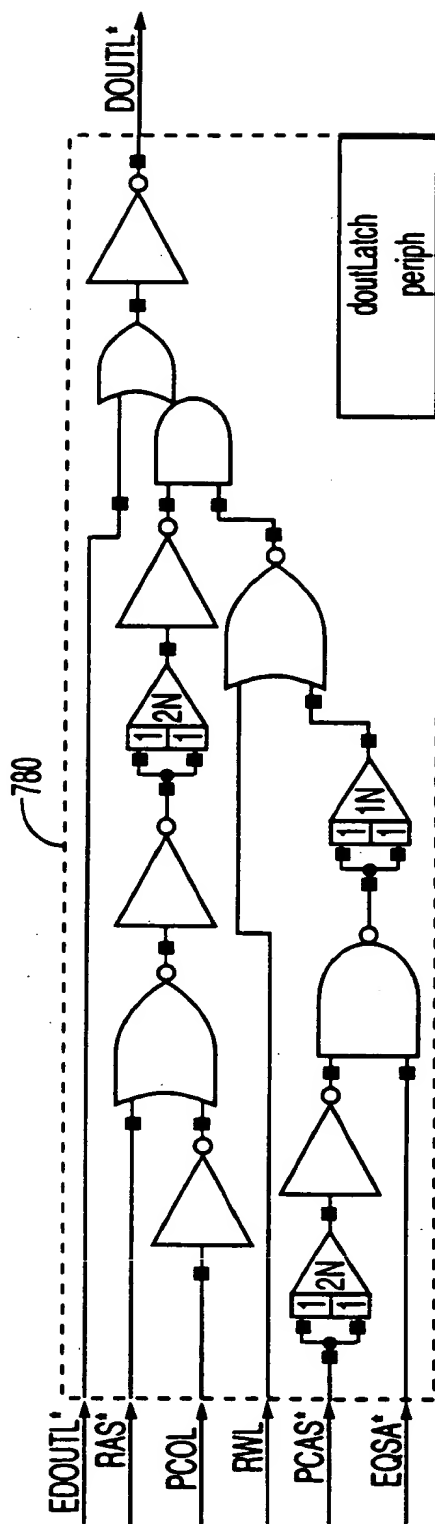


FIG. 47H

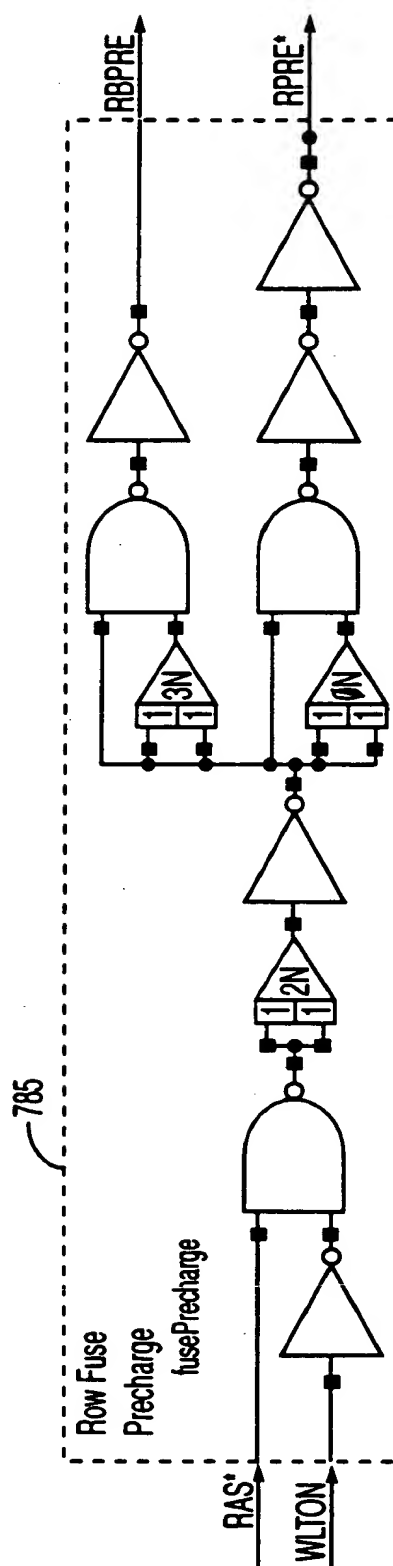


FIG. 471

203/367

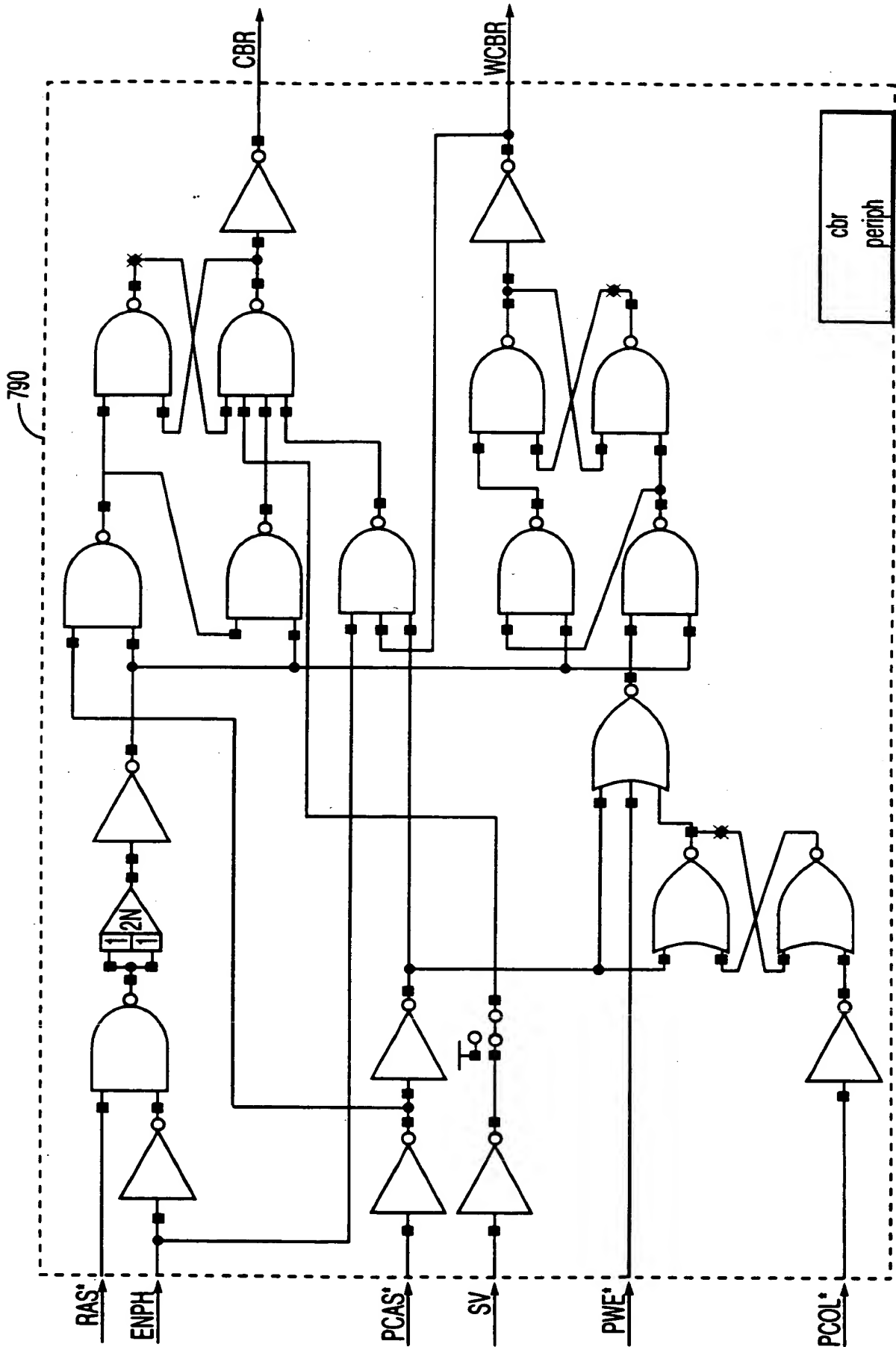


FIG. 47J

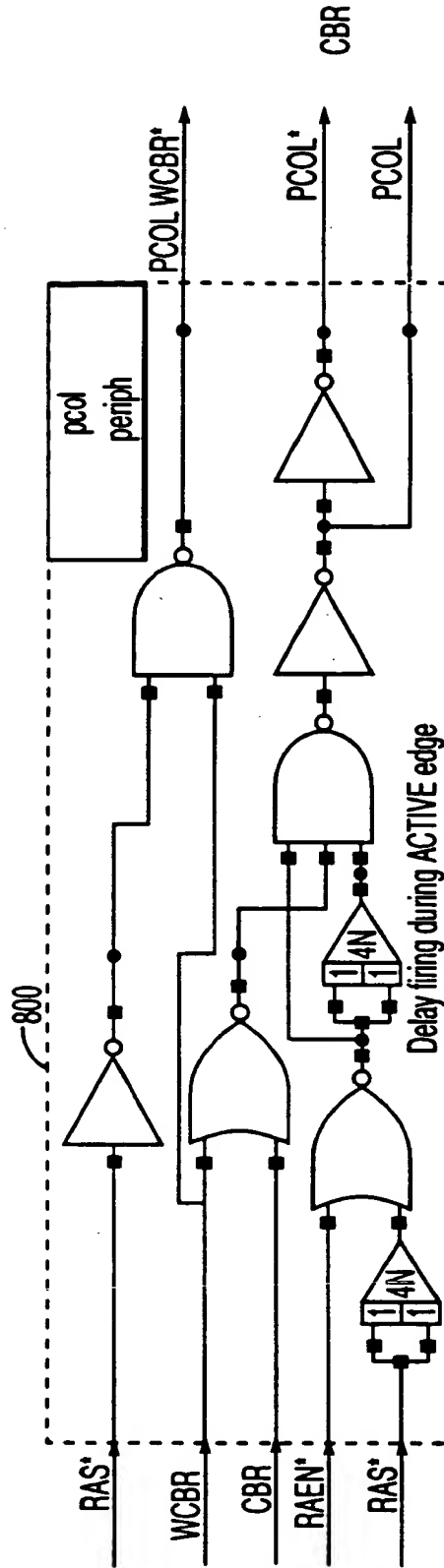


FIG. 47K

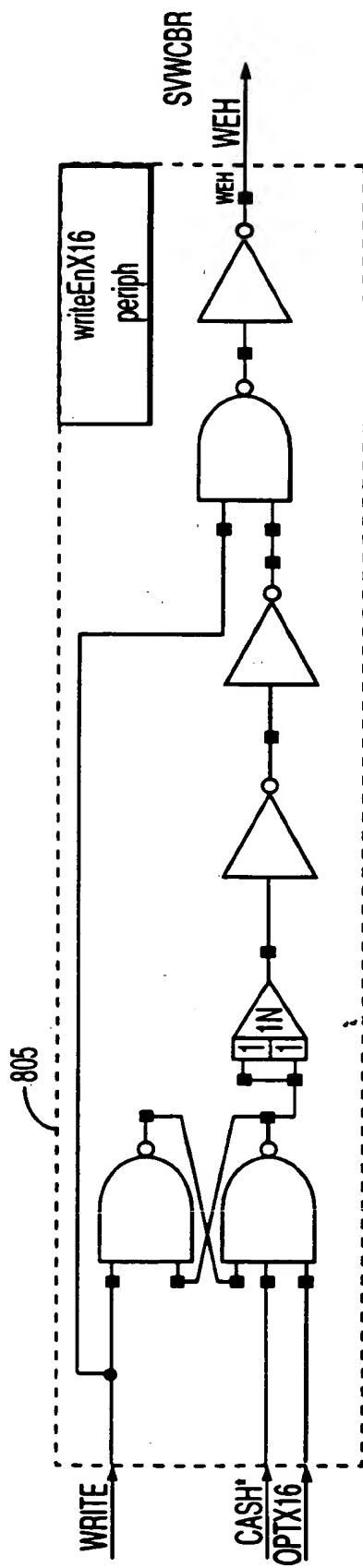


FIG. 47L

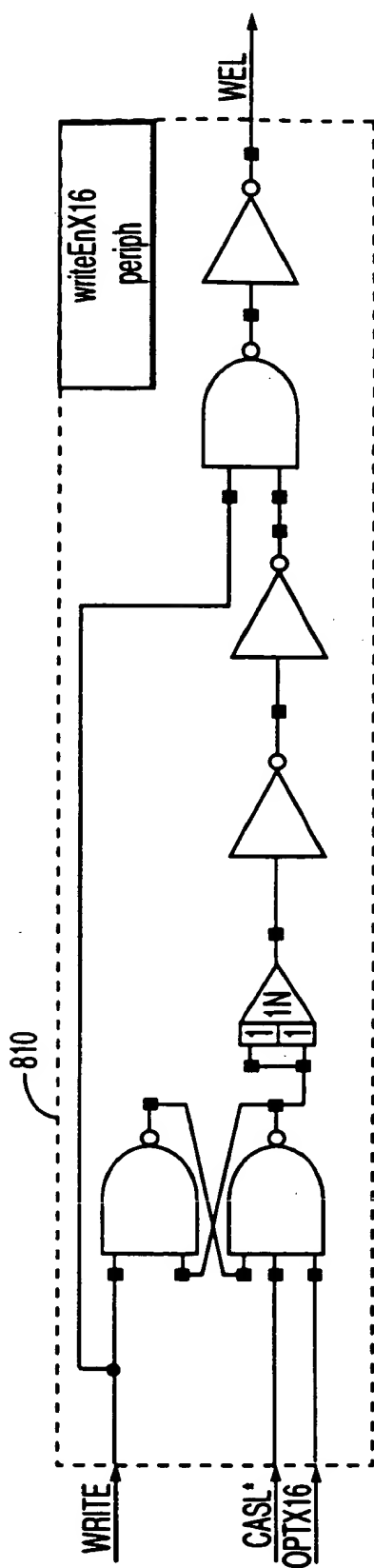


FIG. 47M

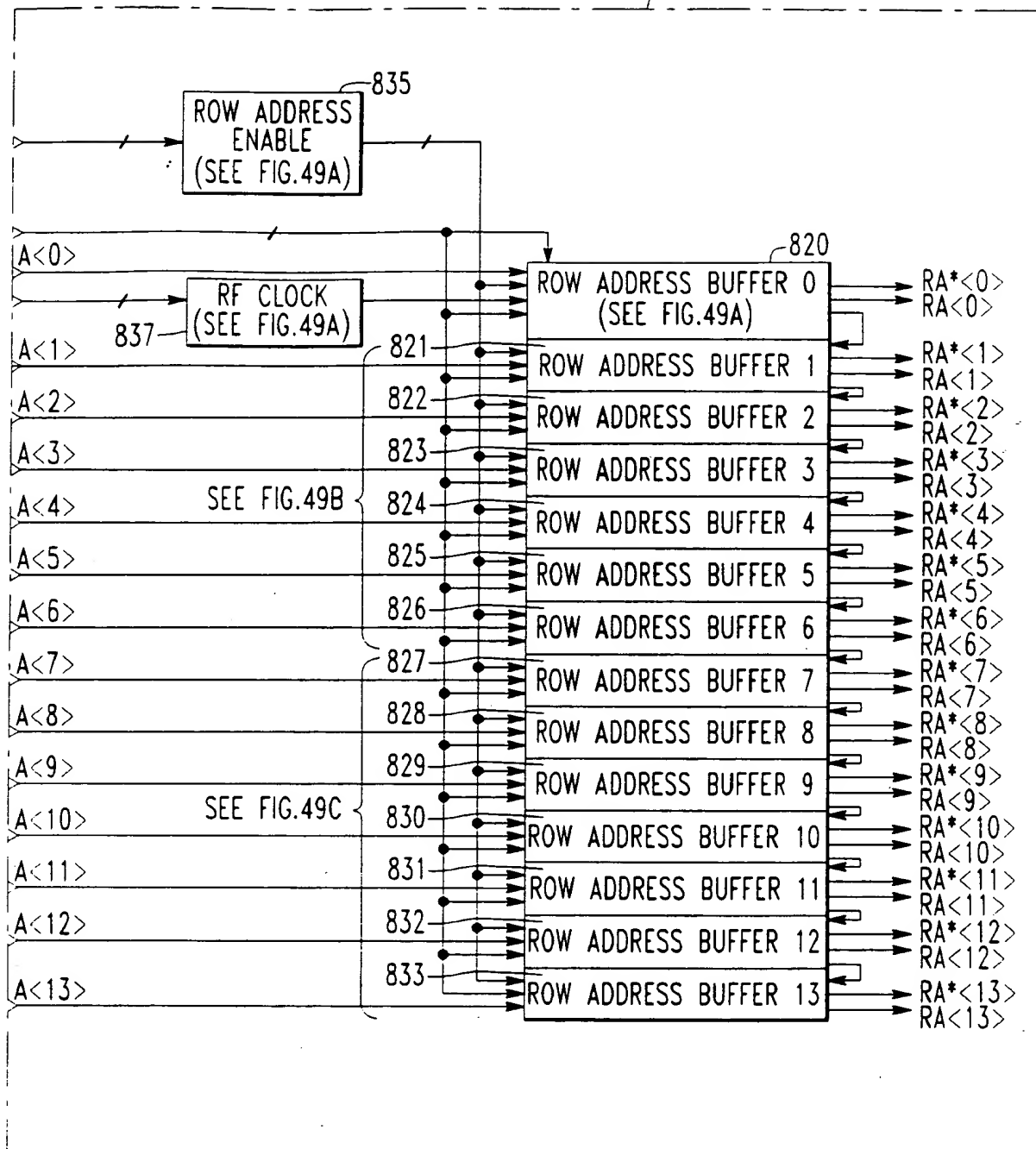


FIG. 48A

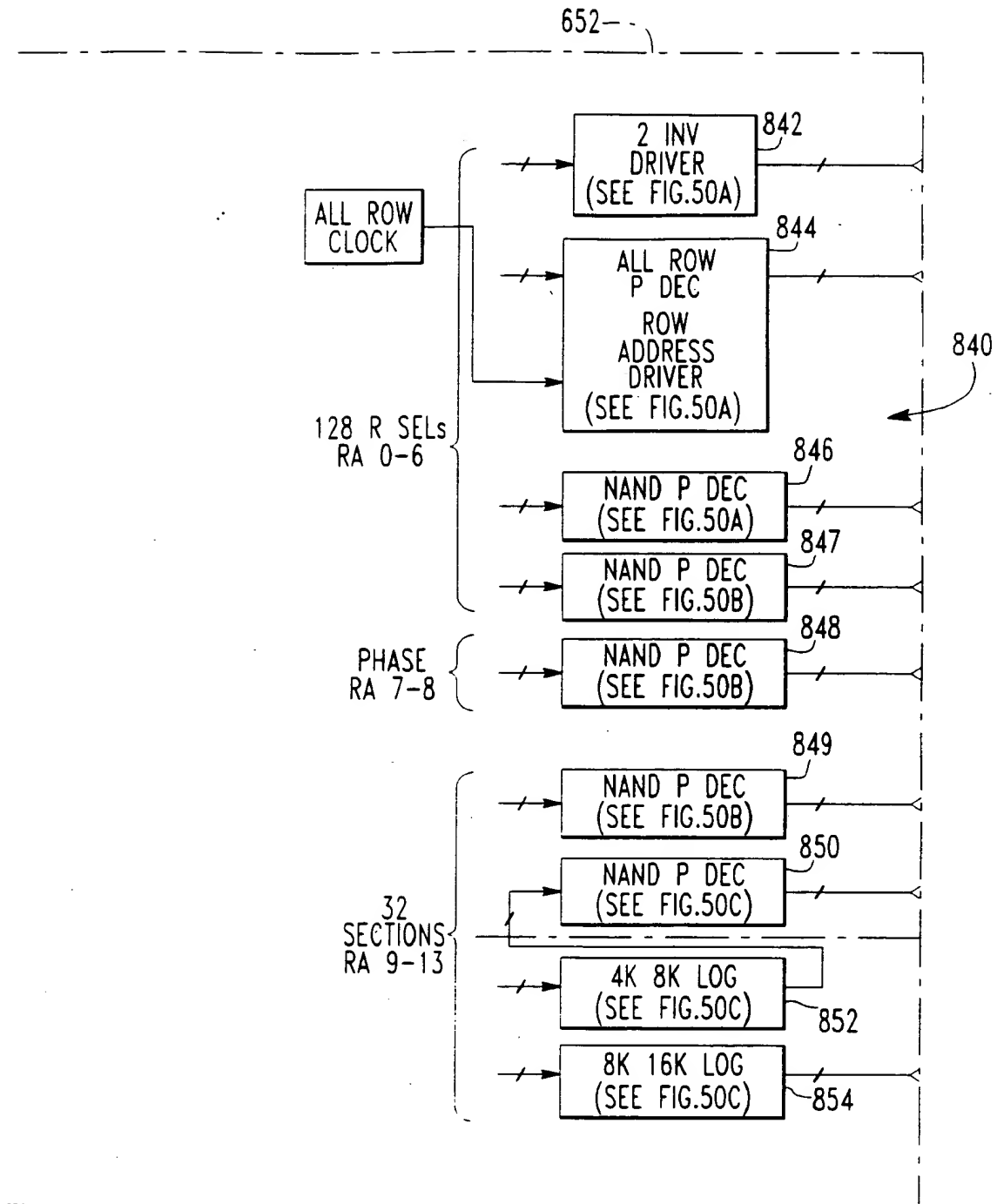


FIG. 48B

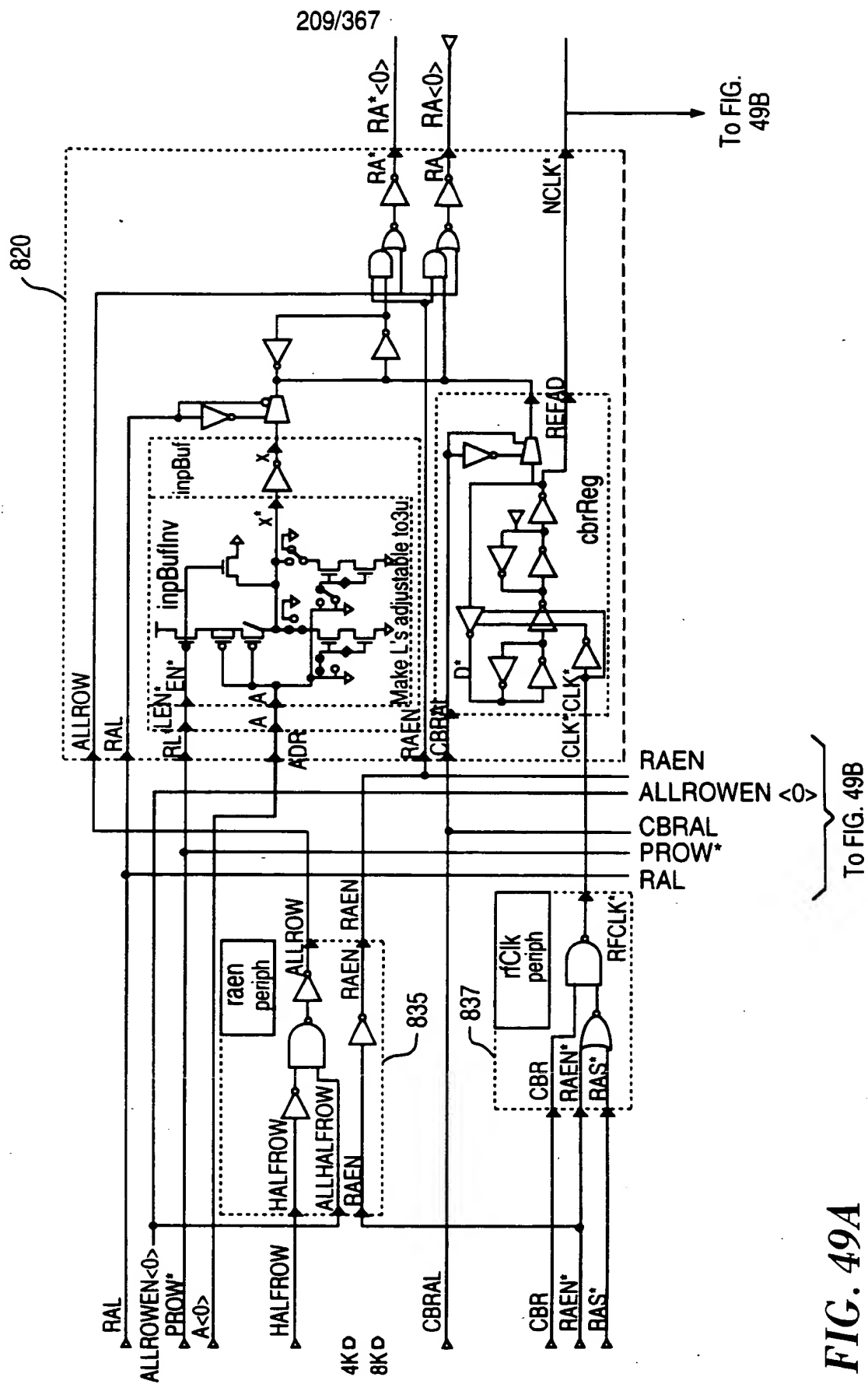


FIG. 49A

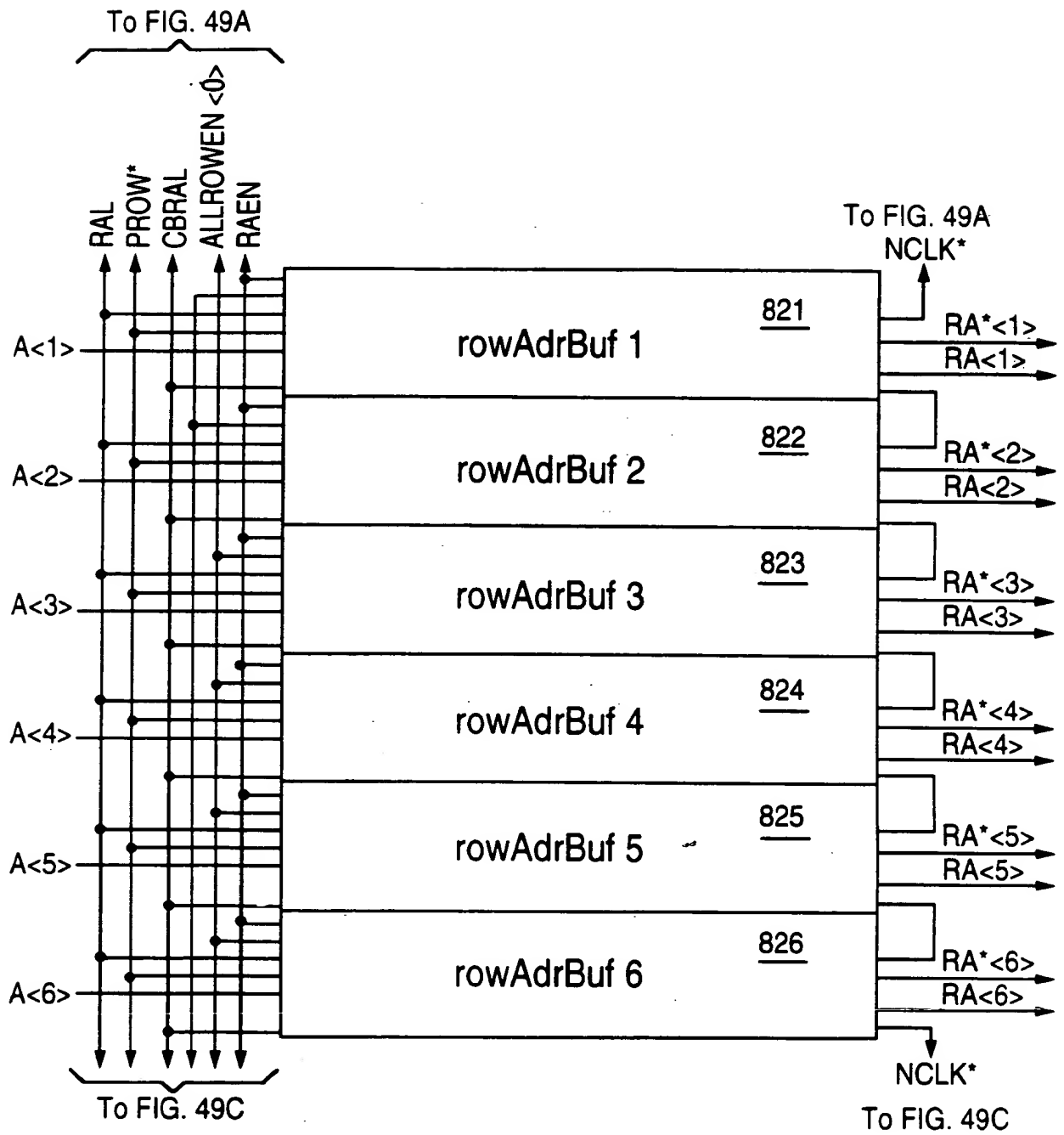


FIG. 49B

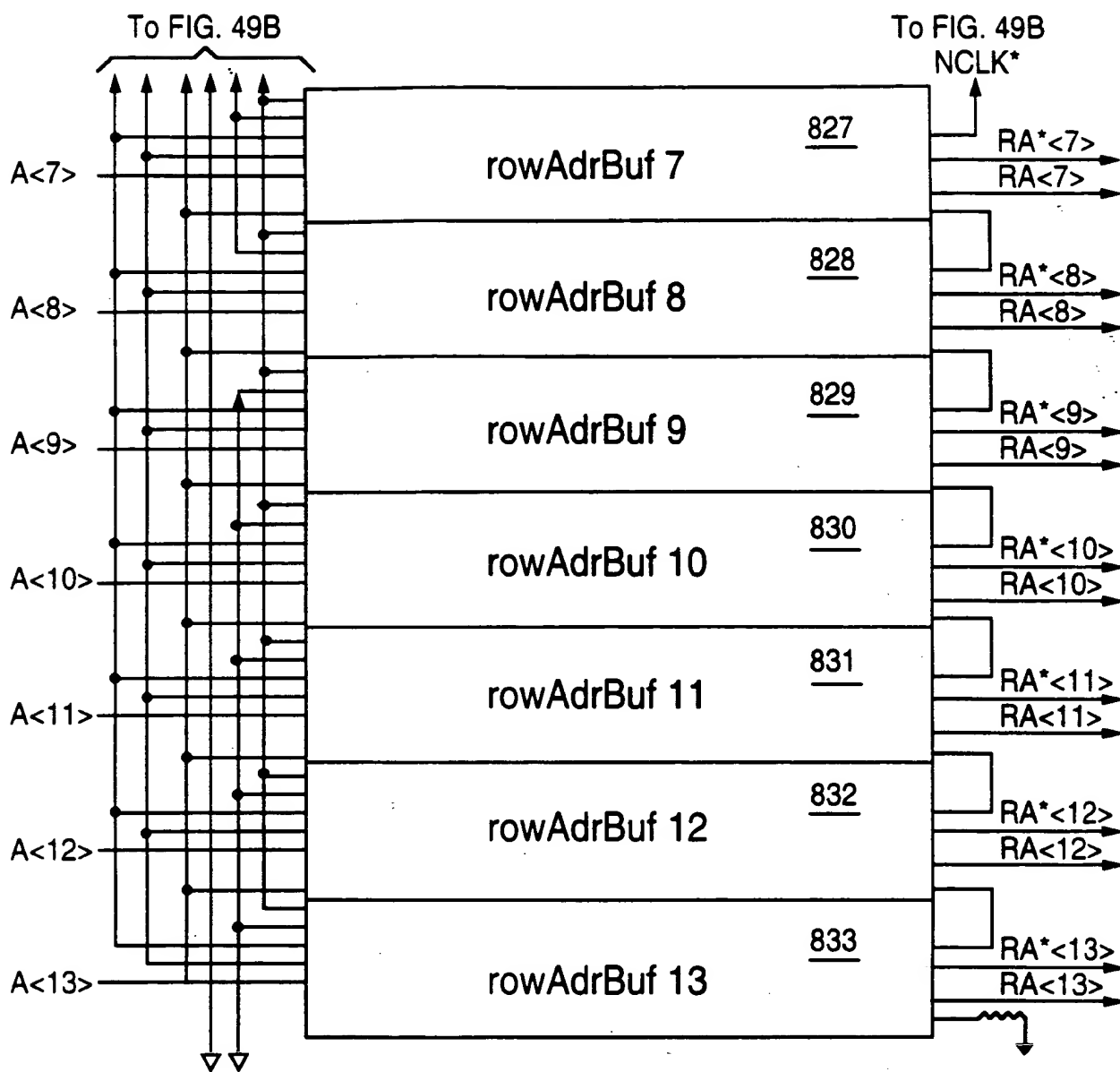


FIG. 49C

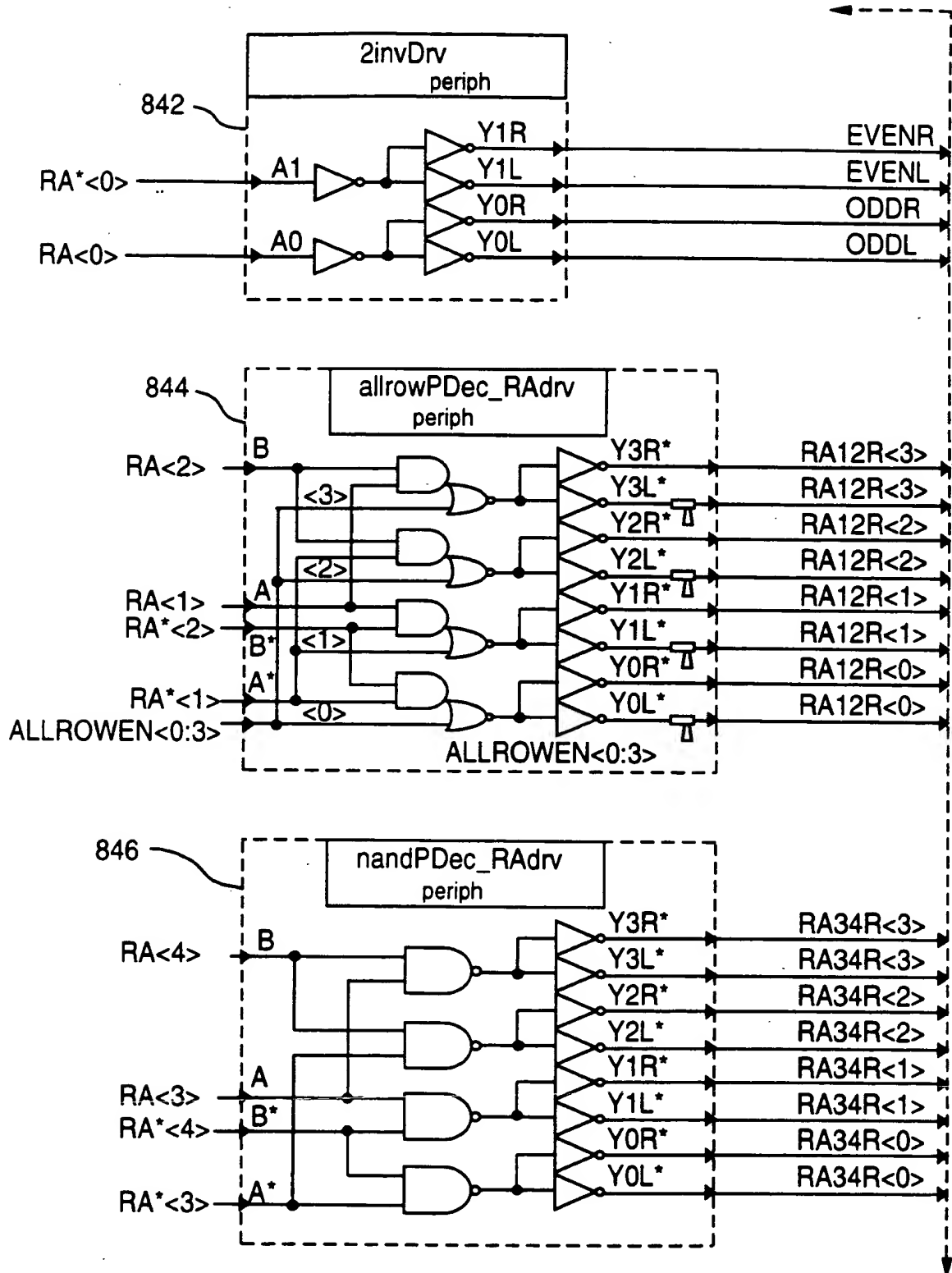
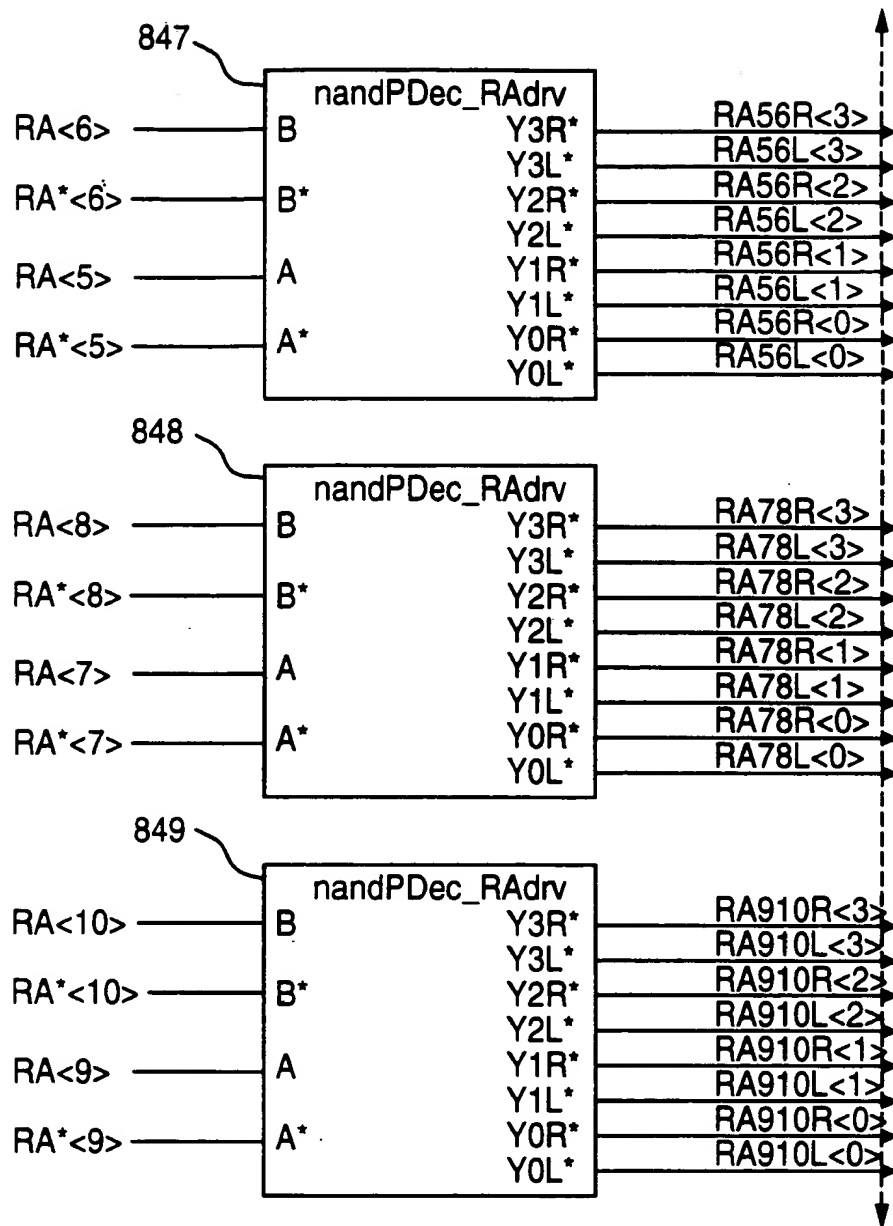


FIG. 50A

**FIG. 50B**

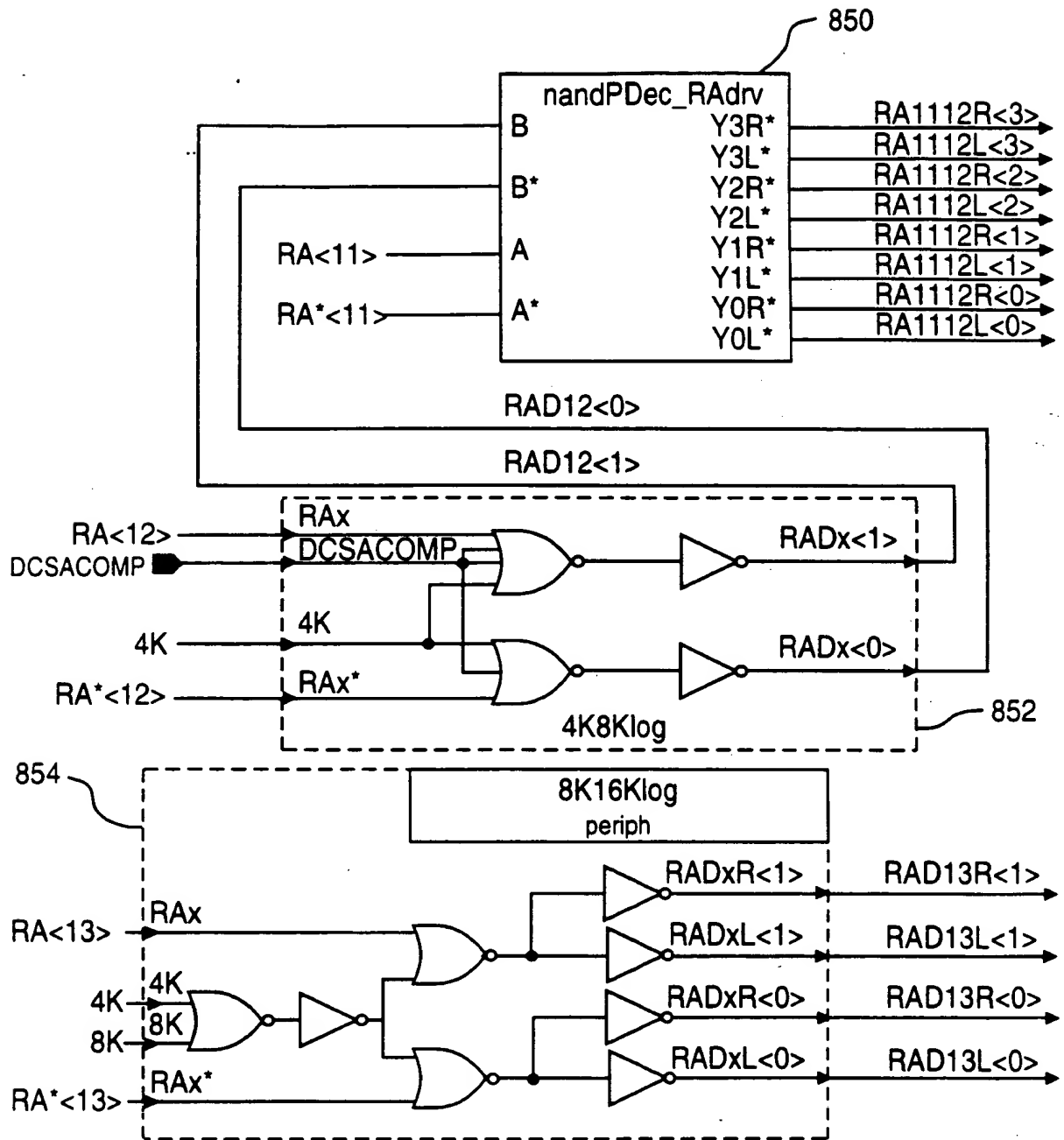


FIG. 50C

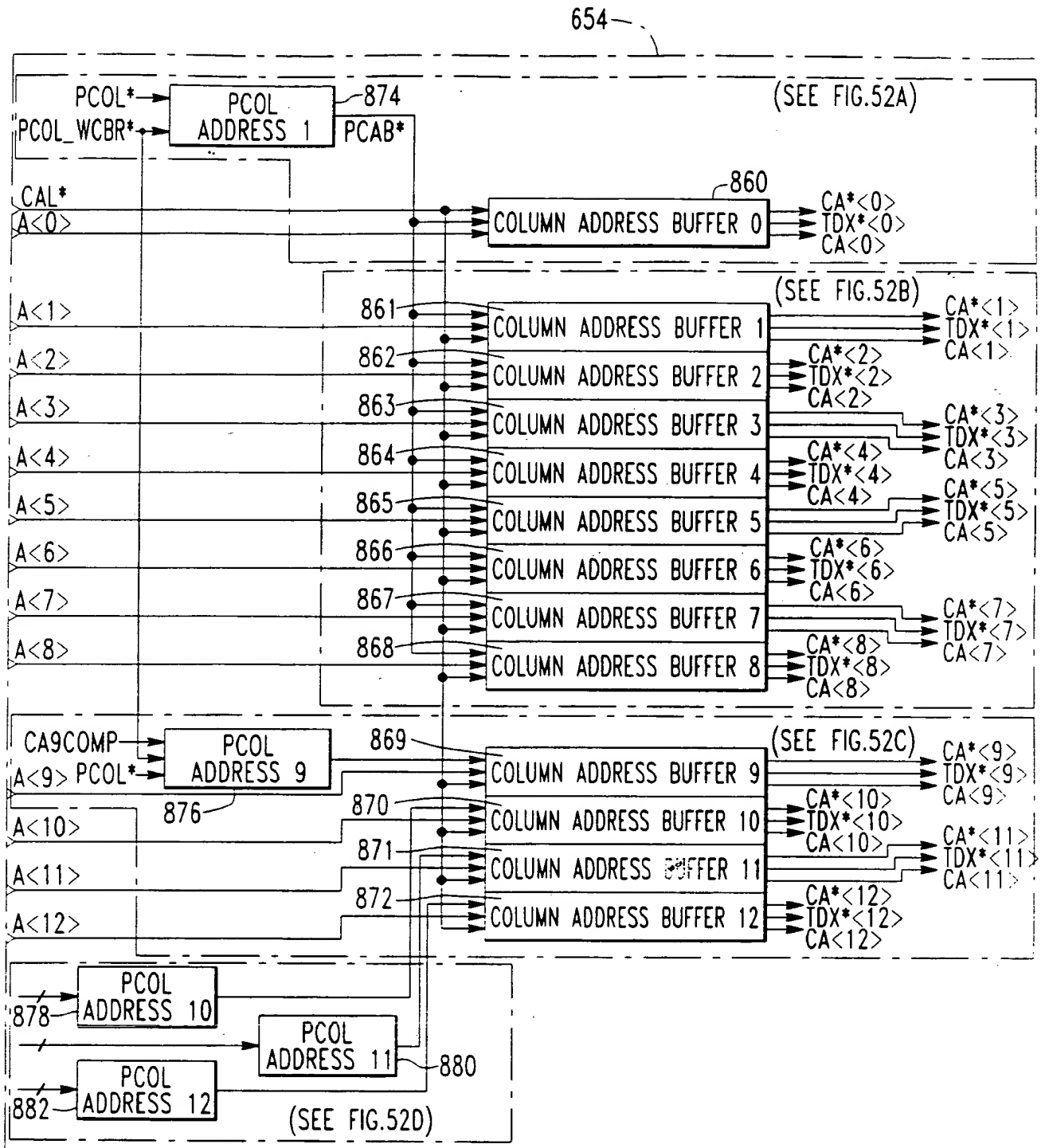


FIG. 51A

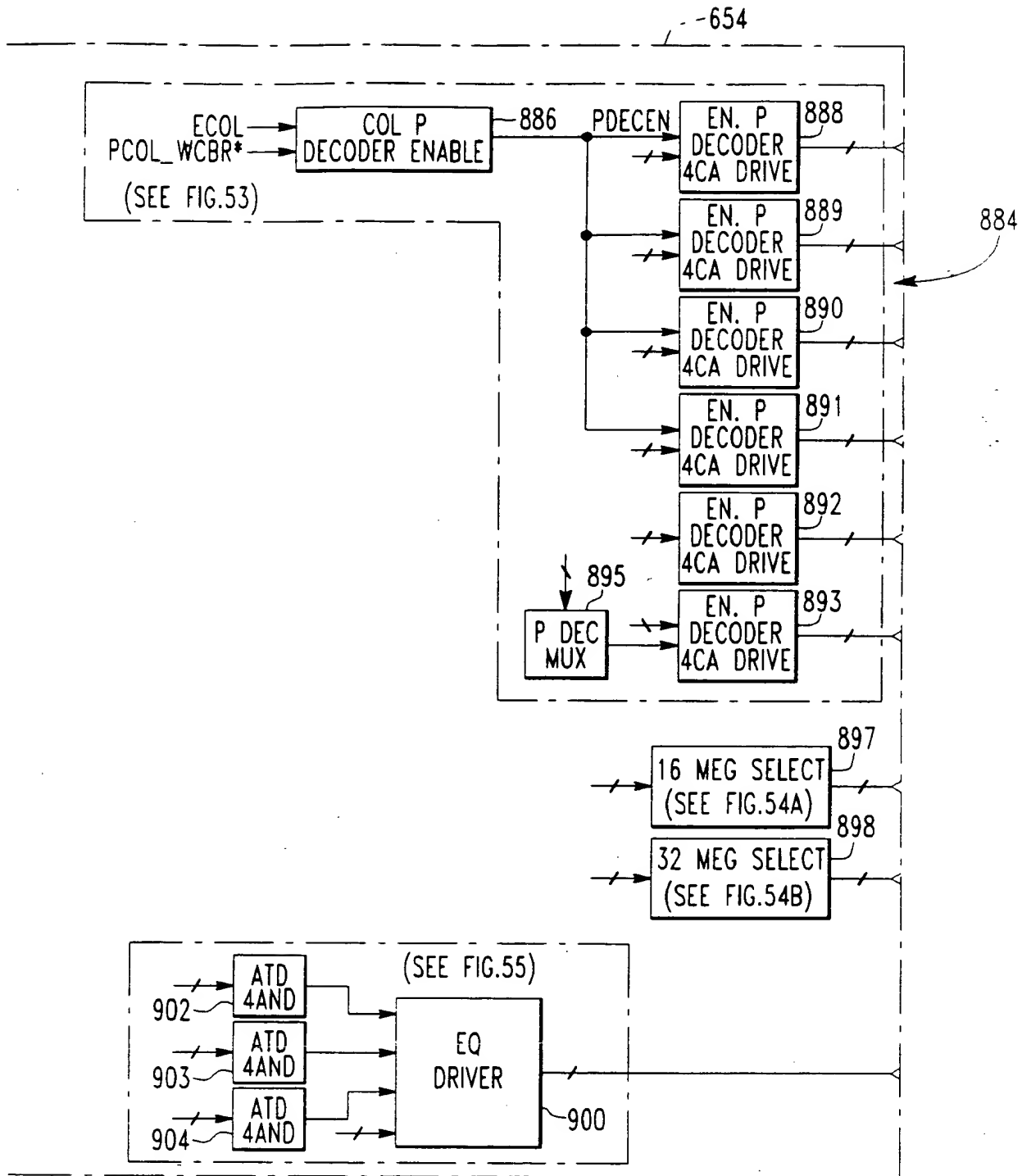


FIG. 51B

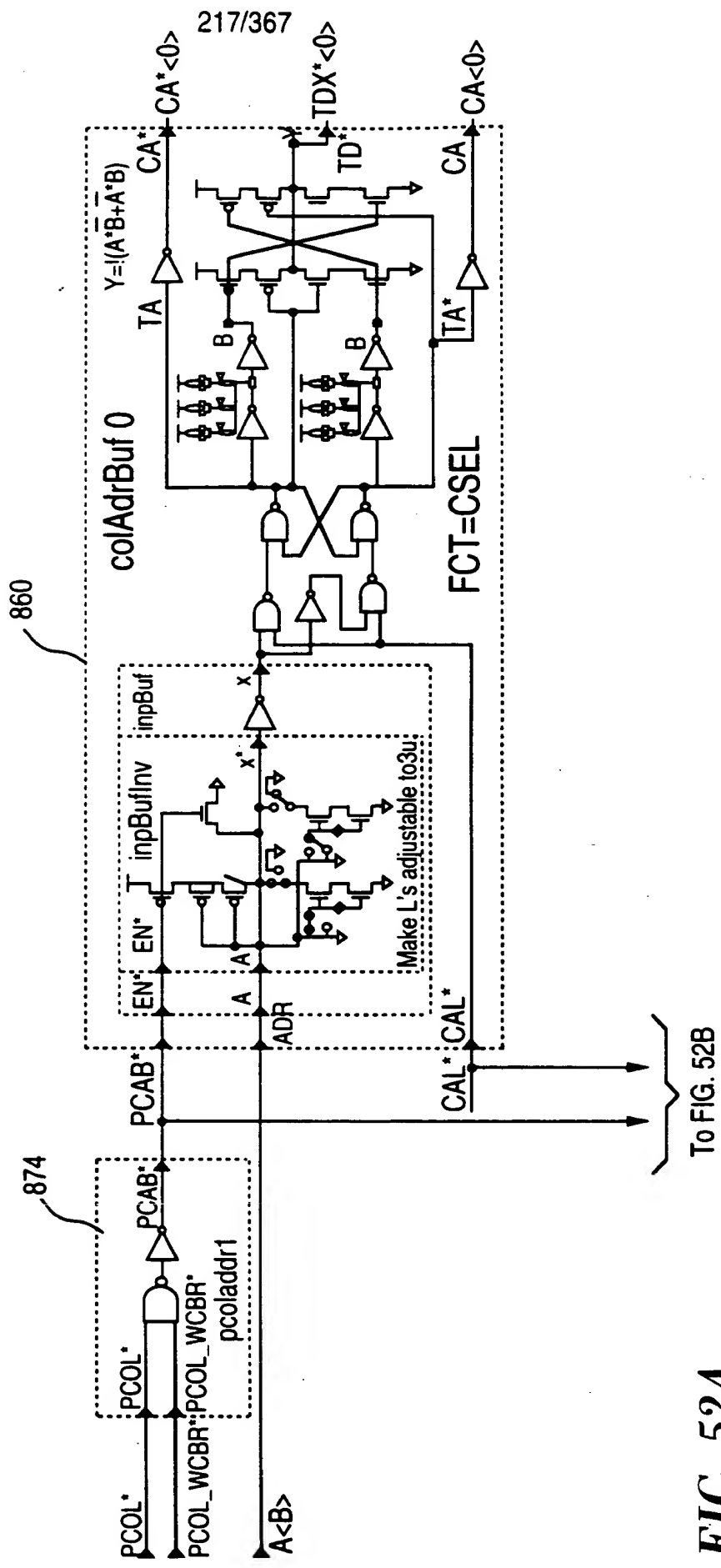


FIG. 52A

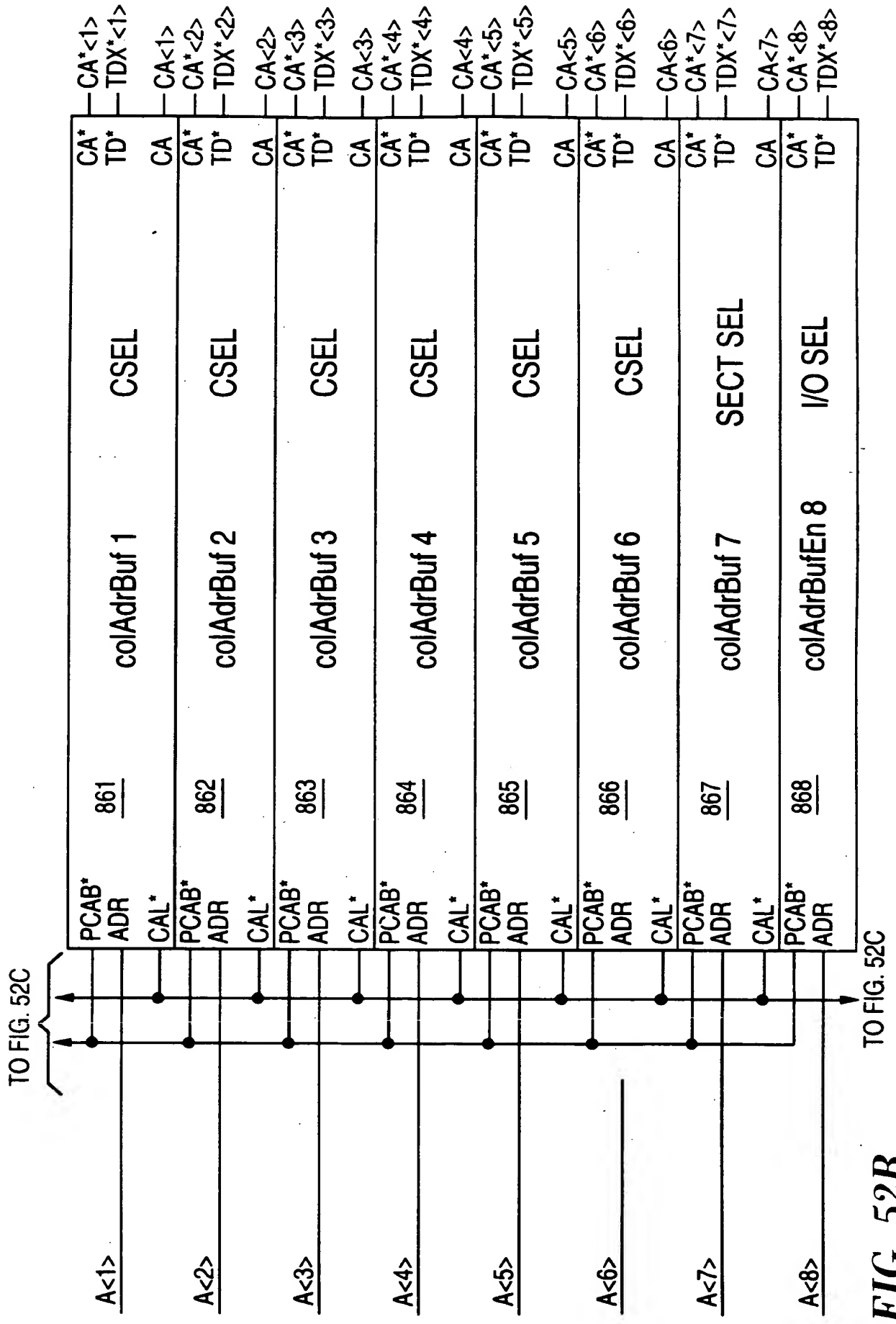


FIG. 52B

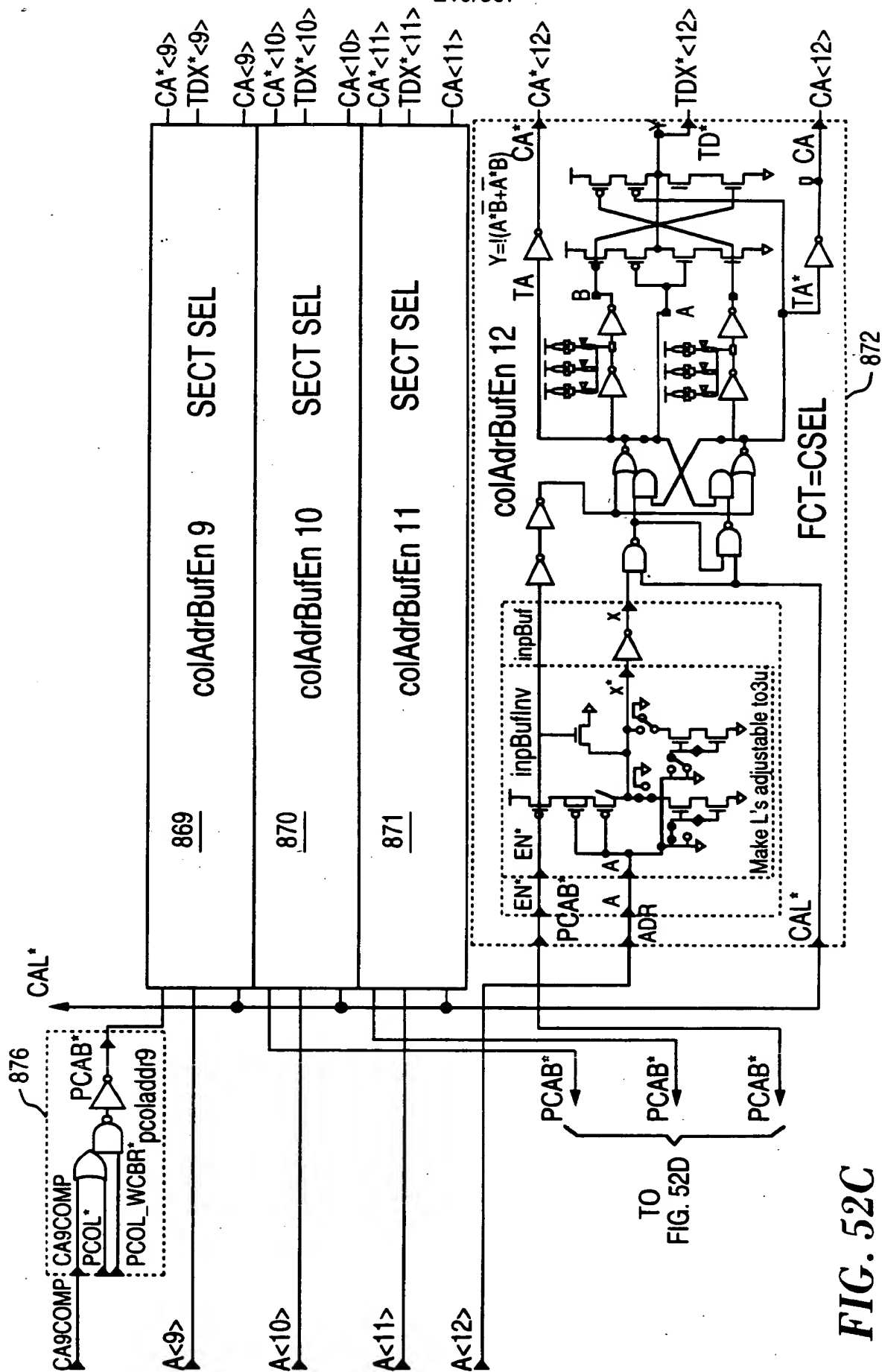


FIG. 52C

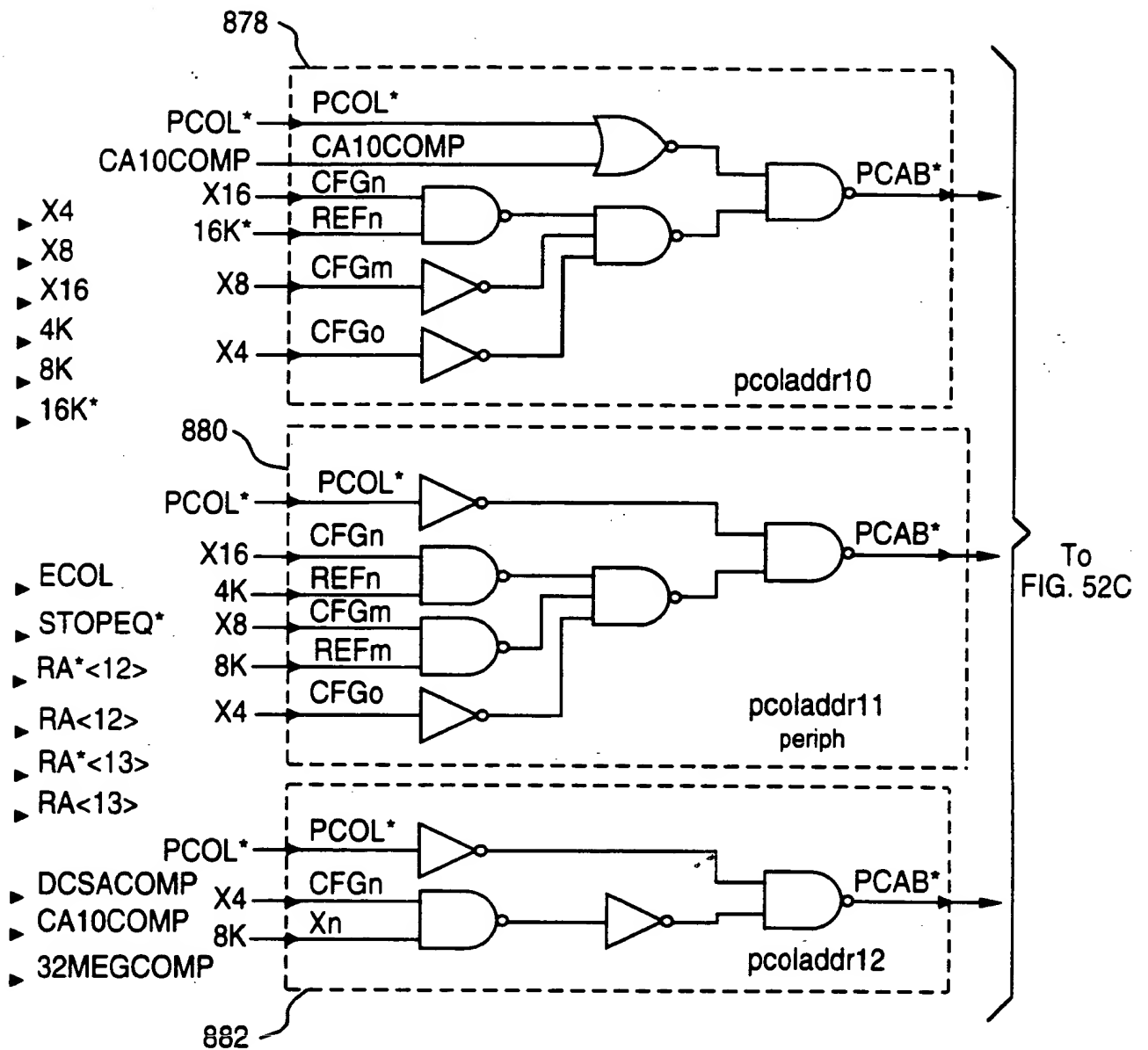


FIG. 52D

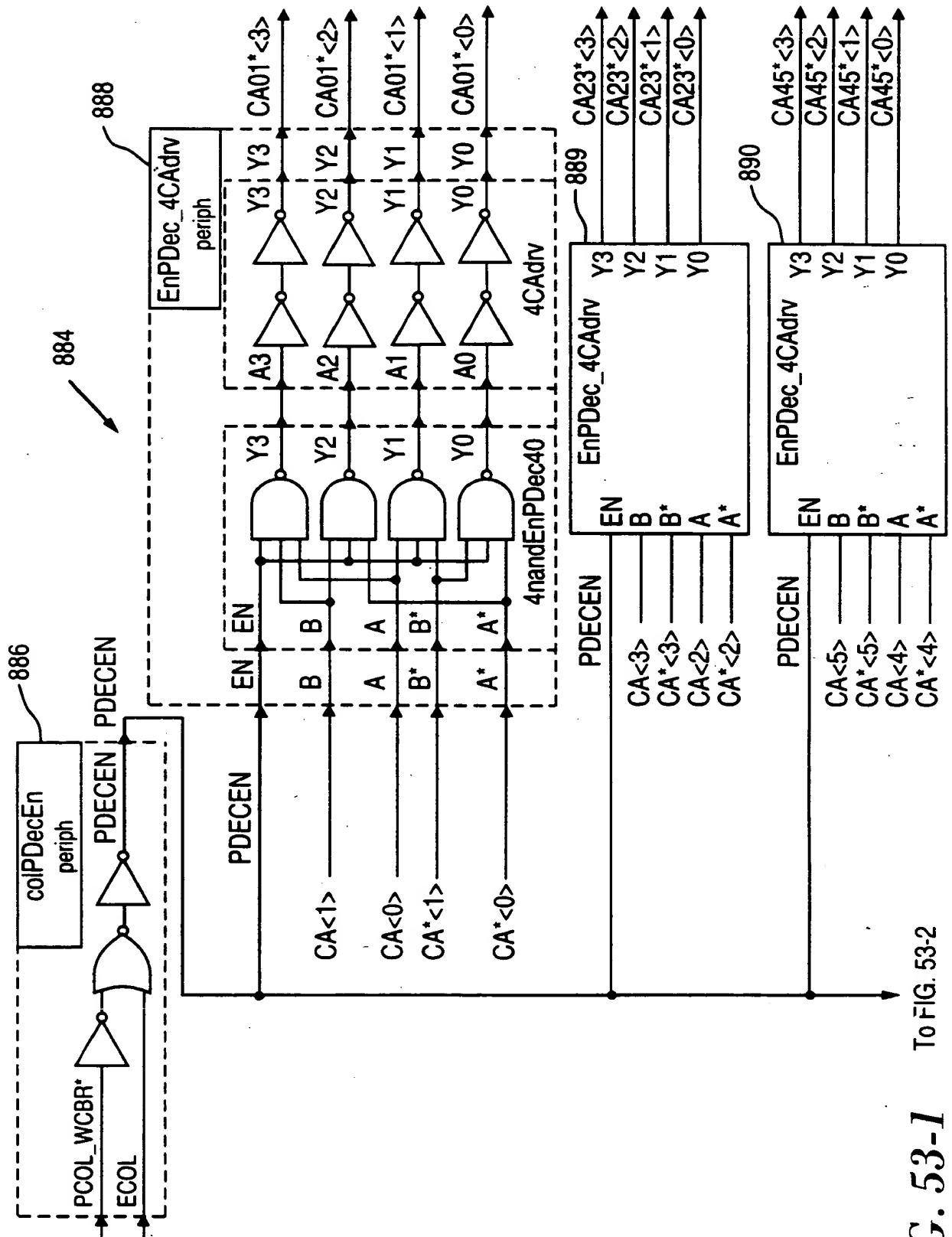


FIG. 53-1 To FIG. 53-2

To FIG. 53-1

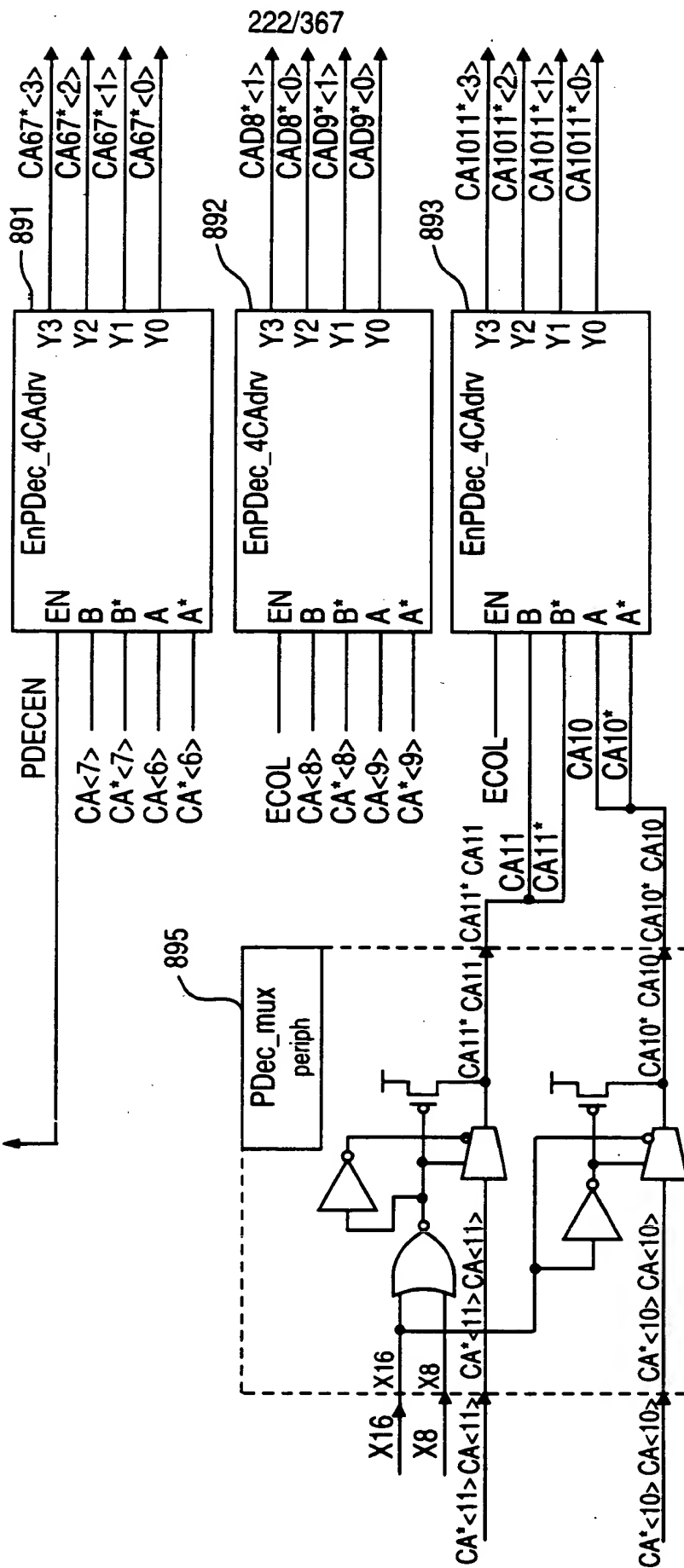


FIG. 53-2

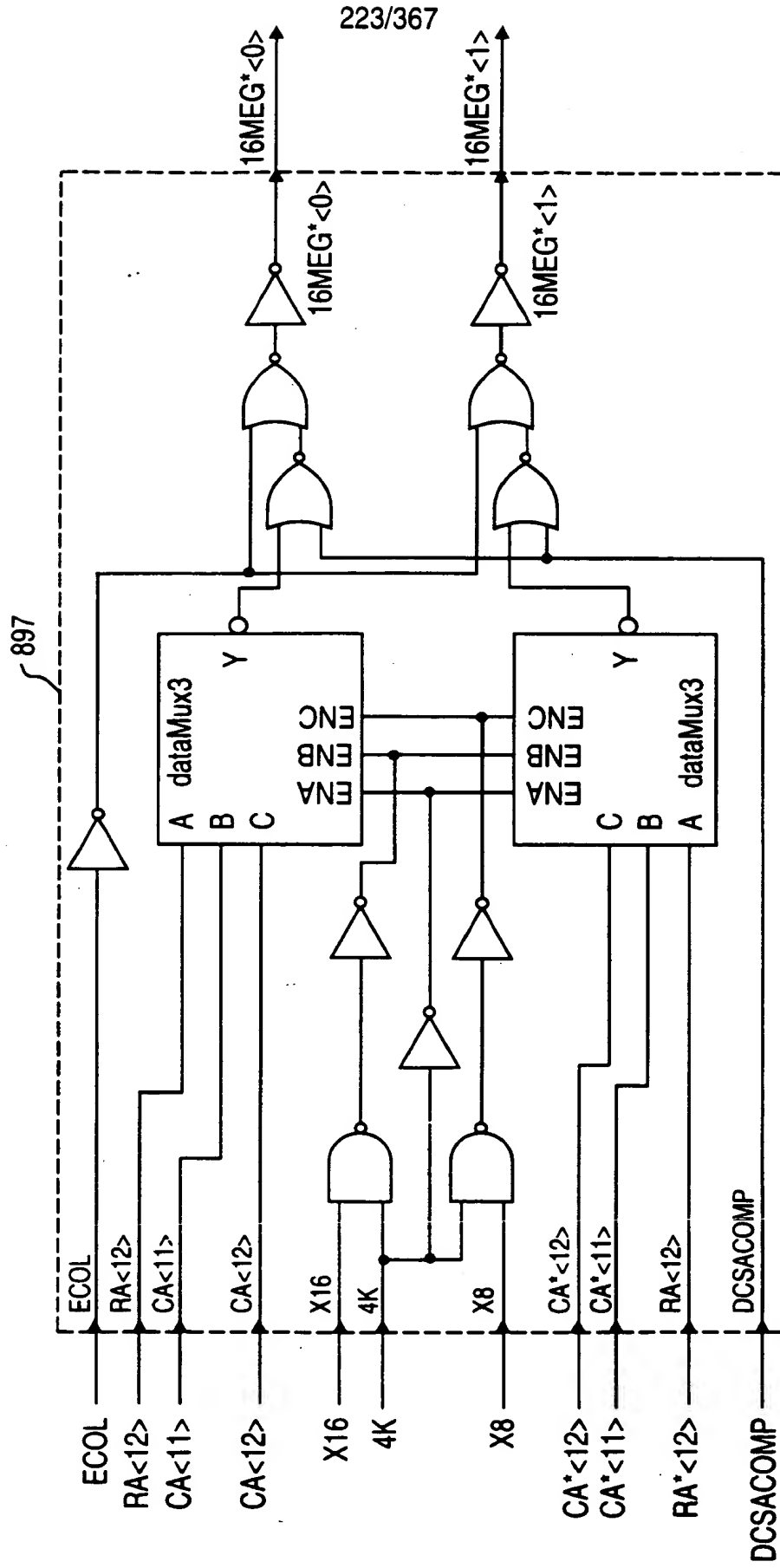


FIG. 54A

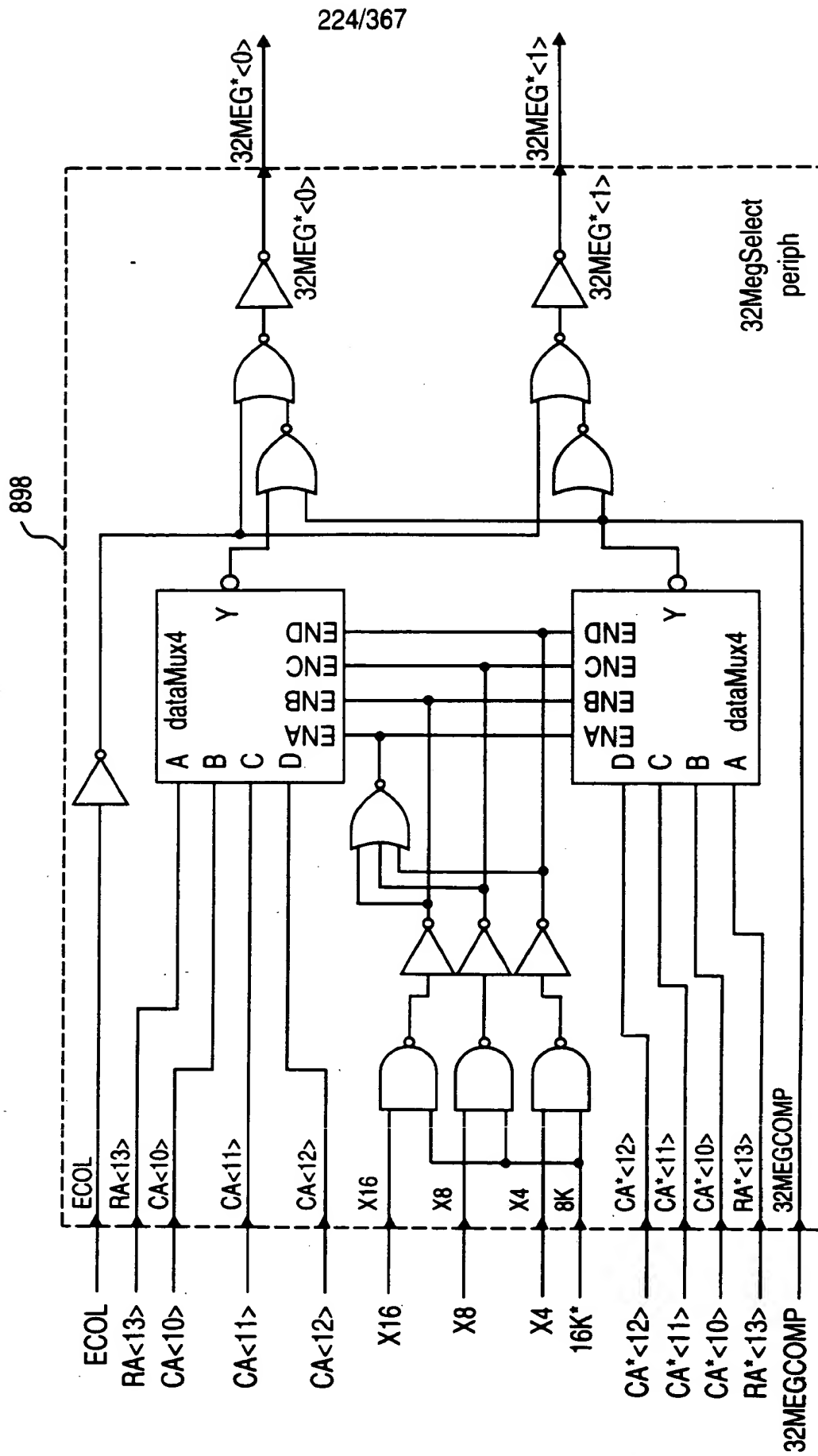


FIG. 54B

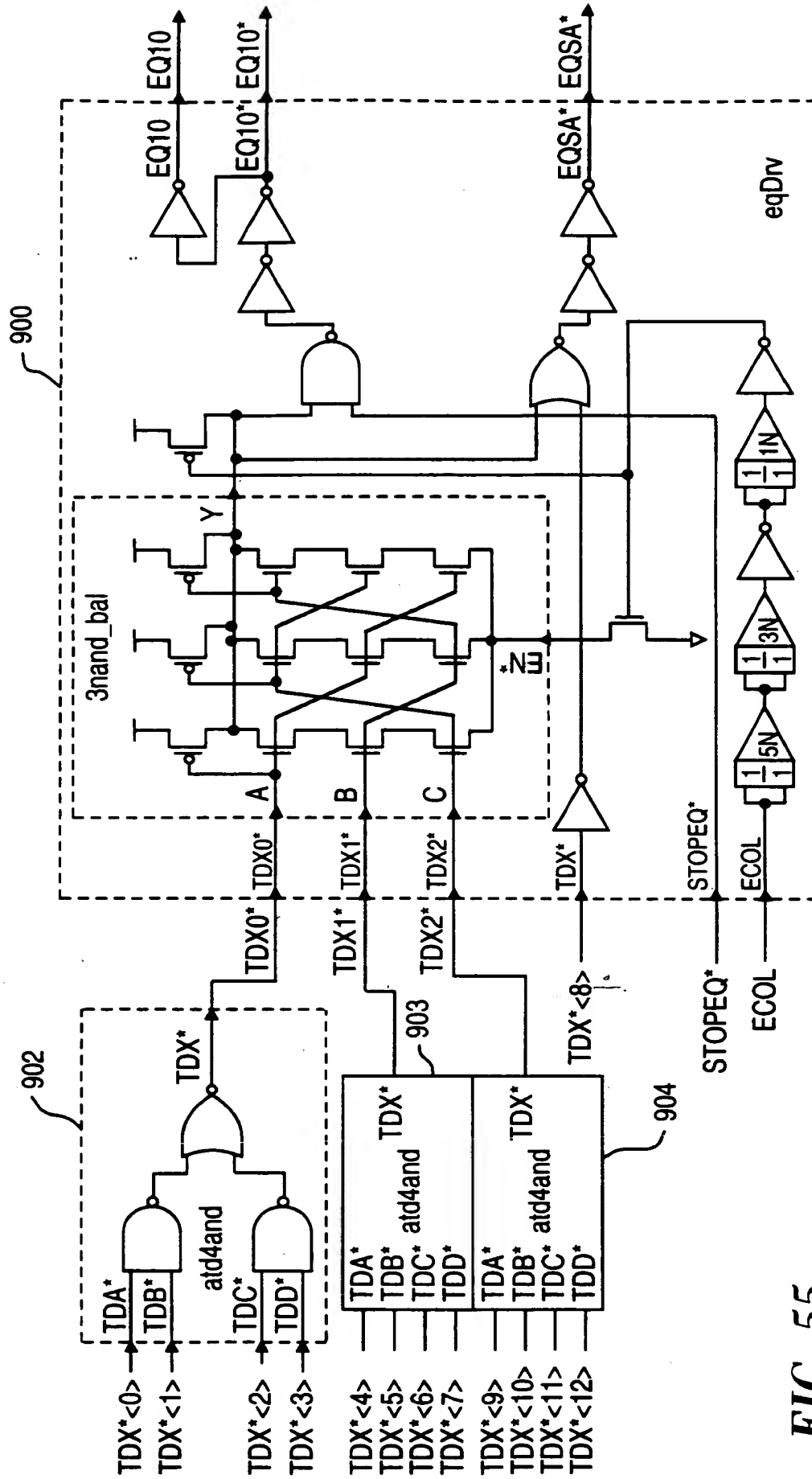


FIG. 55

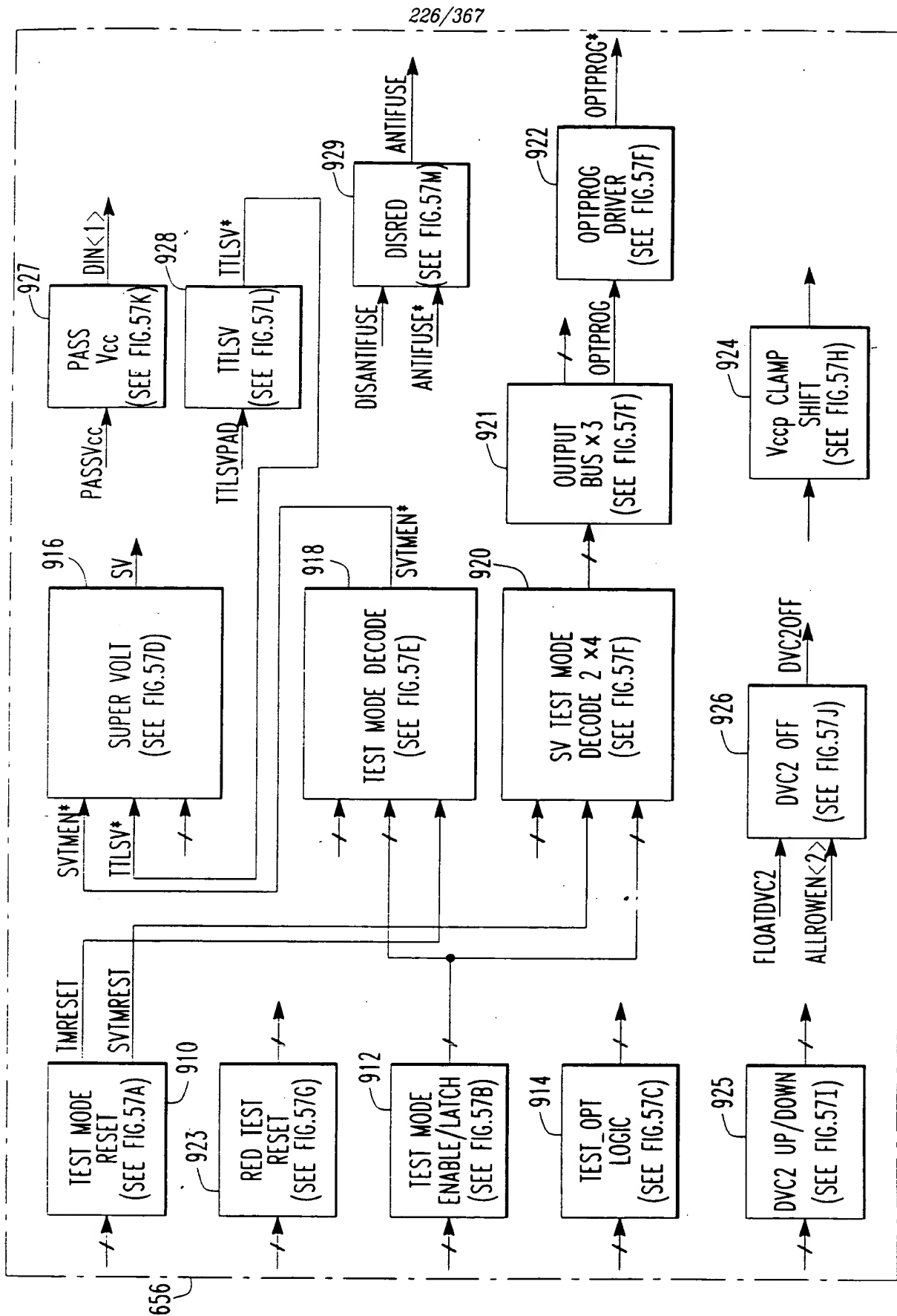


FIG. 56

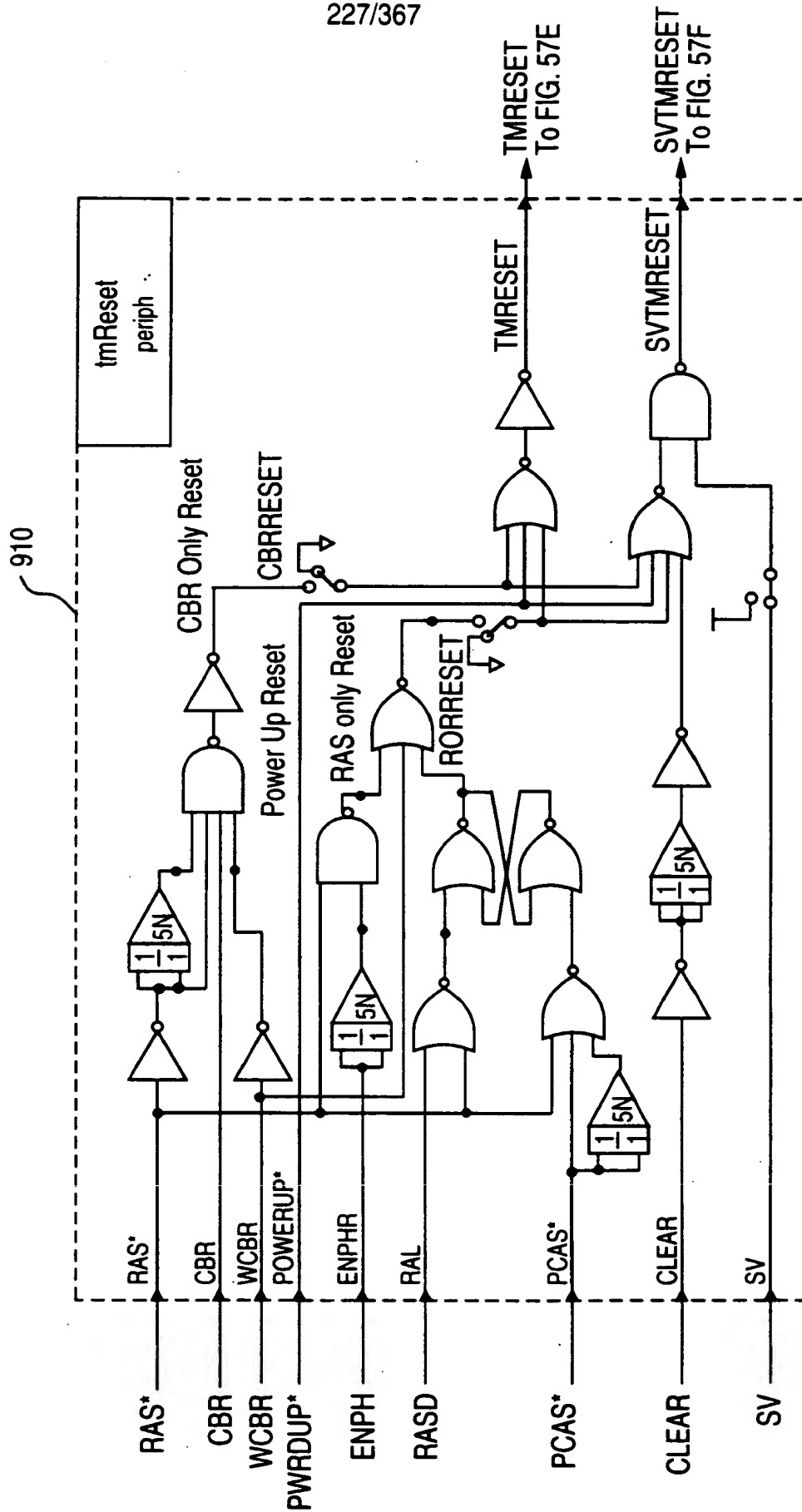


FIG. 57A



FIG. 57B

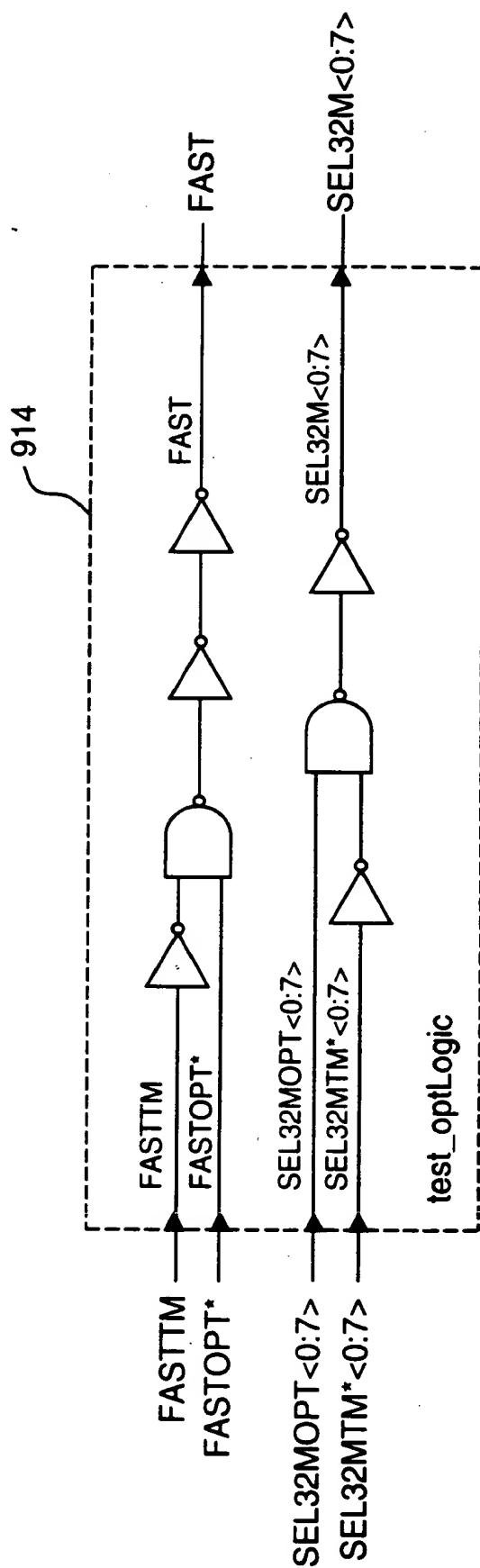


FIG. 57C

FIG. 57D

230/367

916

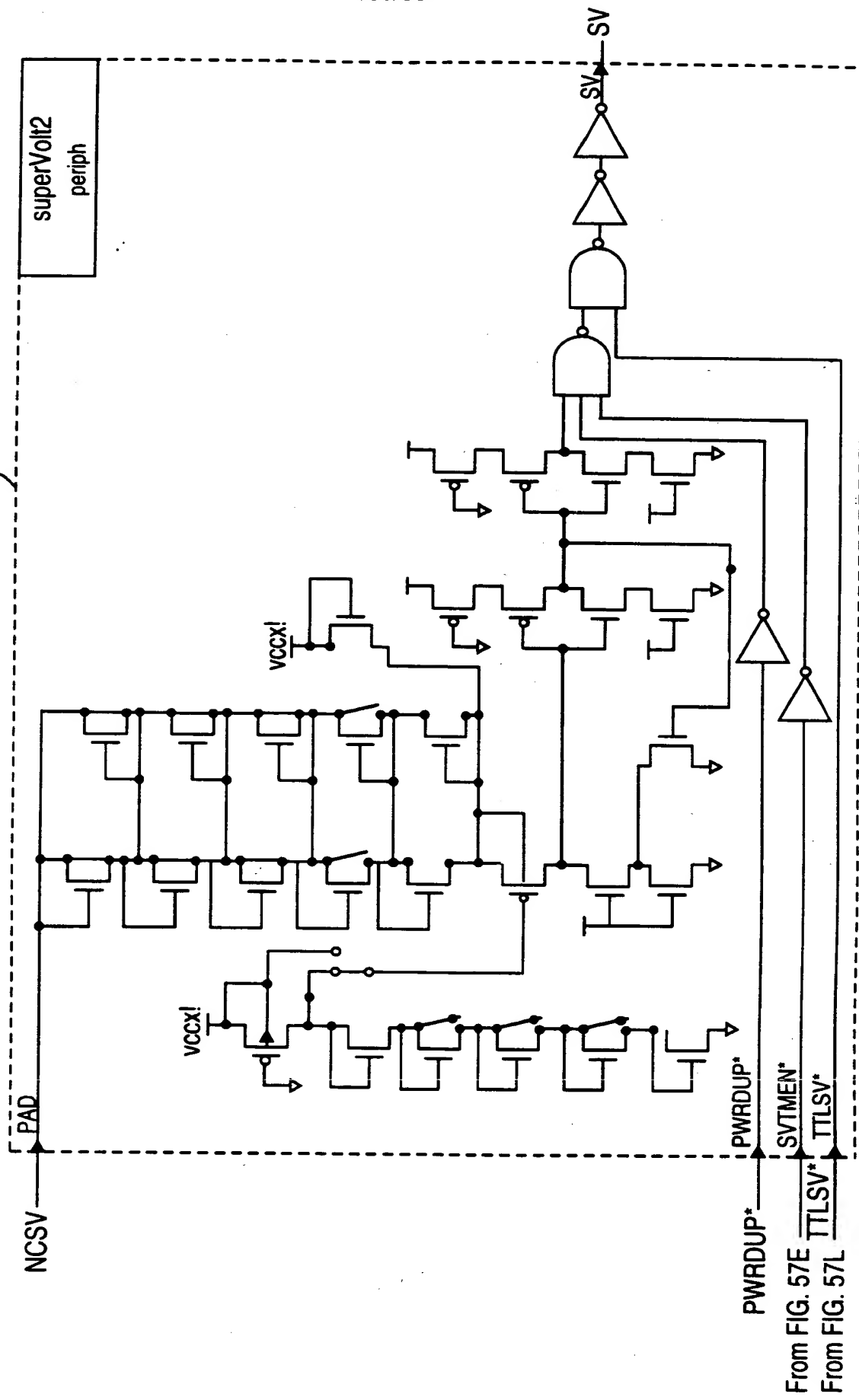


FIG. 57D

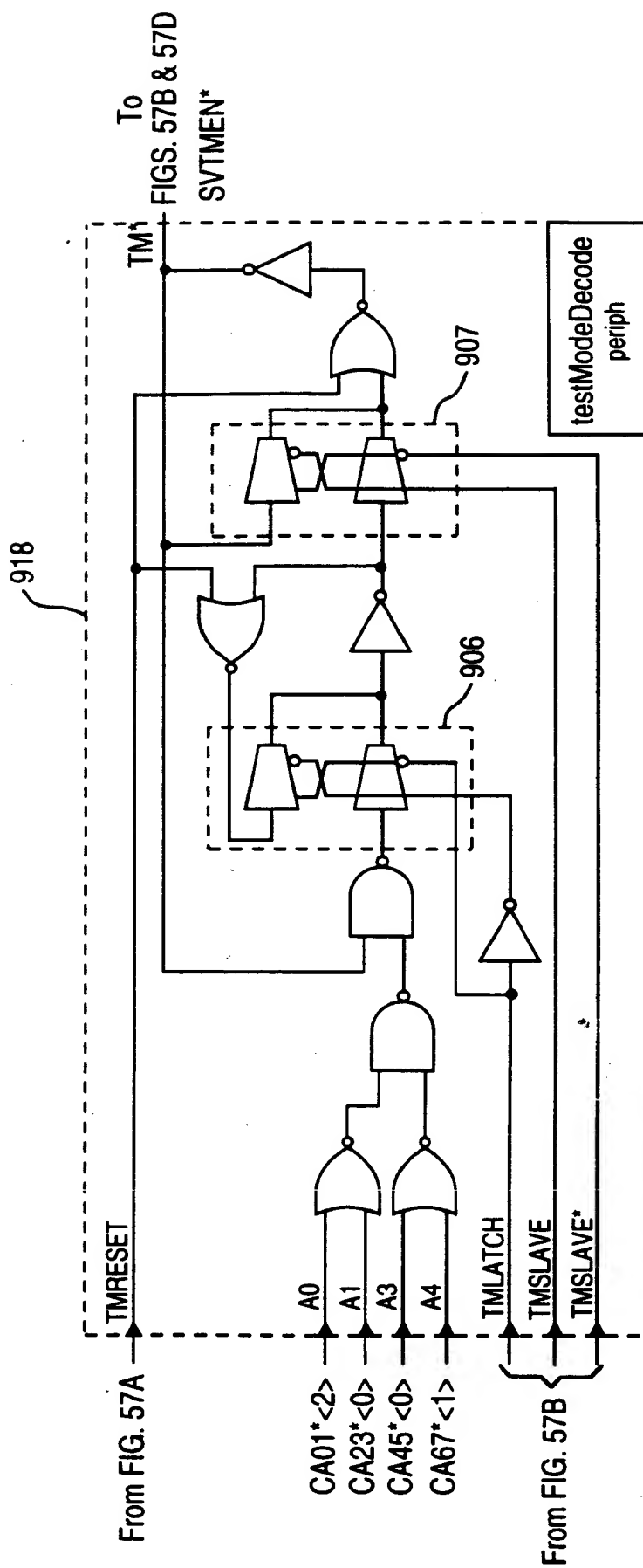


FIG. 57E

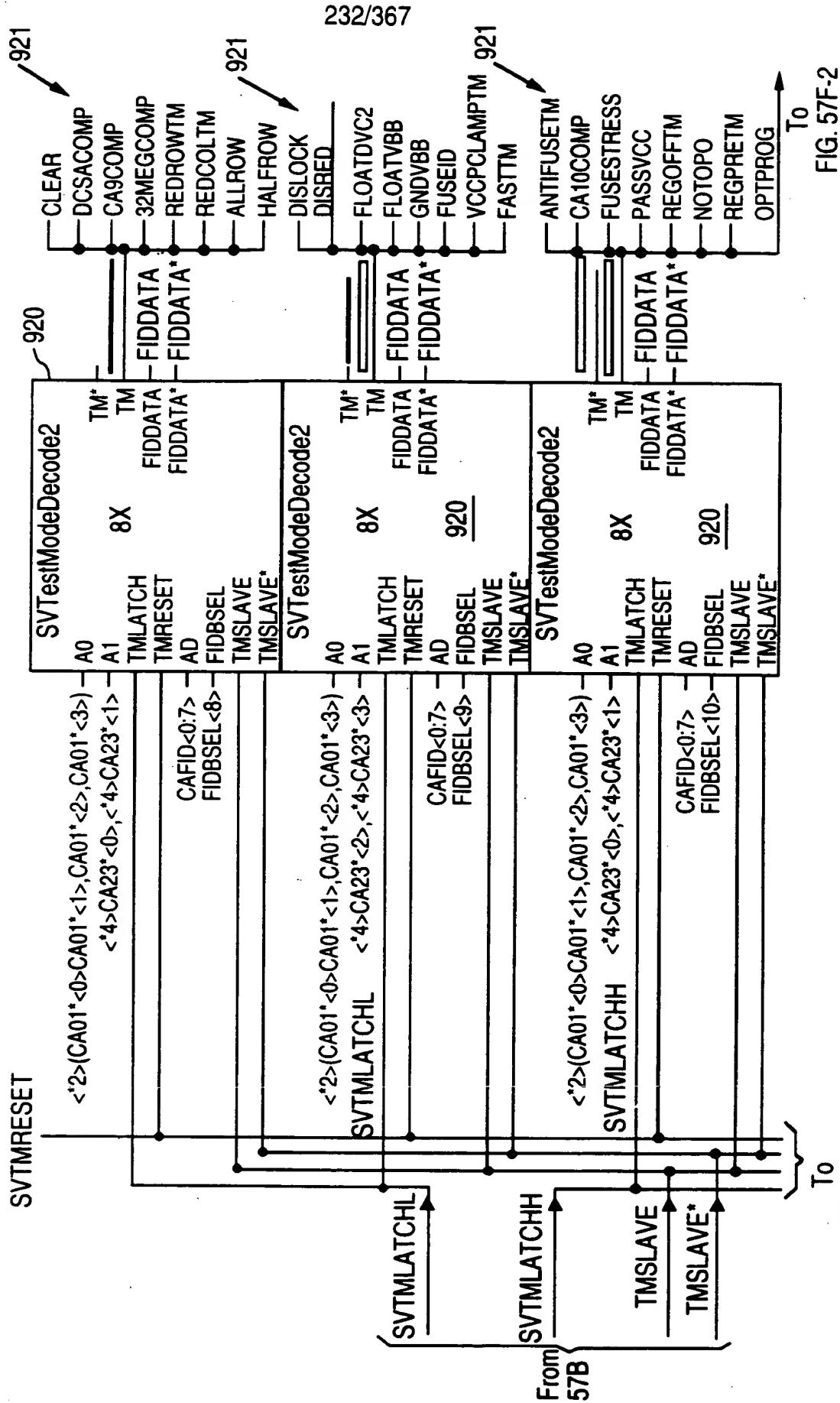


FIG. 57F-3

FIG. 57F-1

FIG. 57F-2

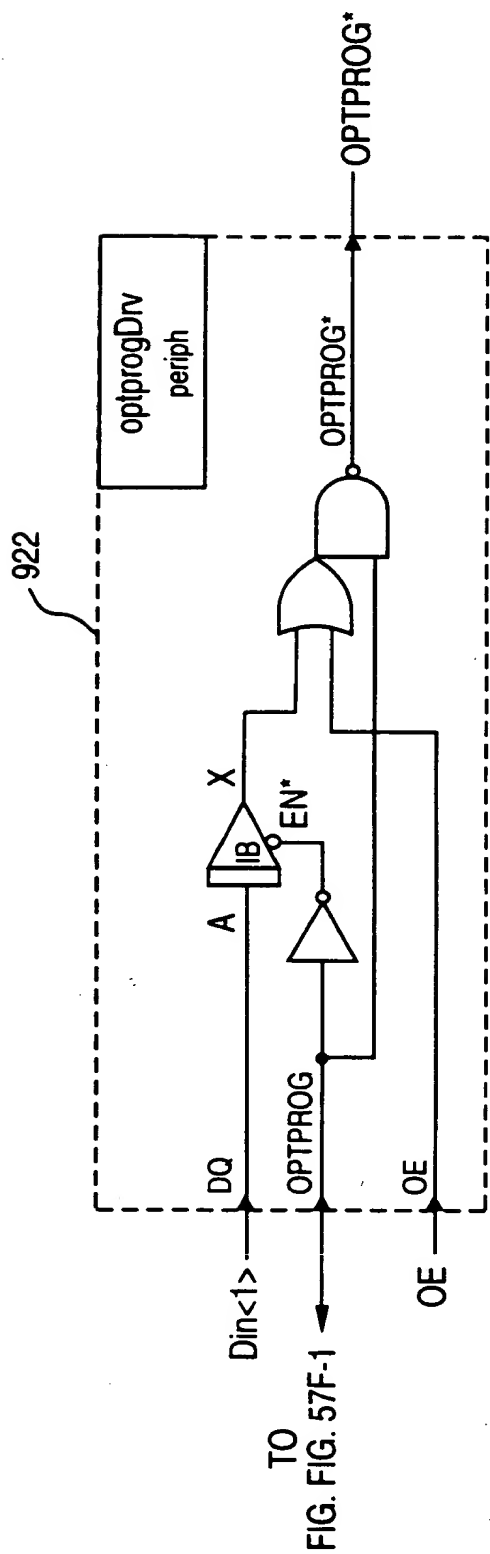
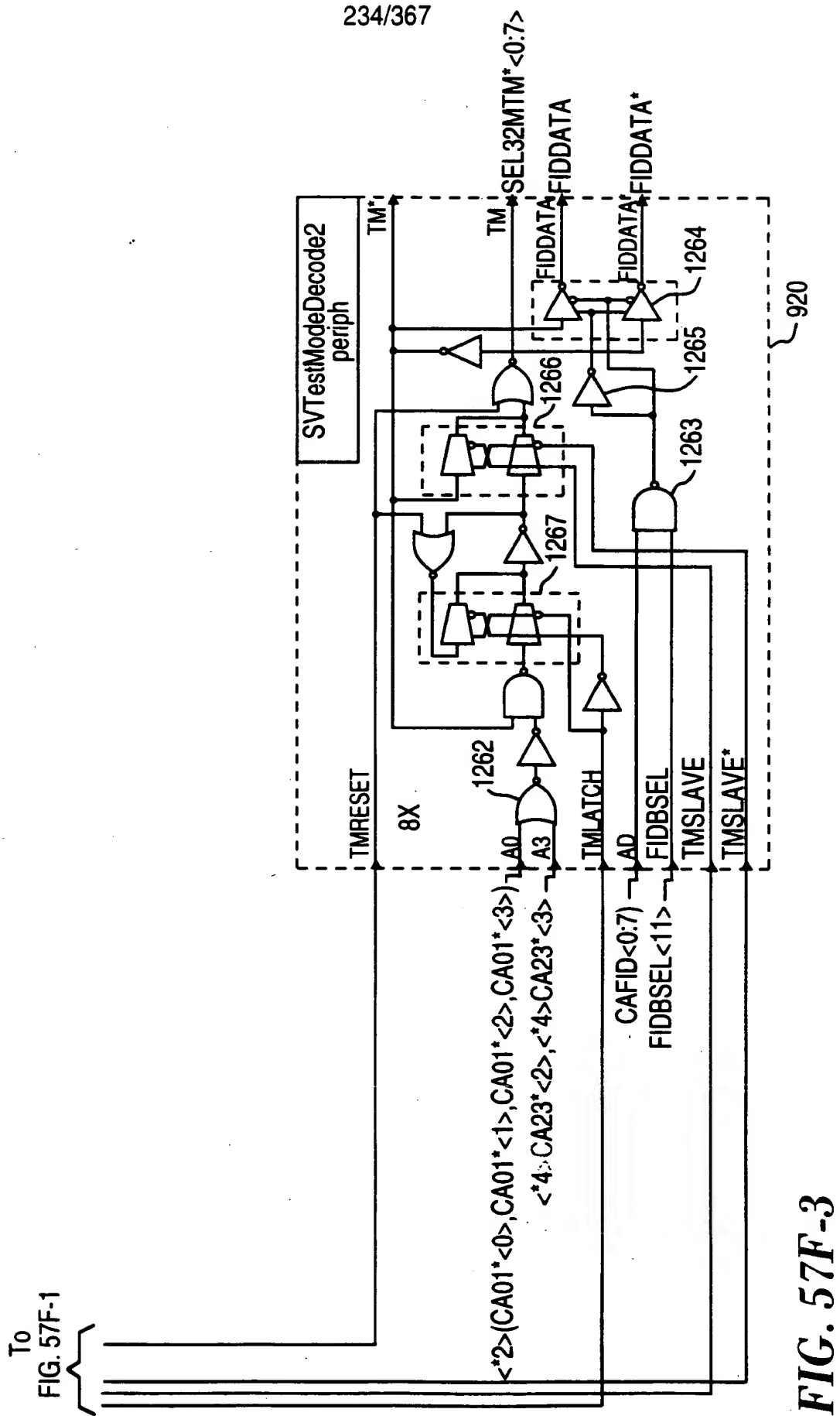


FIG. 57F-2



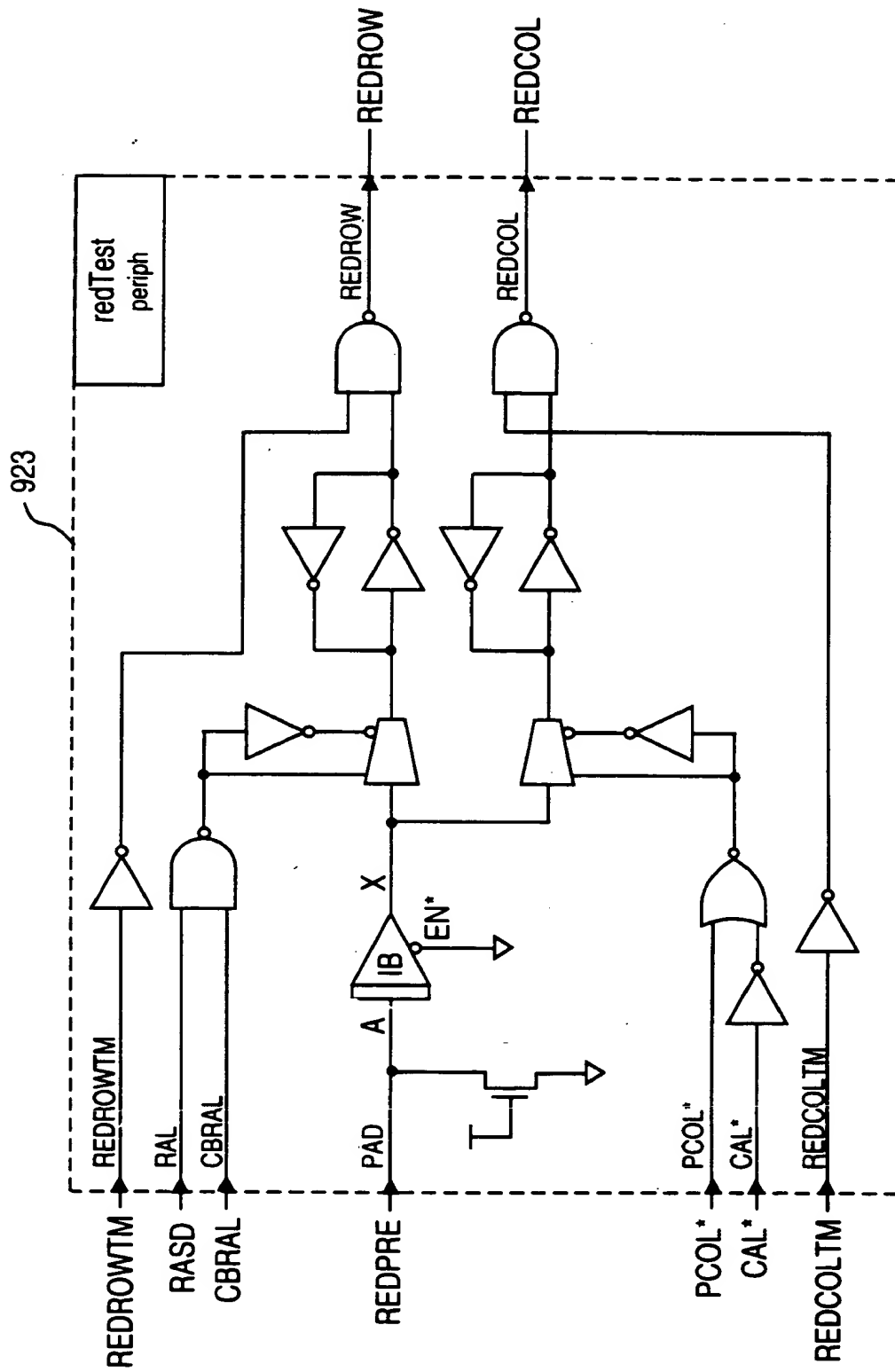


FIG. 57C

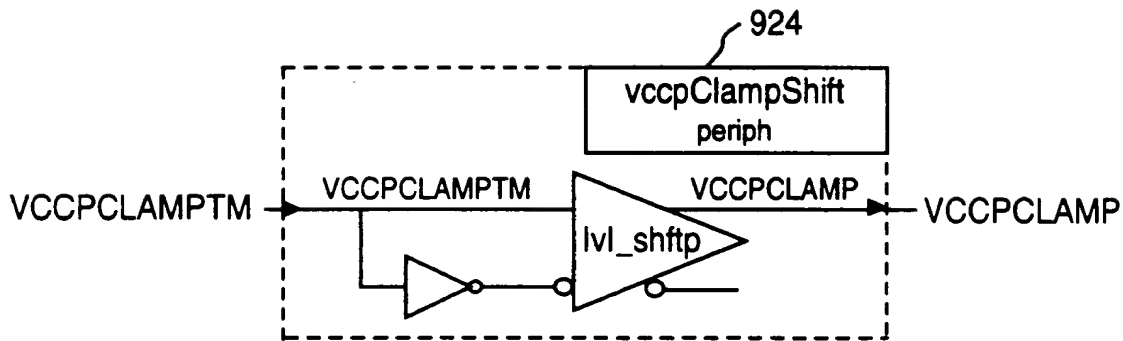
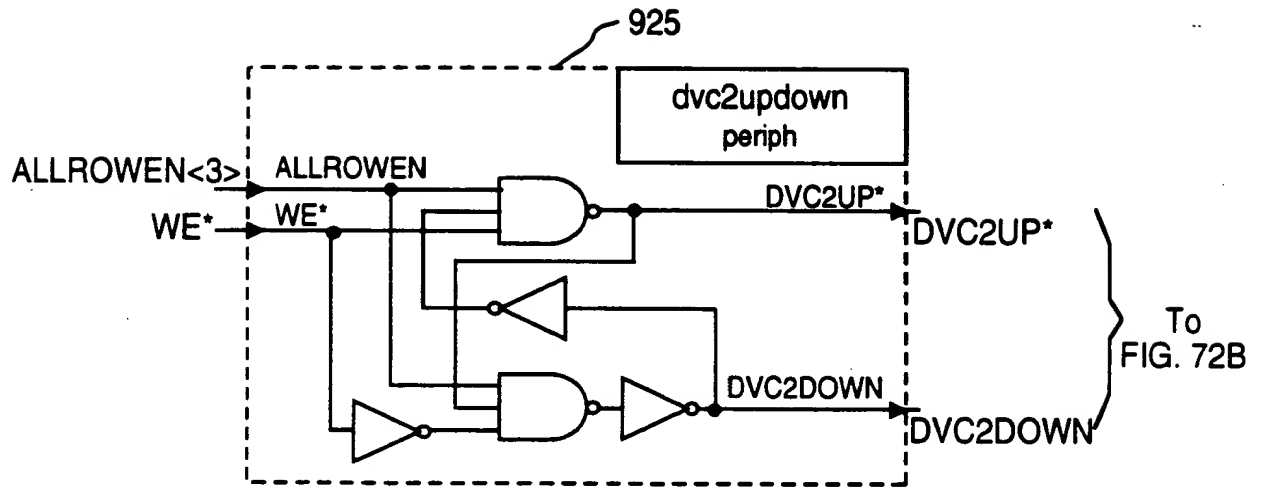
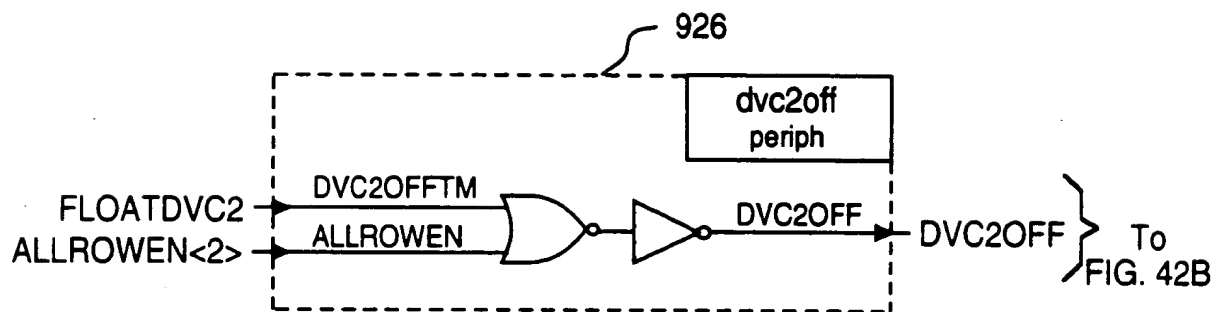


FIG. 57H

**FIG. 57I**

**FIG. 57J**

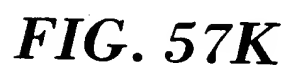
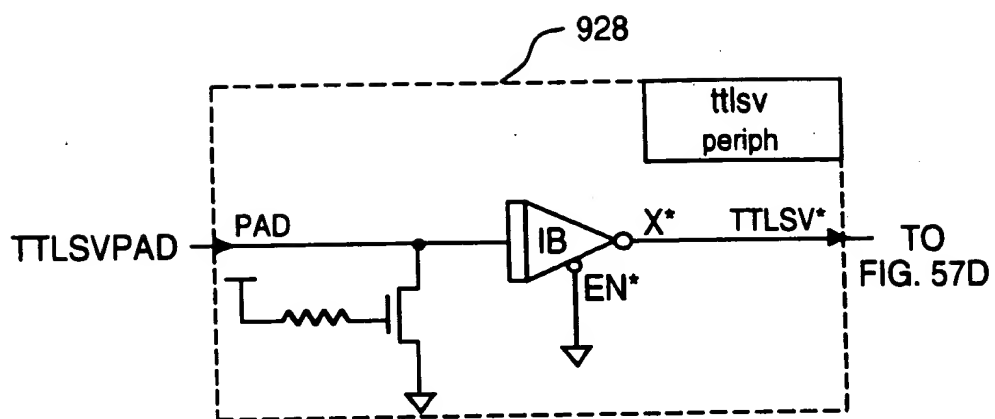


FIG. 57K

**FIG. 57L**

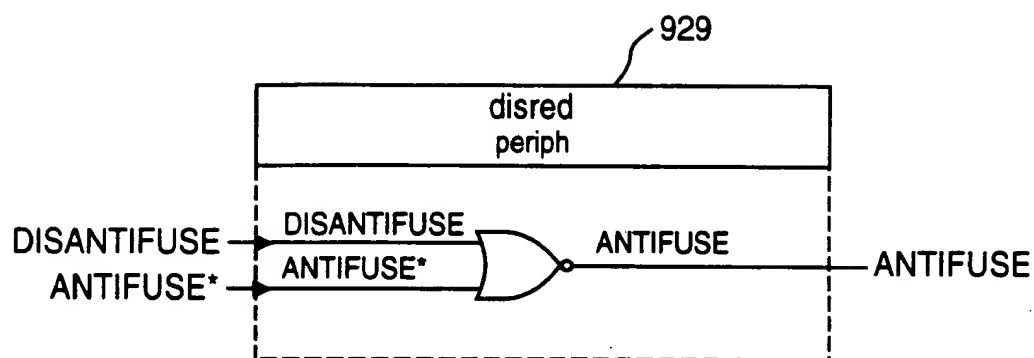


FIG. 57M



FIG. 58A

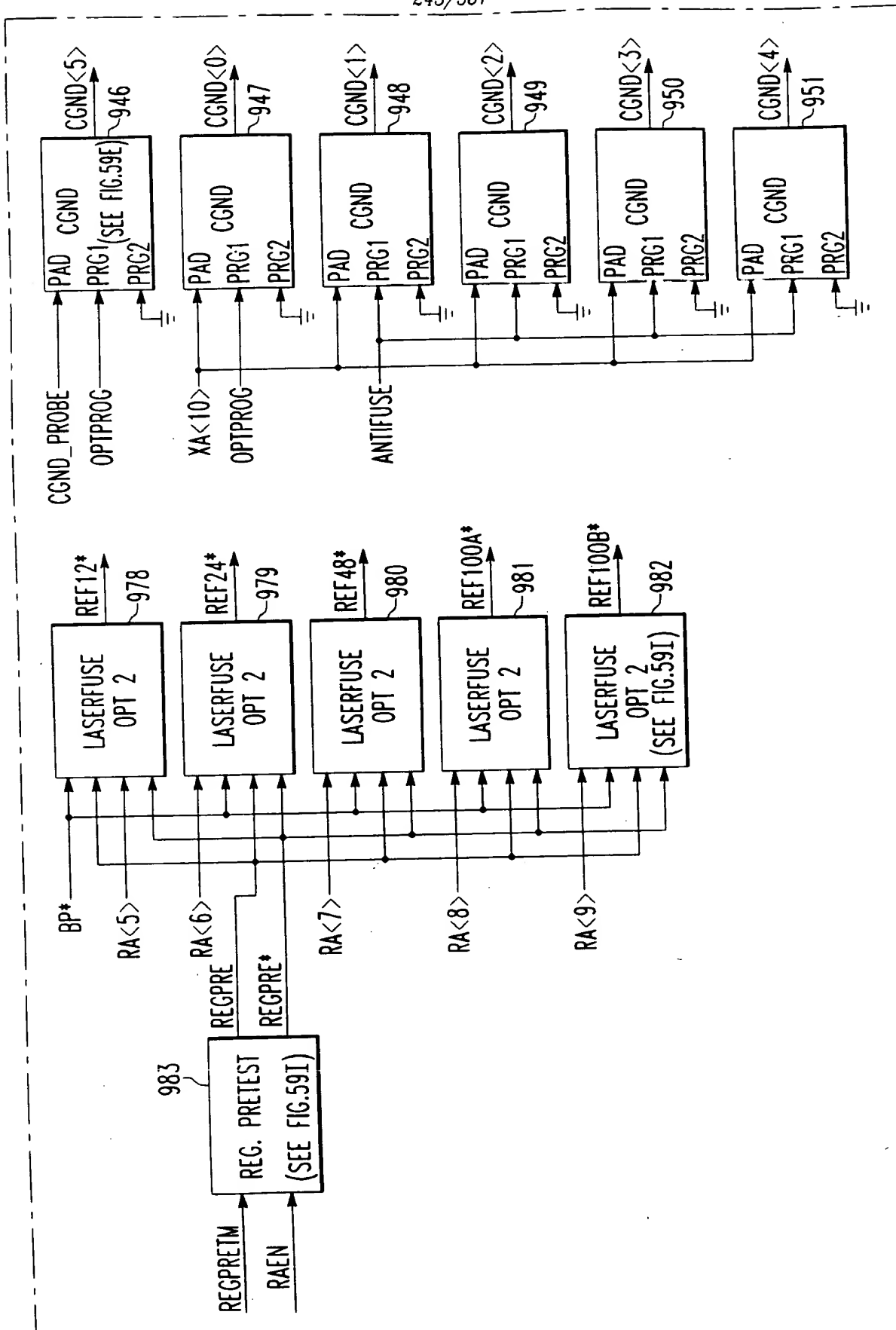


FIG. 58B

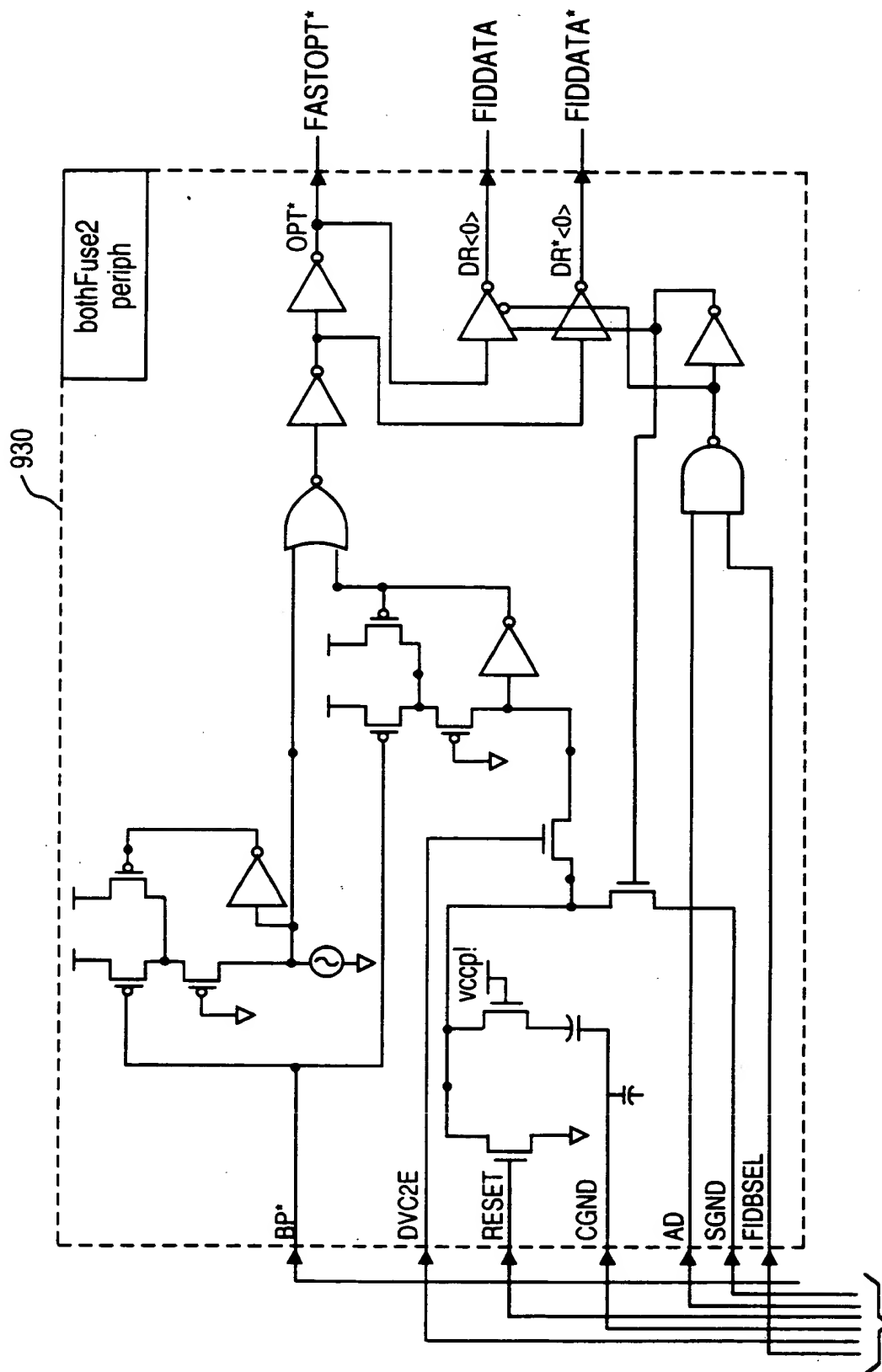


FIG. 59B

FIG. 59A

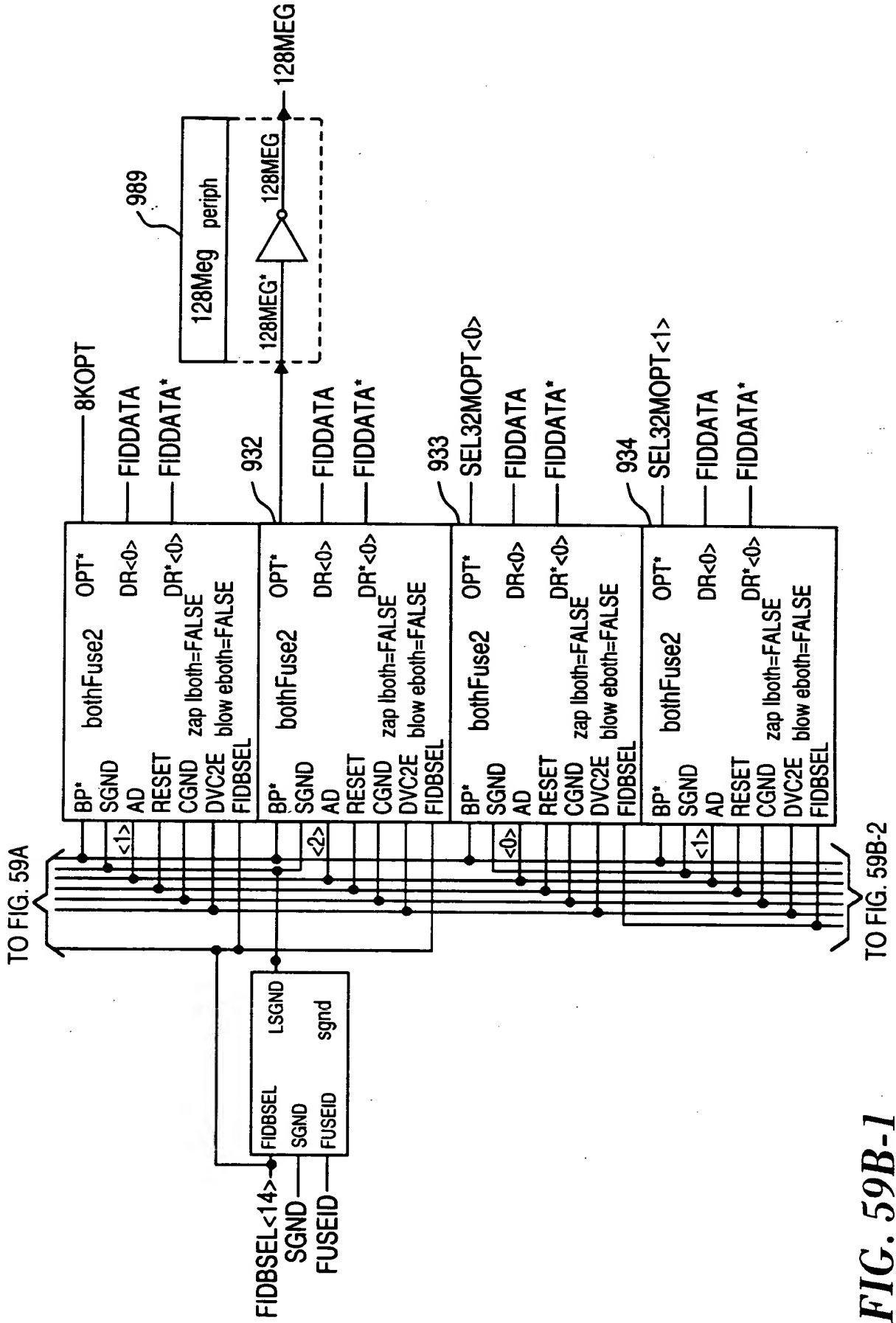


FIG. 59B-1

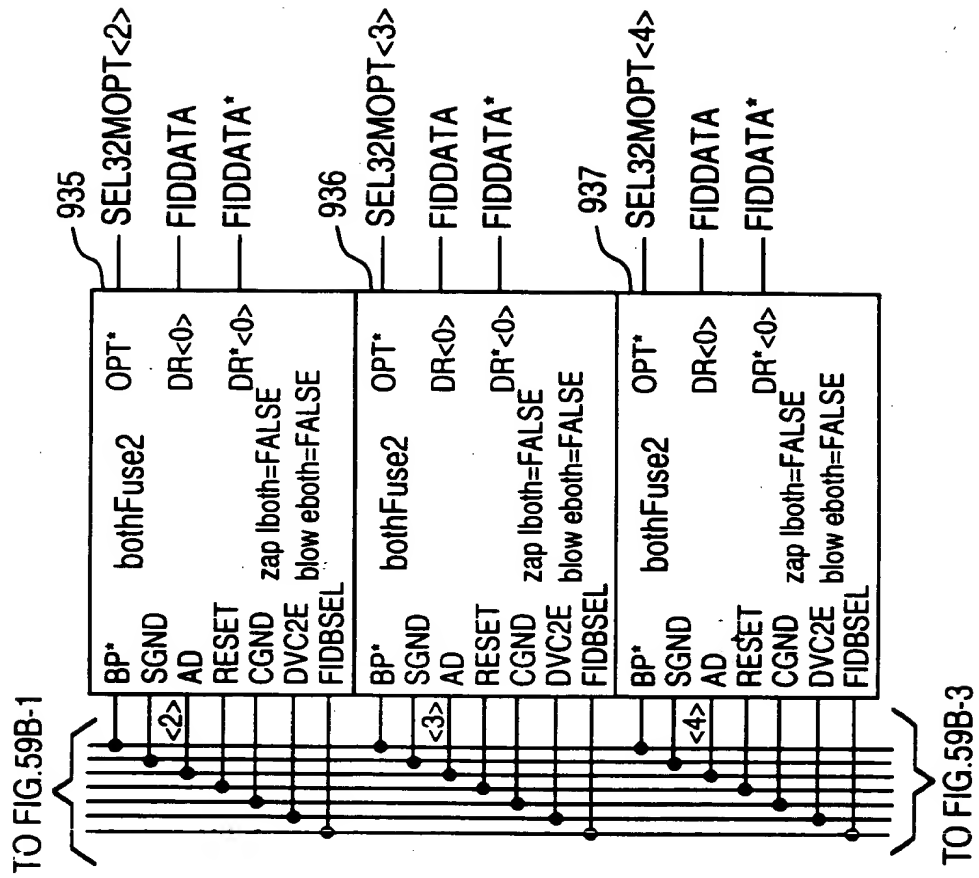


FIG. 59B-2

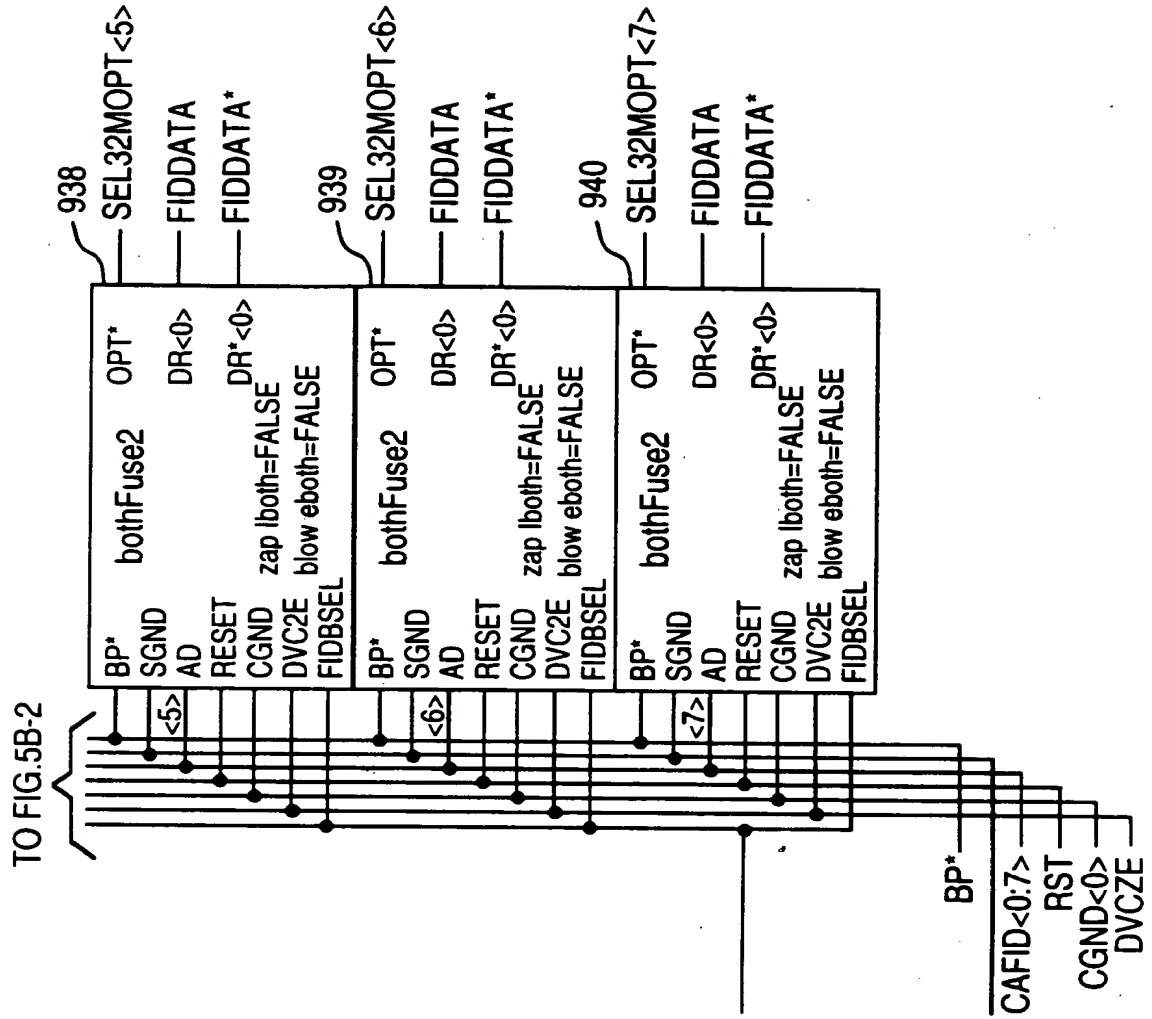
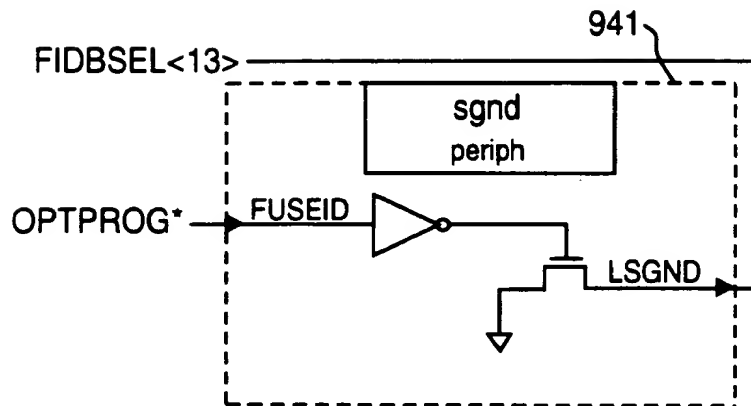


FIG. 59B-3

**FIG. 59C**

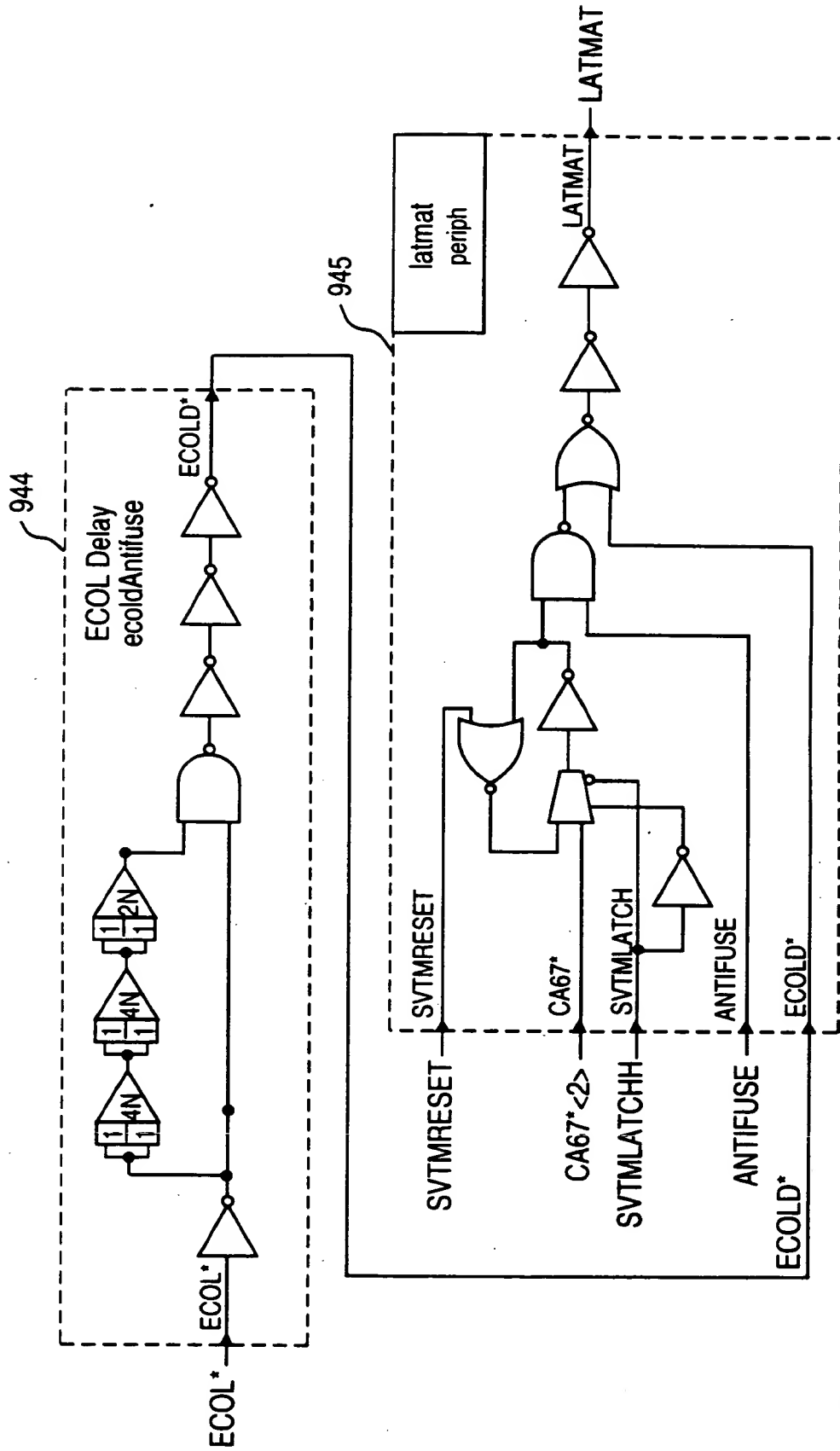


FIG. 59D

250/367

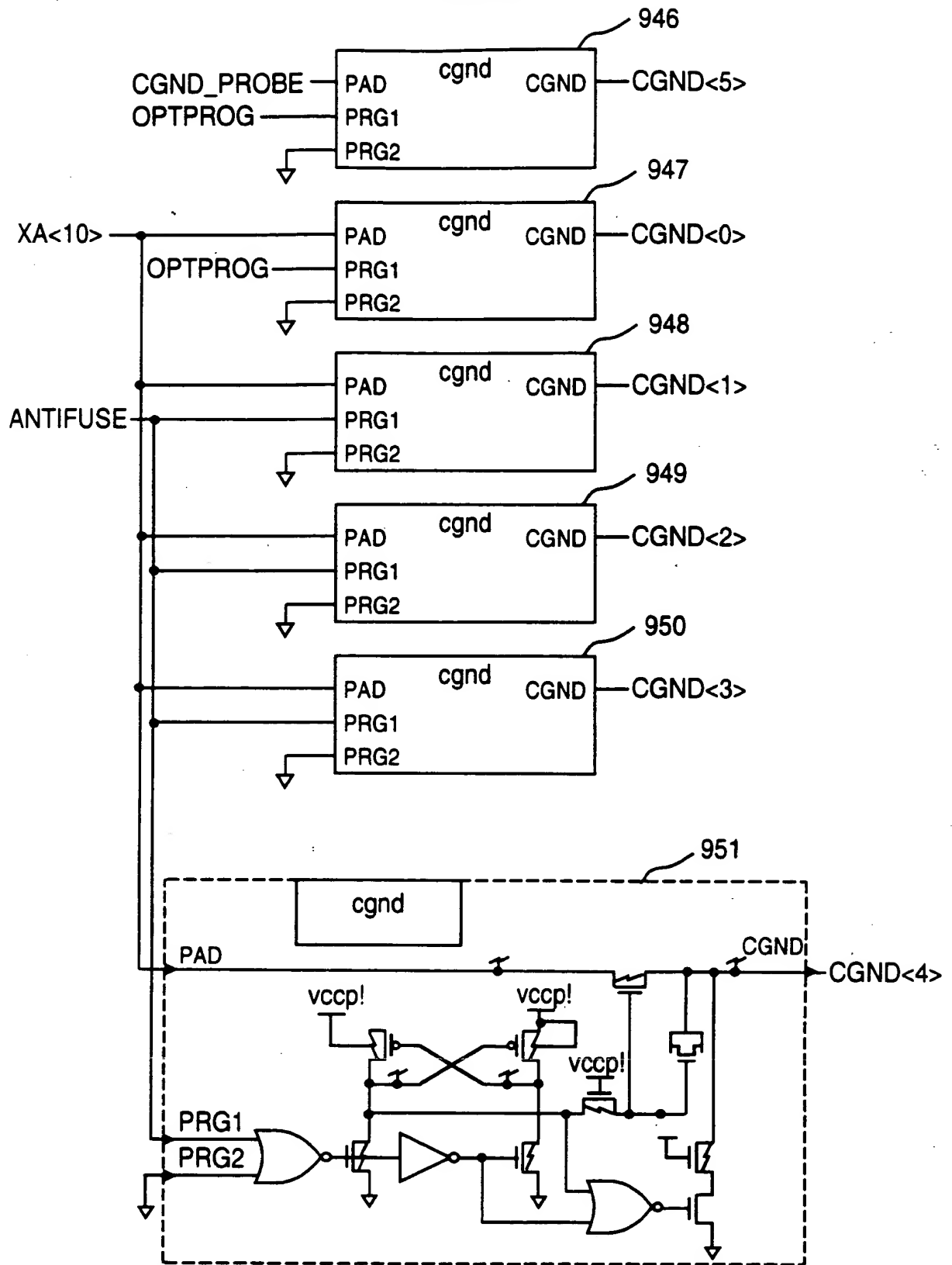


FIG. 59E

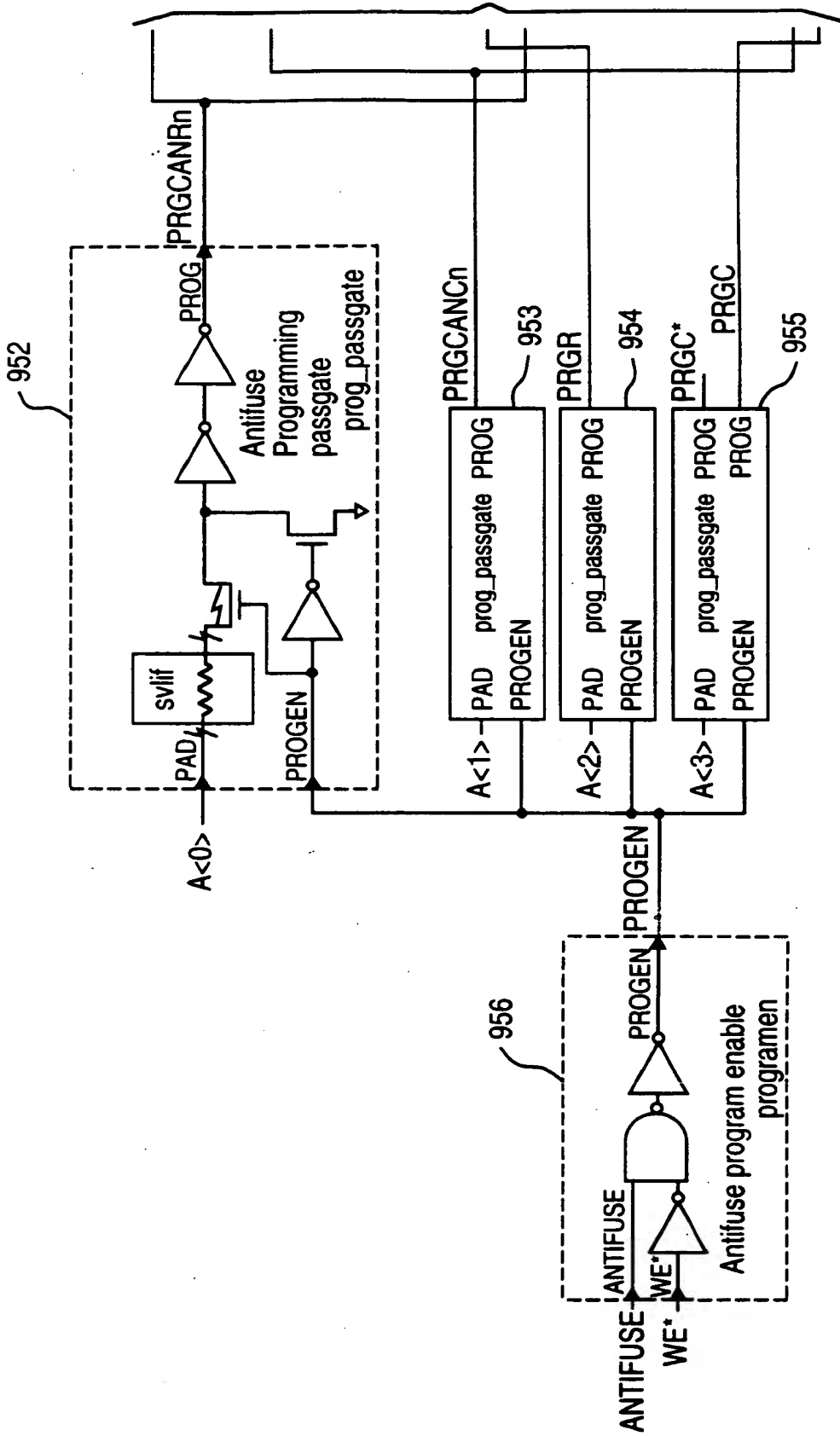
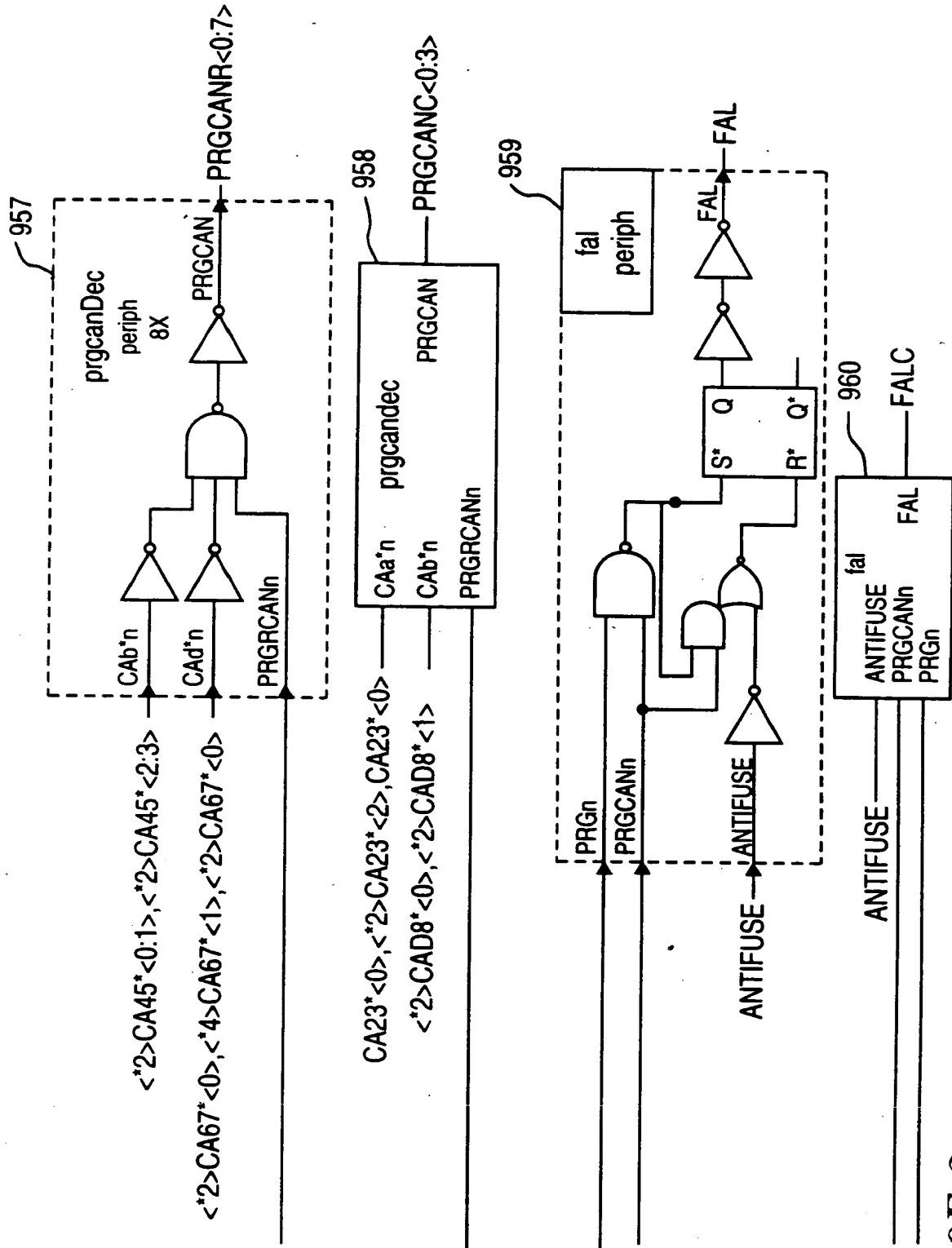


FIG. 59F-1



To
FIG.
59F-1

FIG. 59F-2

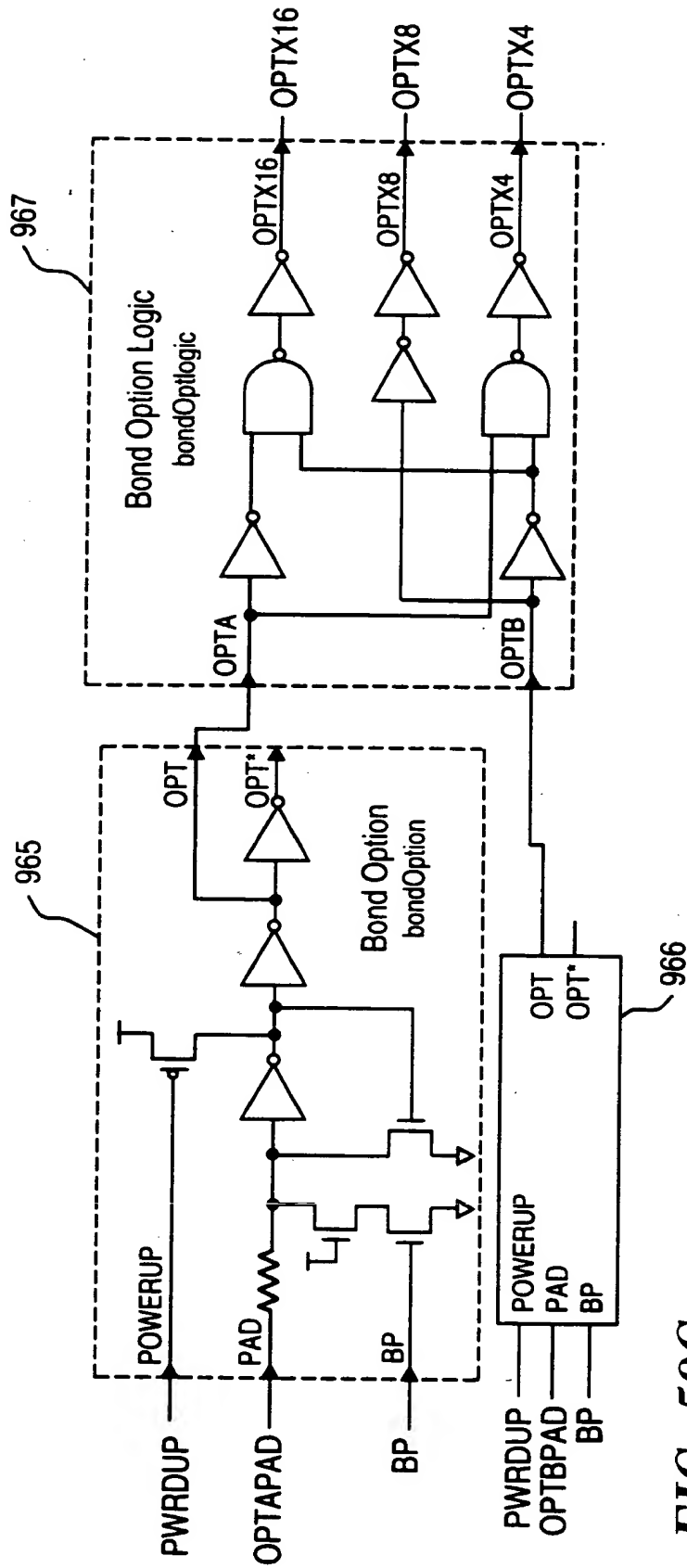


FIG. 59C

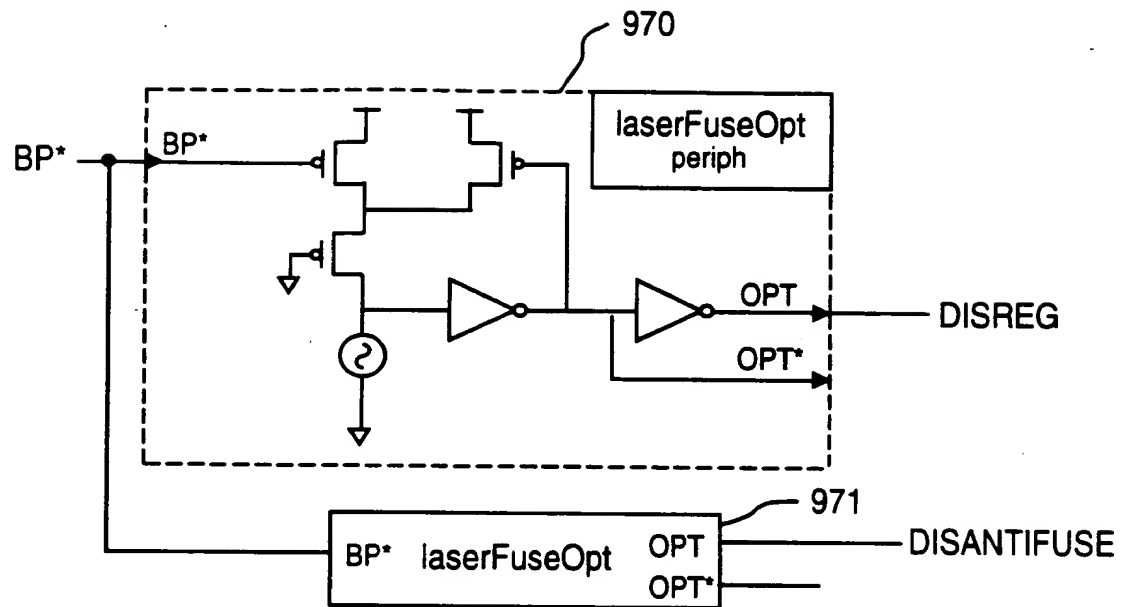


FIG. 59H

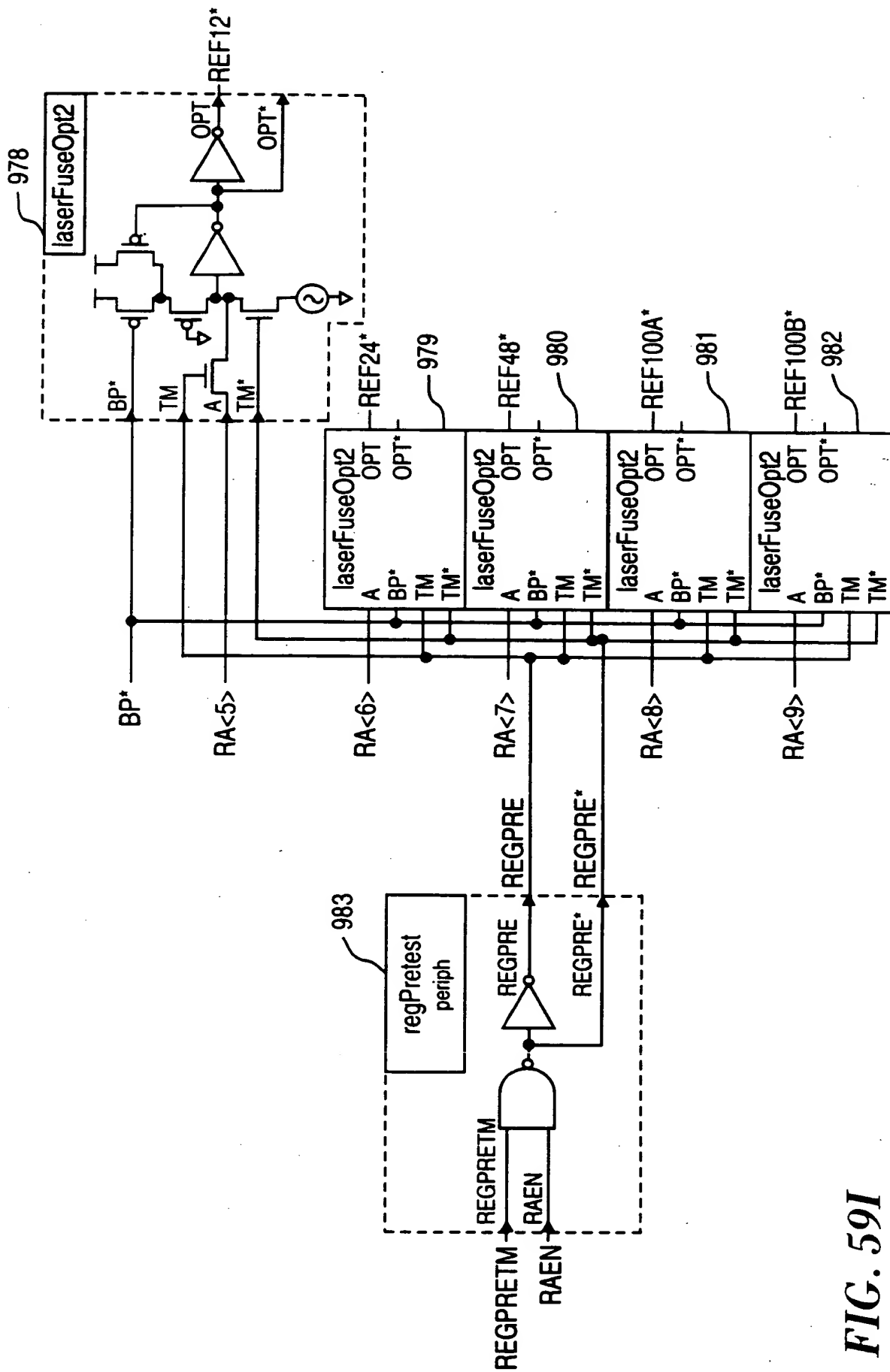
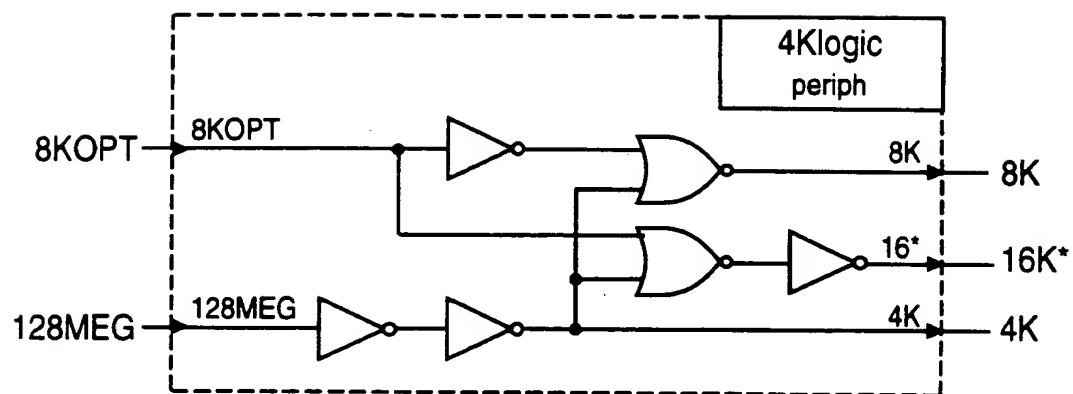


FIG. 591

**FIG. 59J**

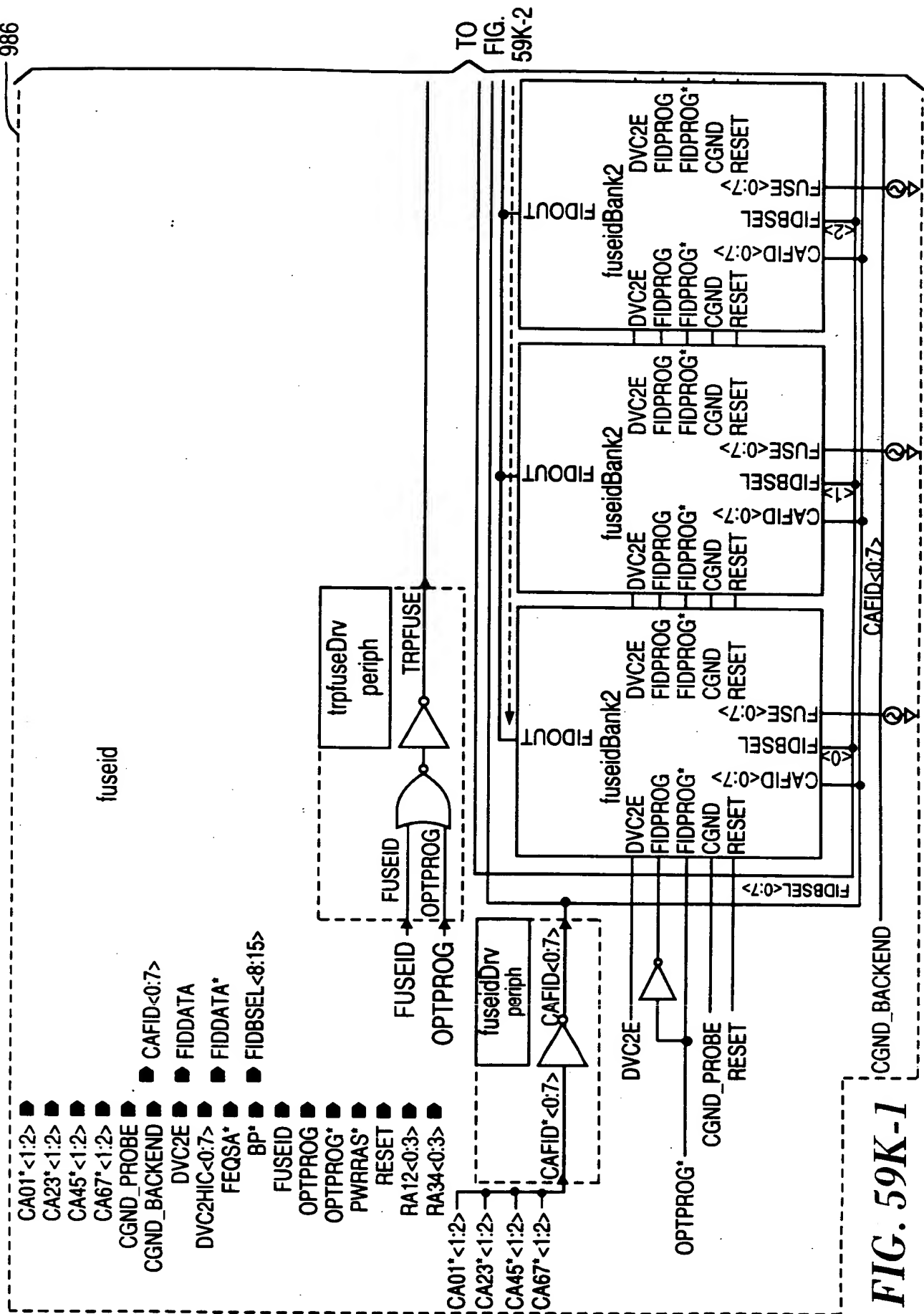


FIG. 59K-1

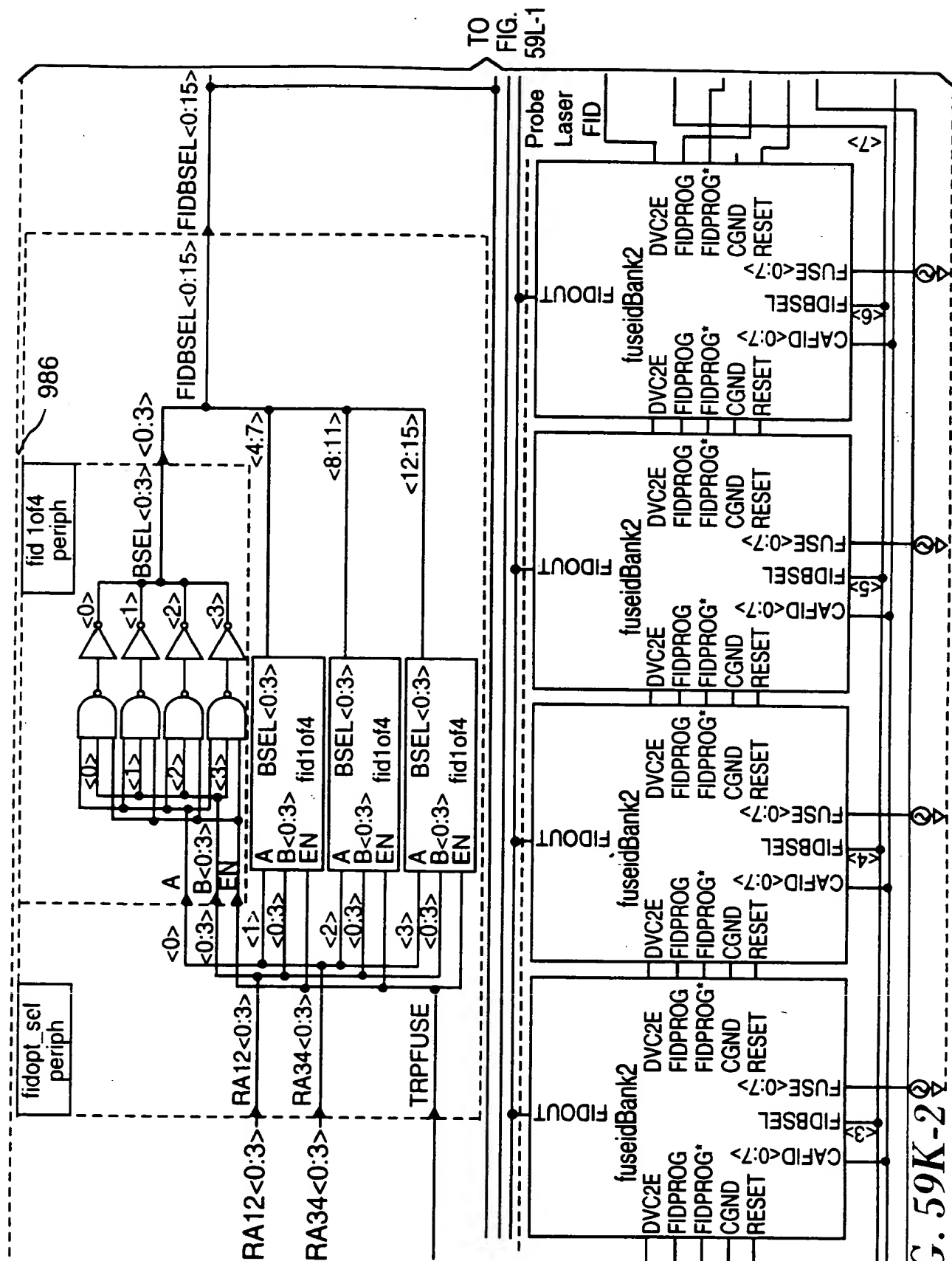
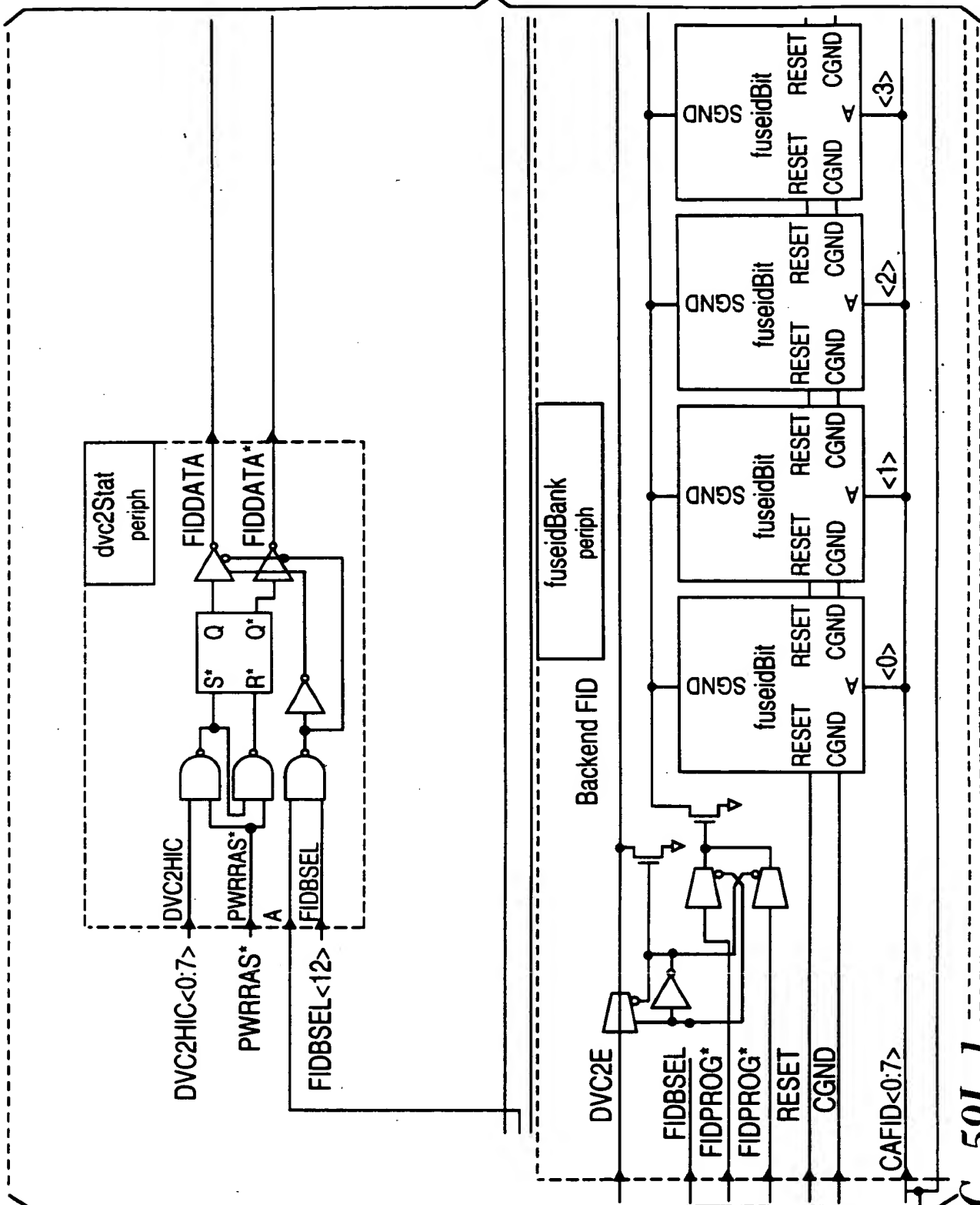


FIG. 59K-29.

TO
FIG.
59L-2



TO
FIG.
59K-1

FIG. 59L-1

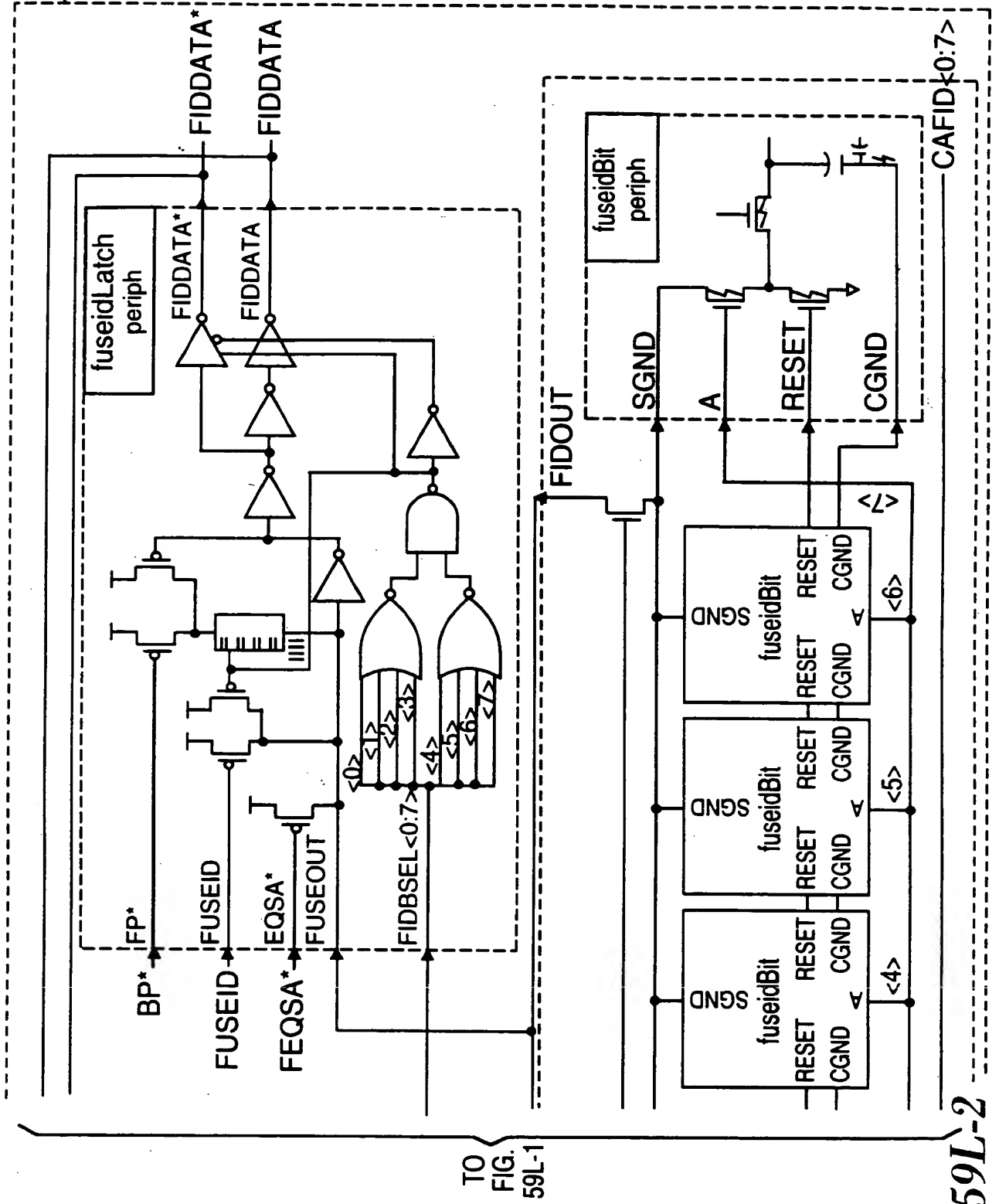


FIG. 59L-2

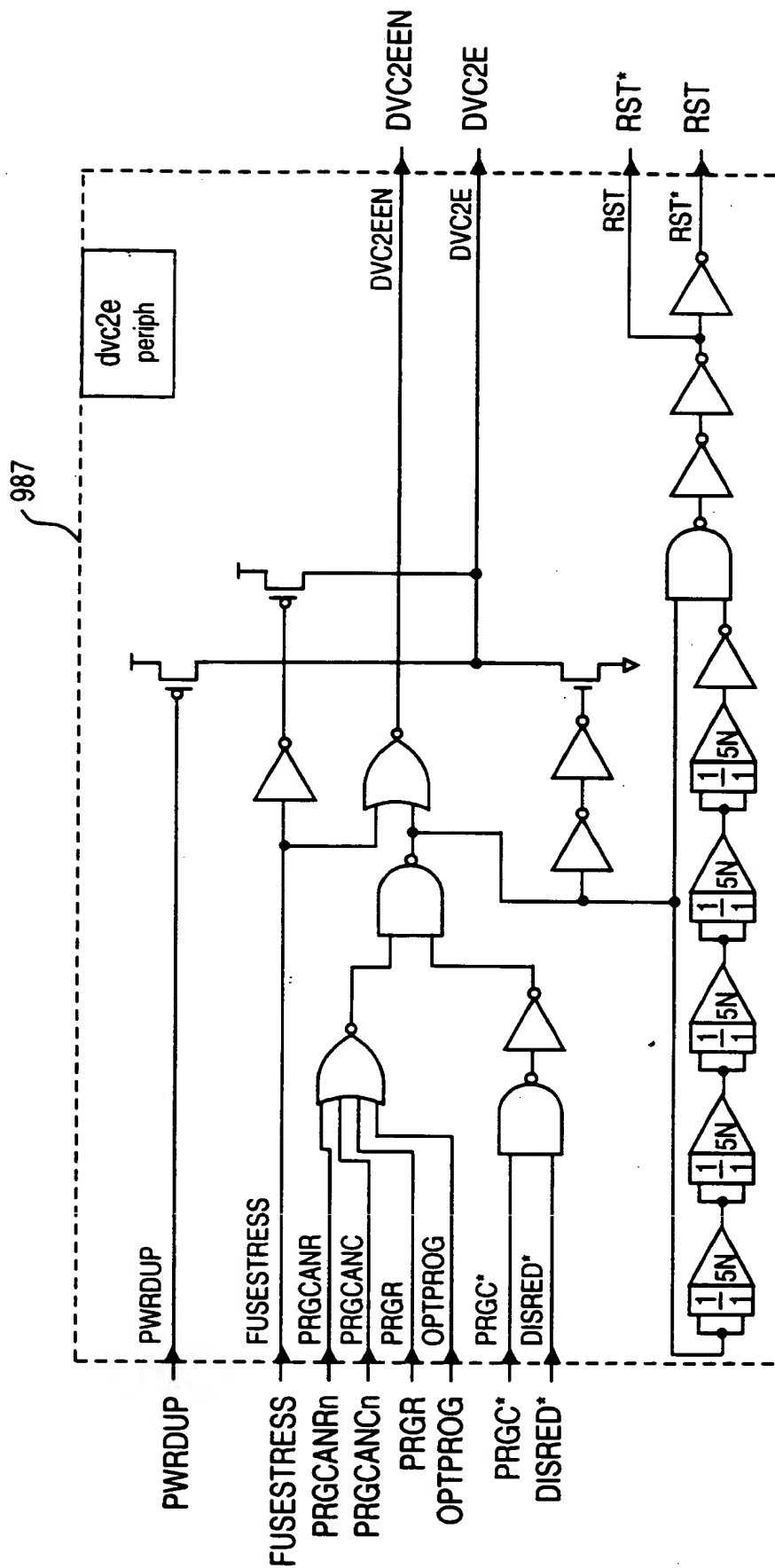
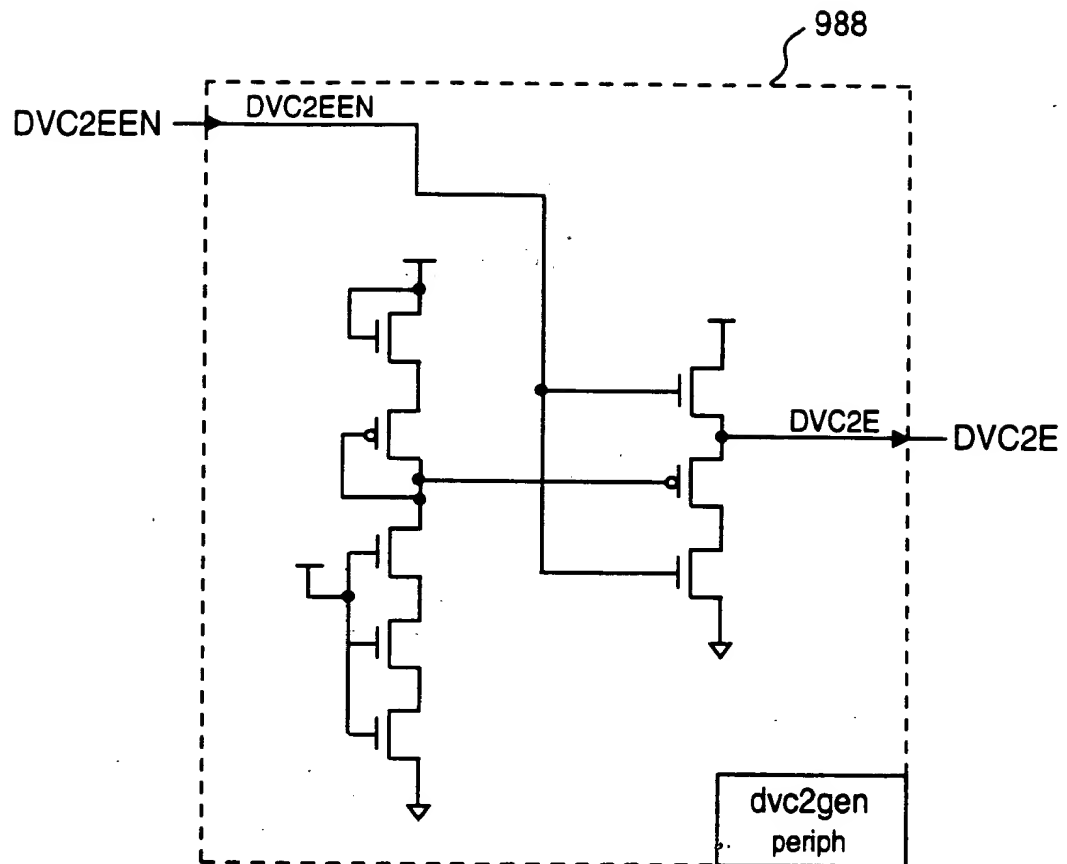


FIG. 59M



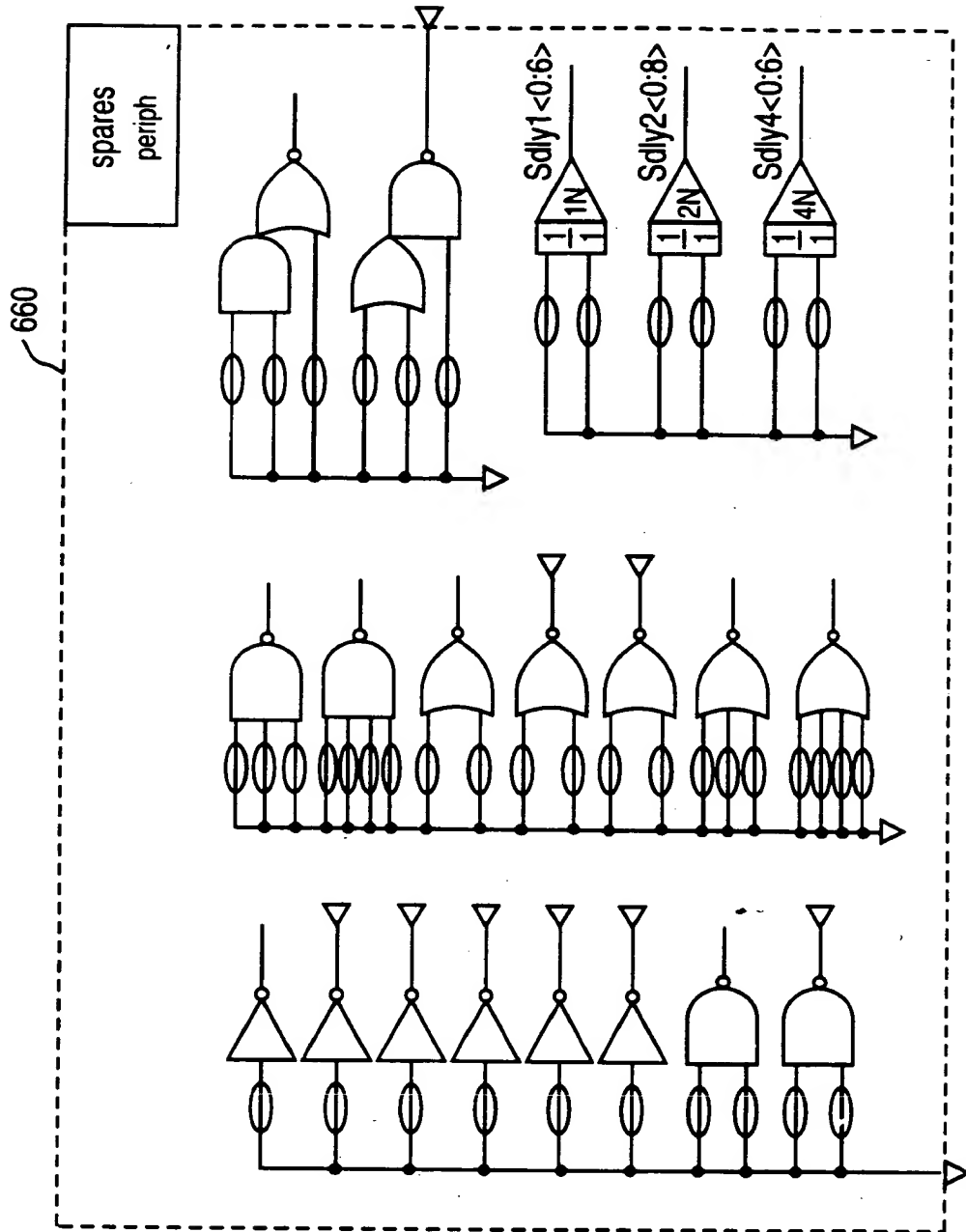
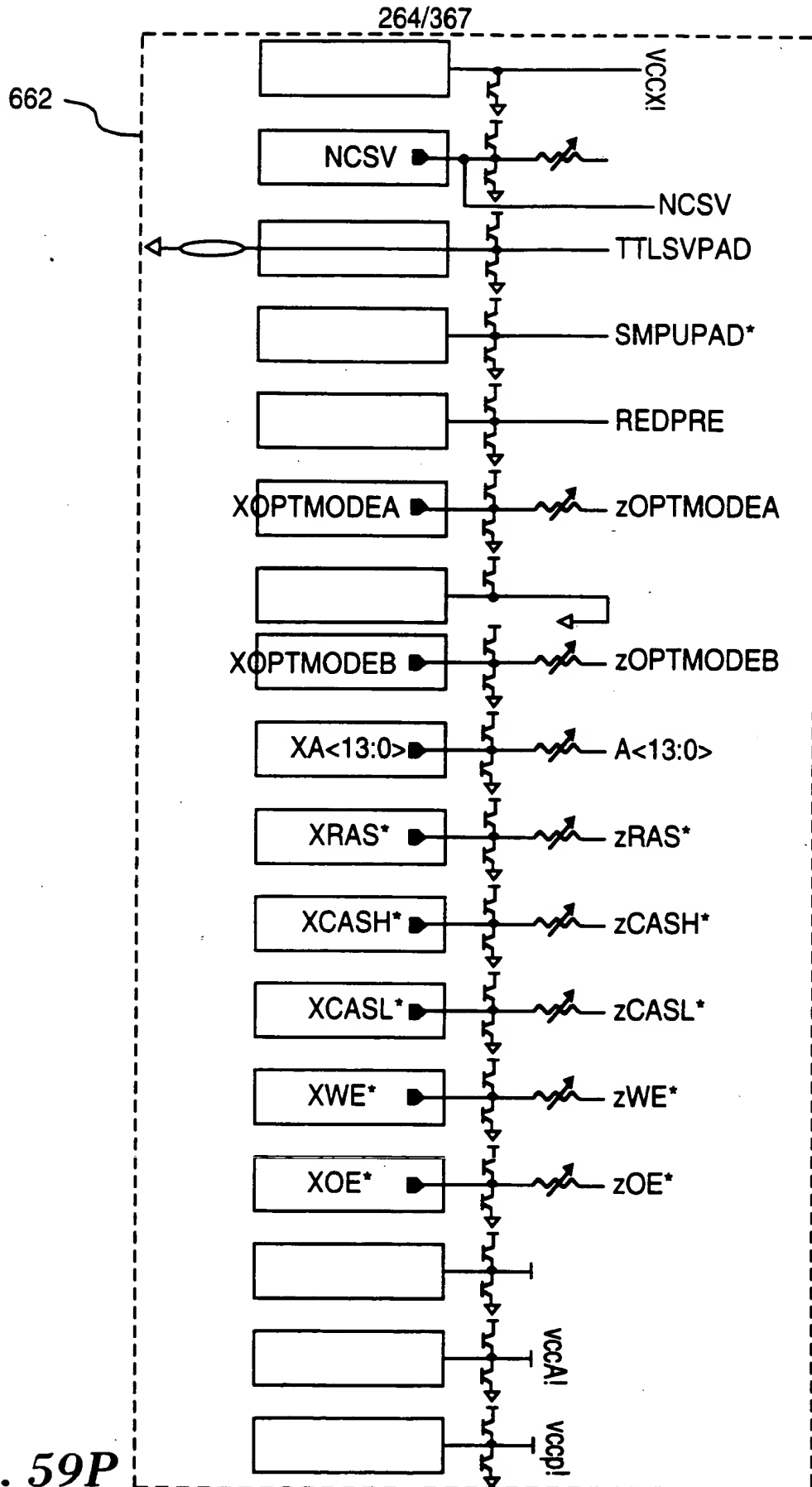


FIG. 590



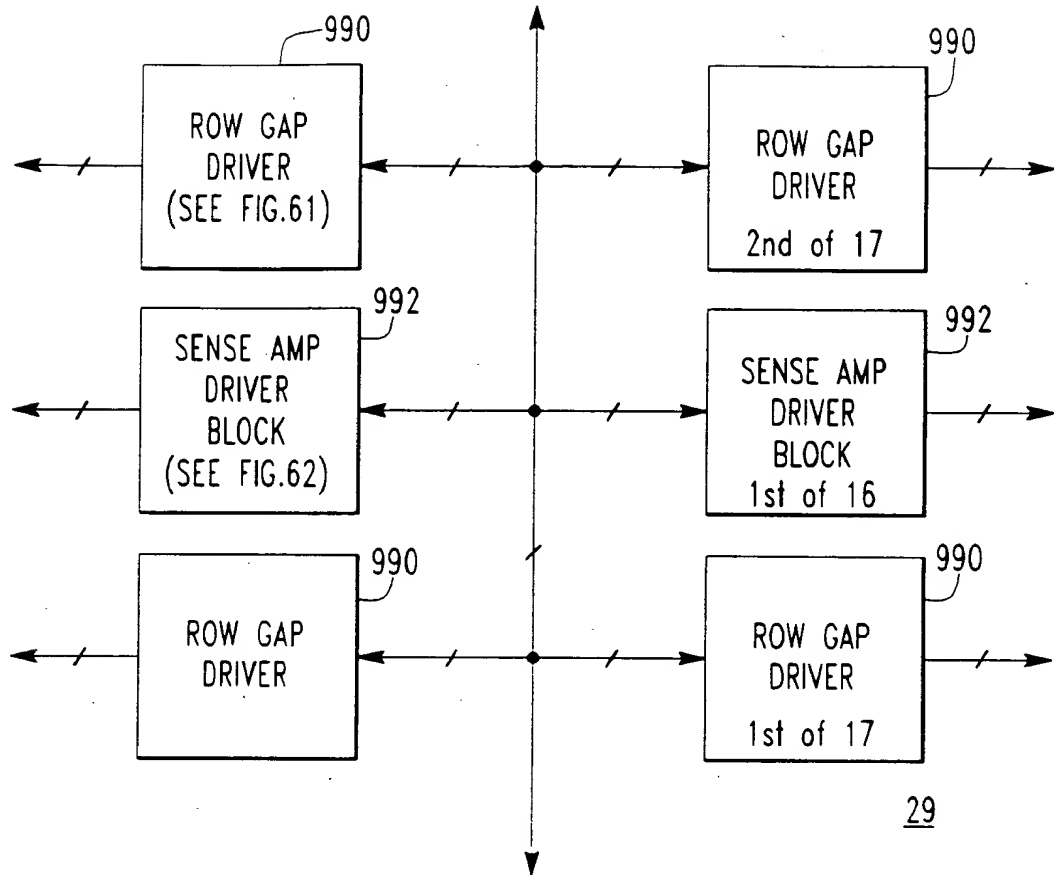


FIG. 60

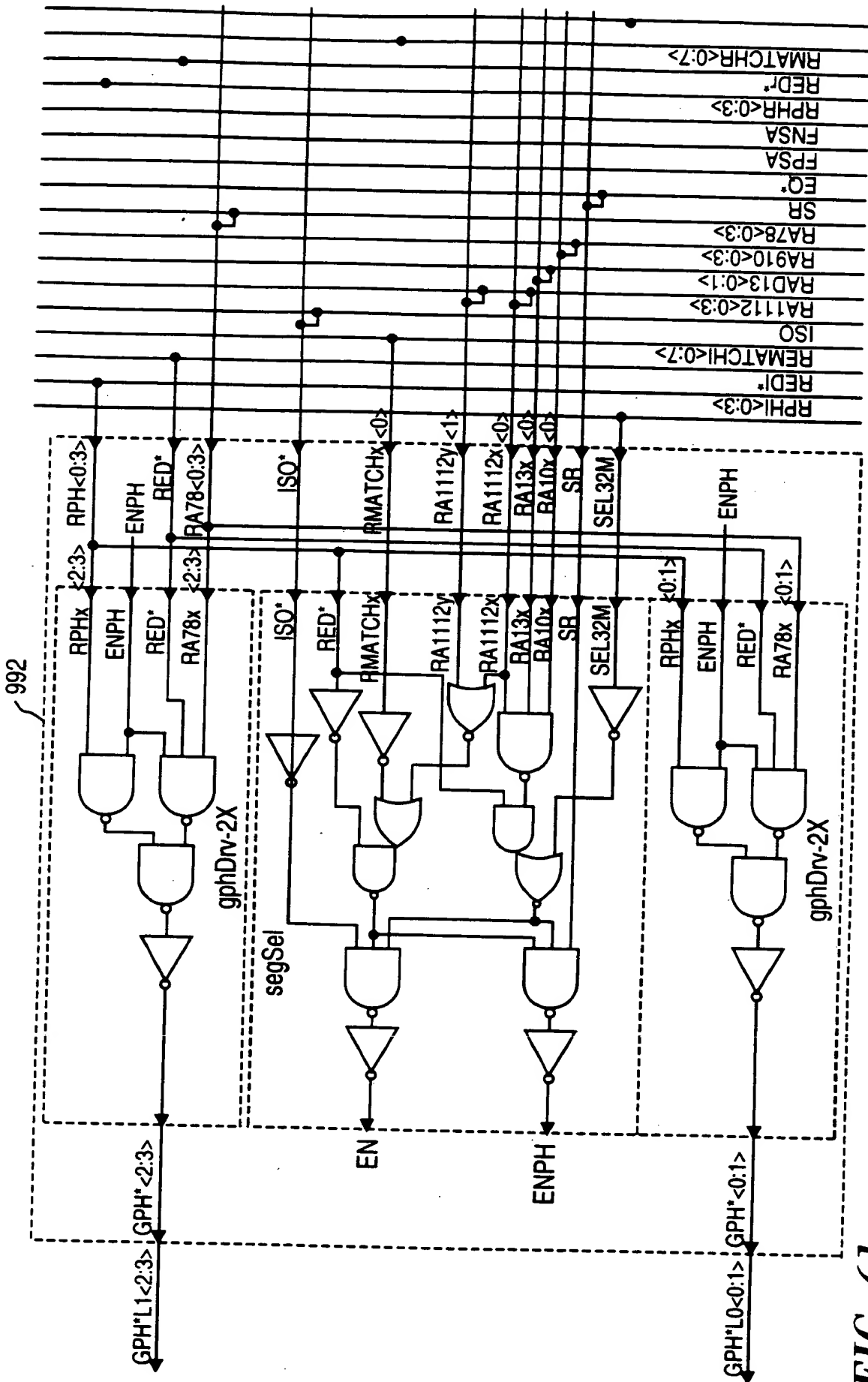


FIG. 61

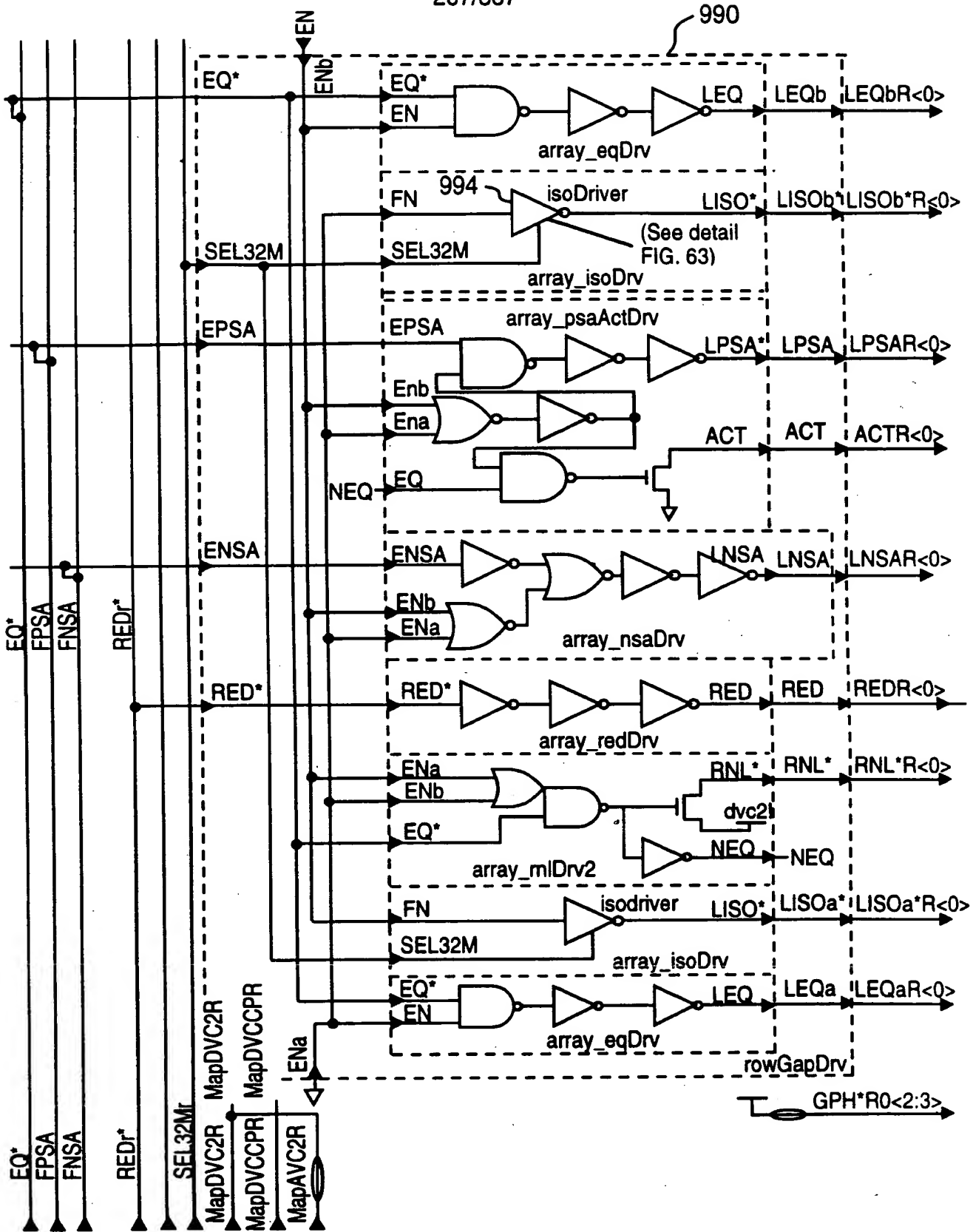


FIG. 62

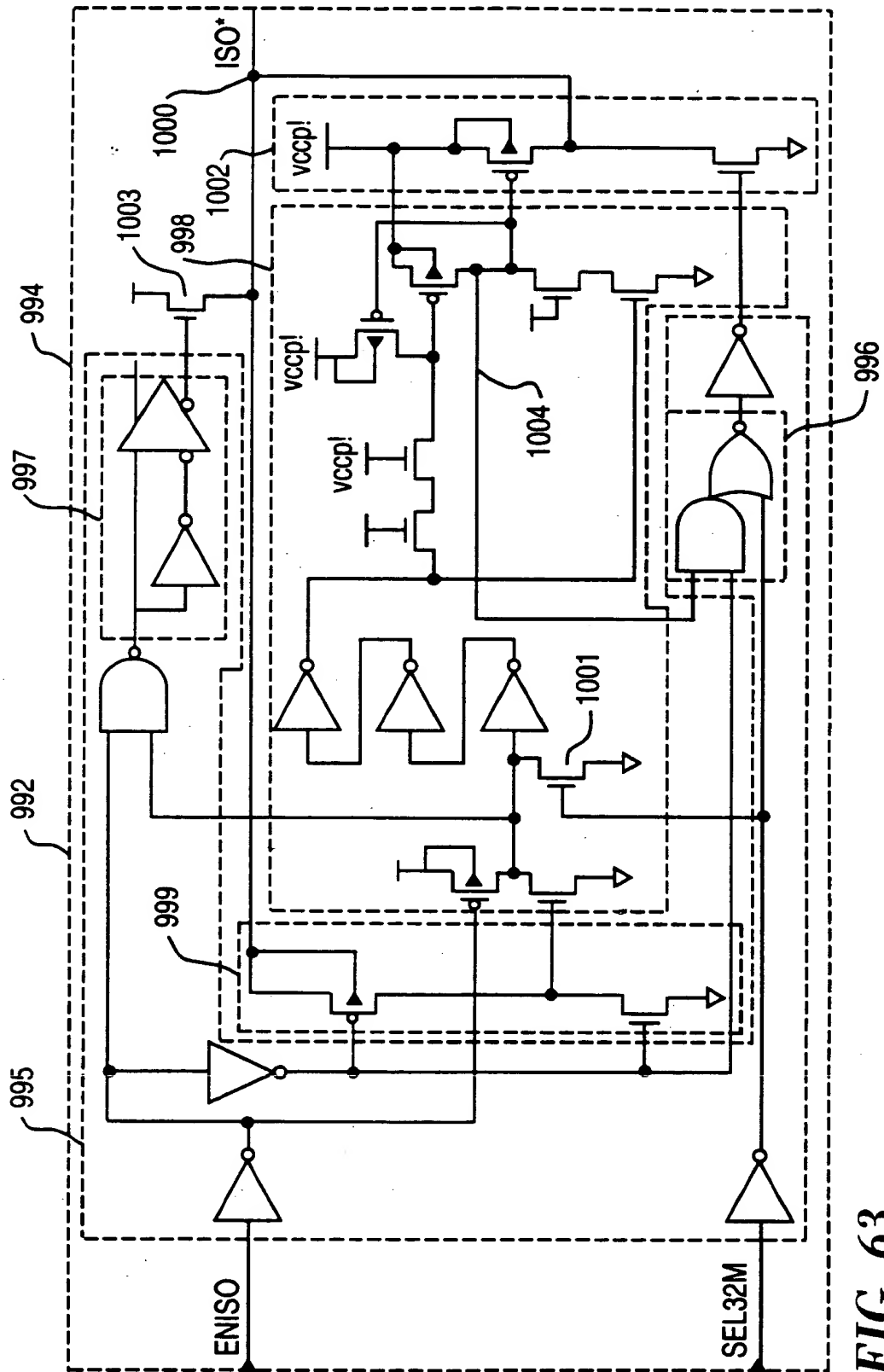


FIG. 63

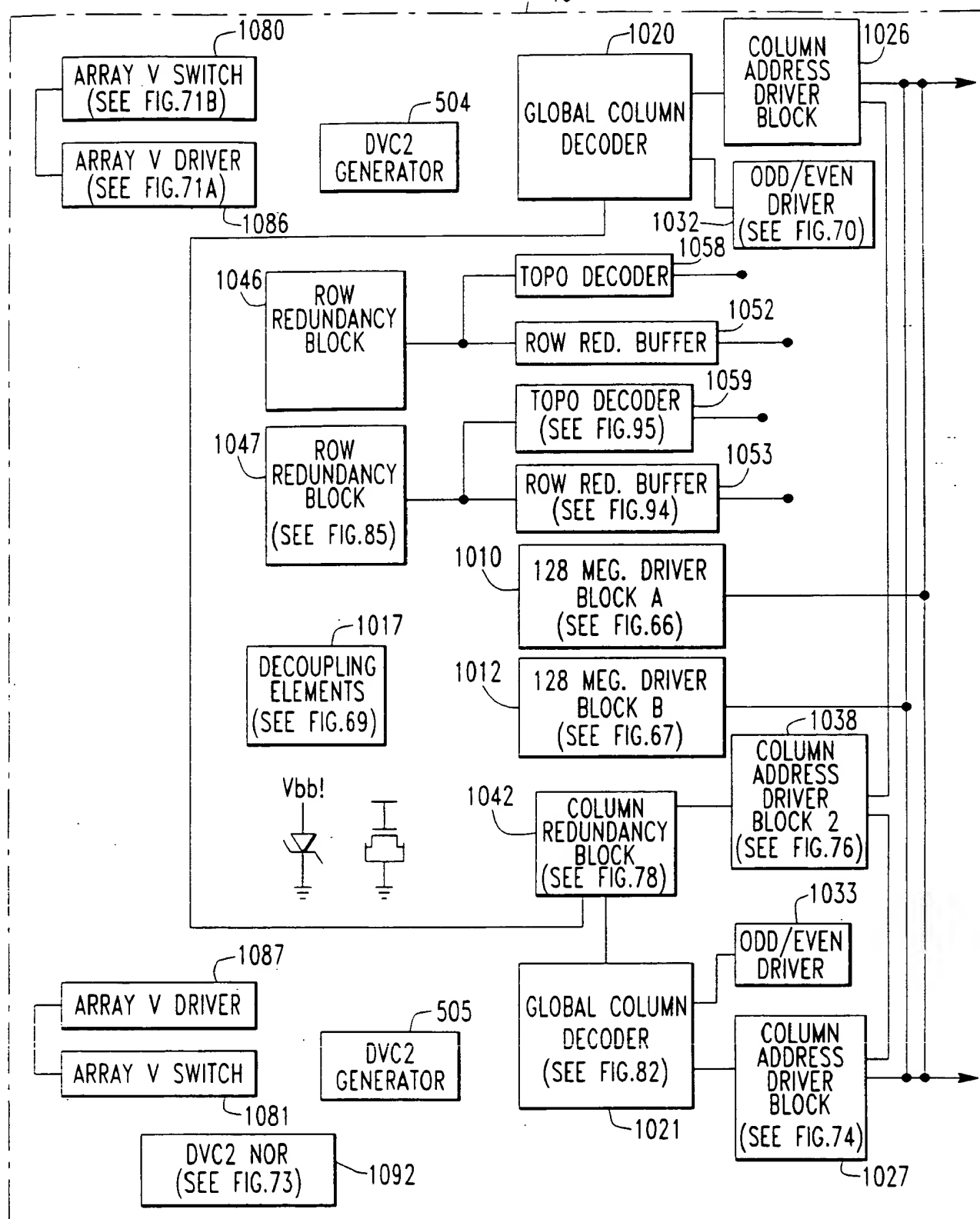


FIG. 64A

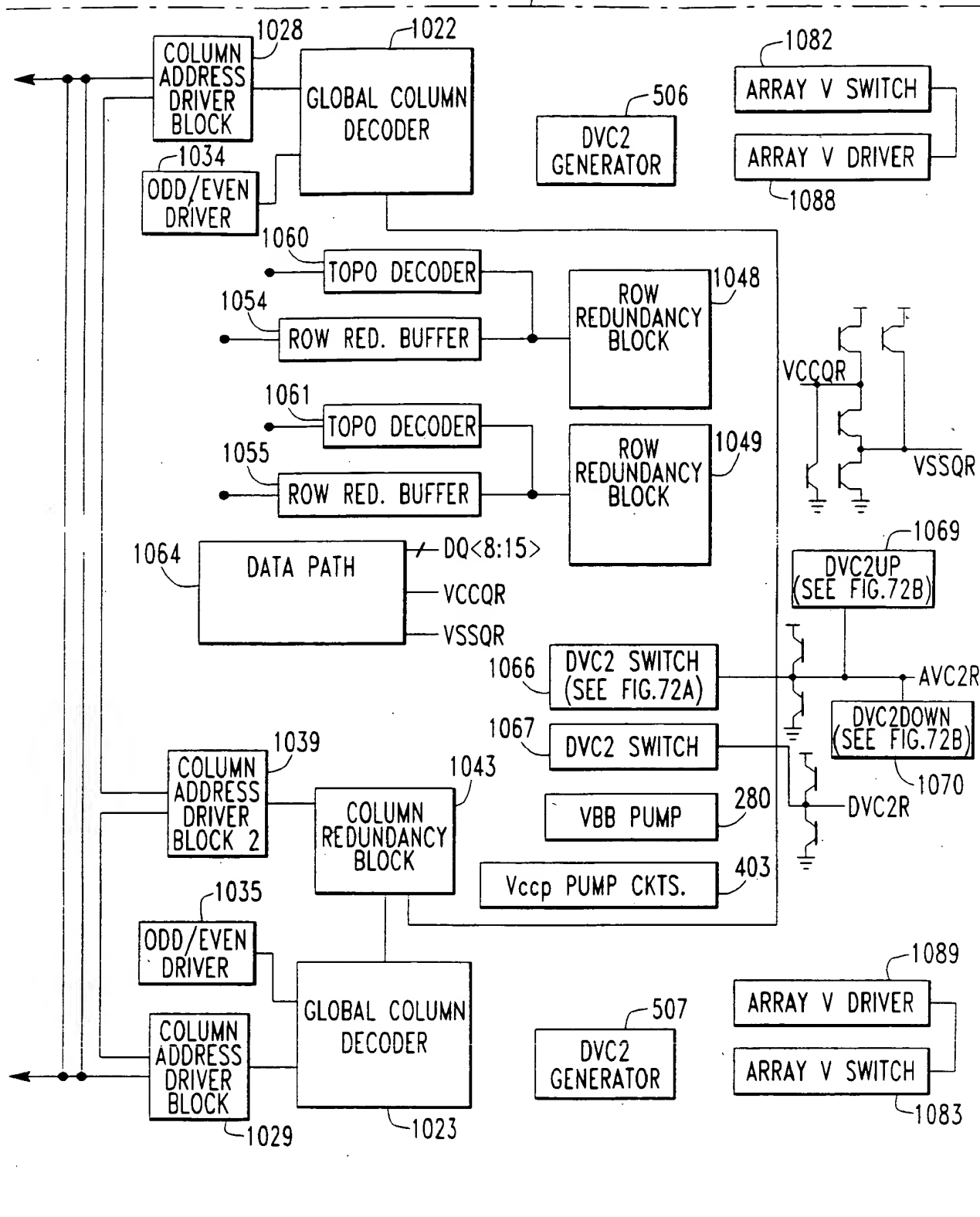


FIG. 64B

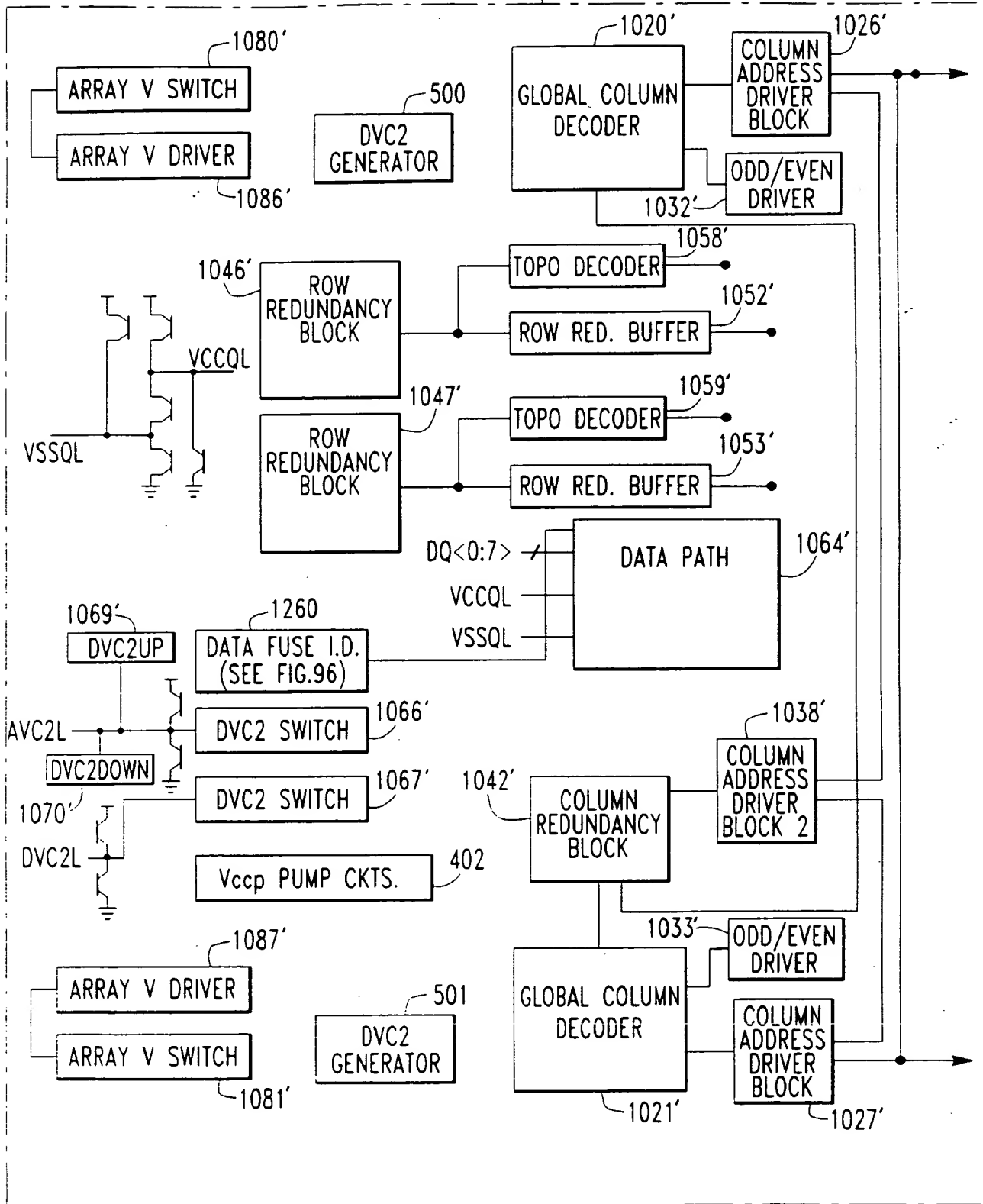


FIG. 65A

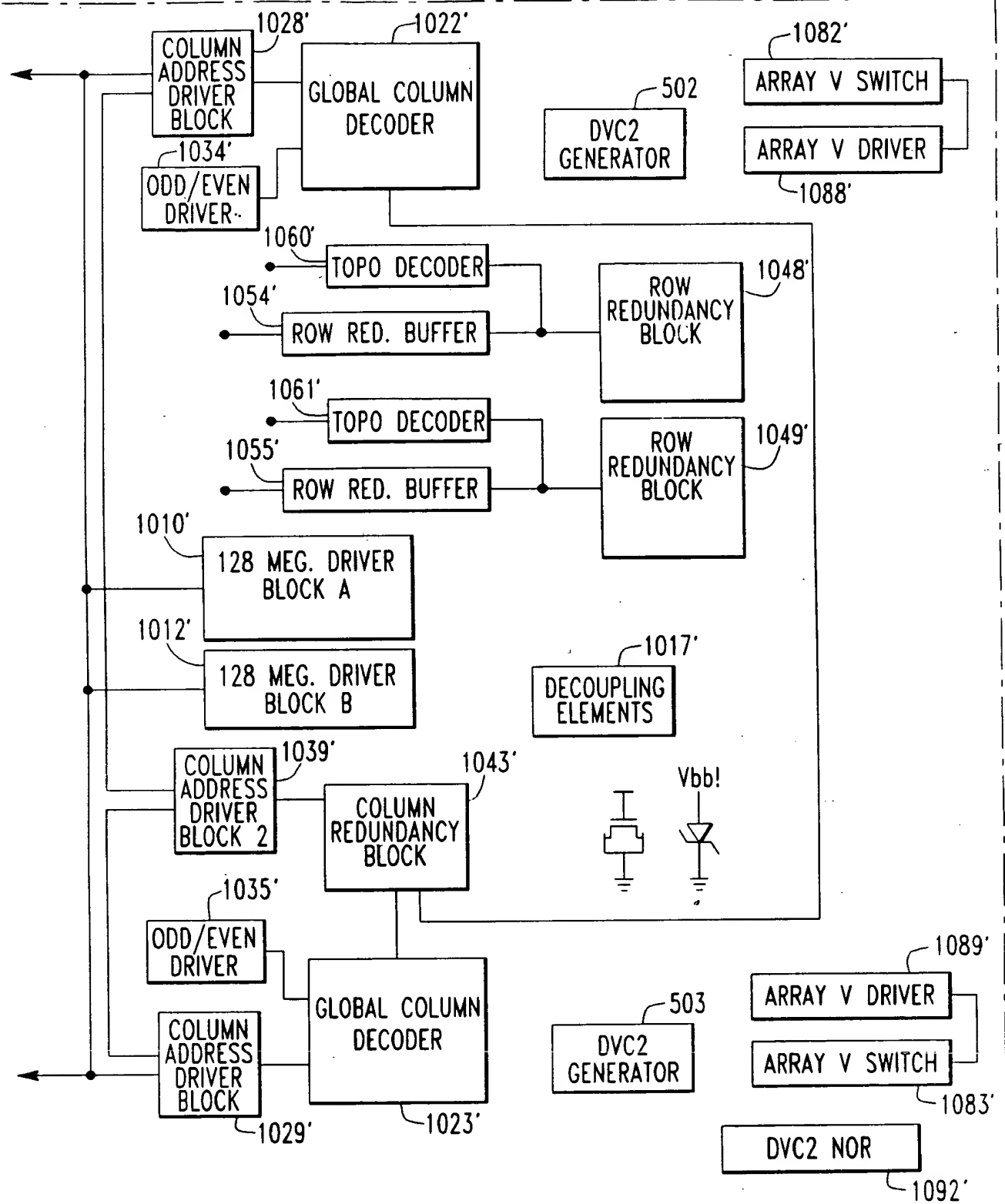


FIG. 65B

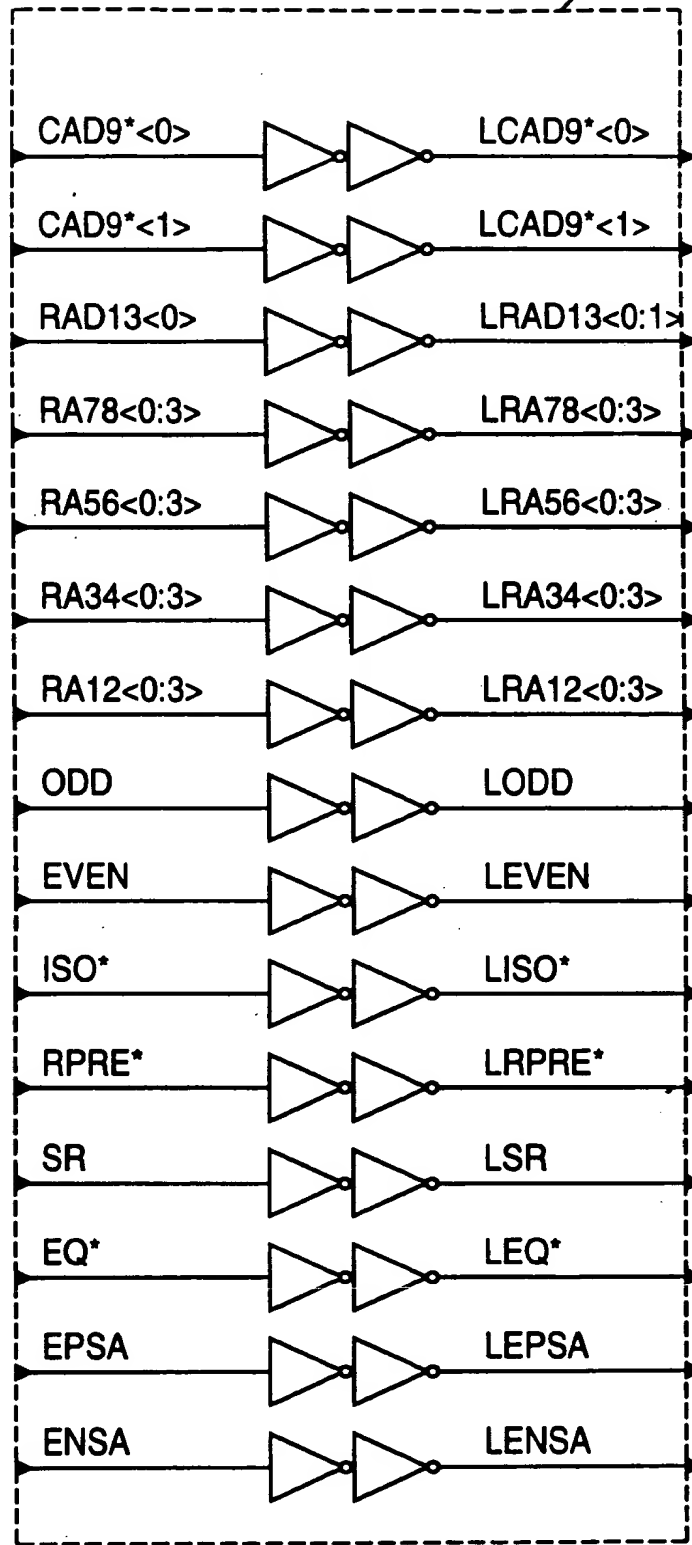


FIG. 66

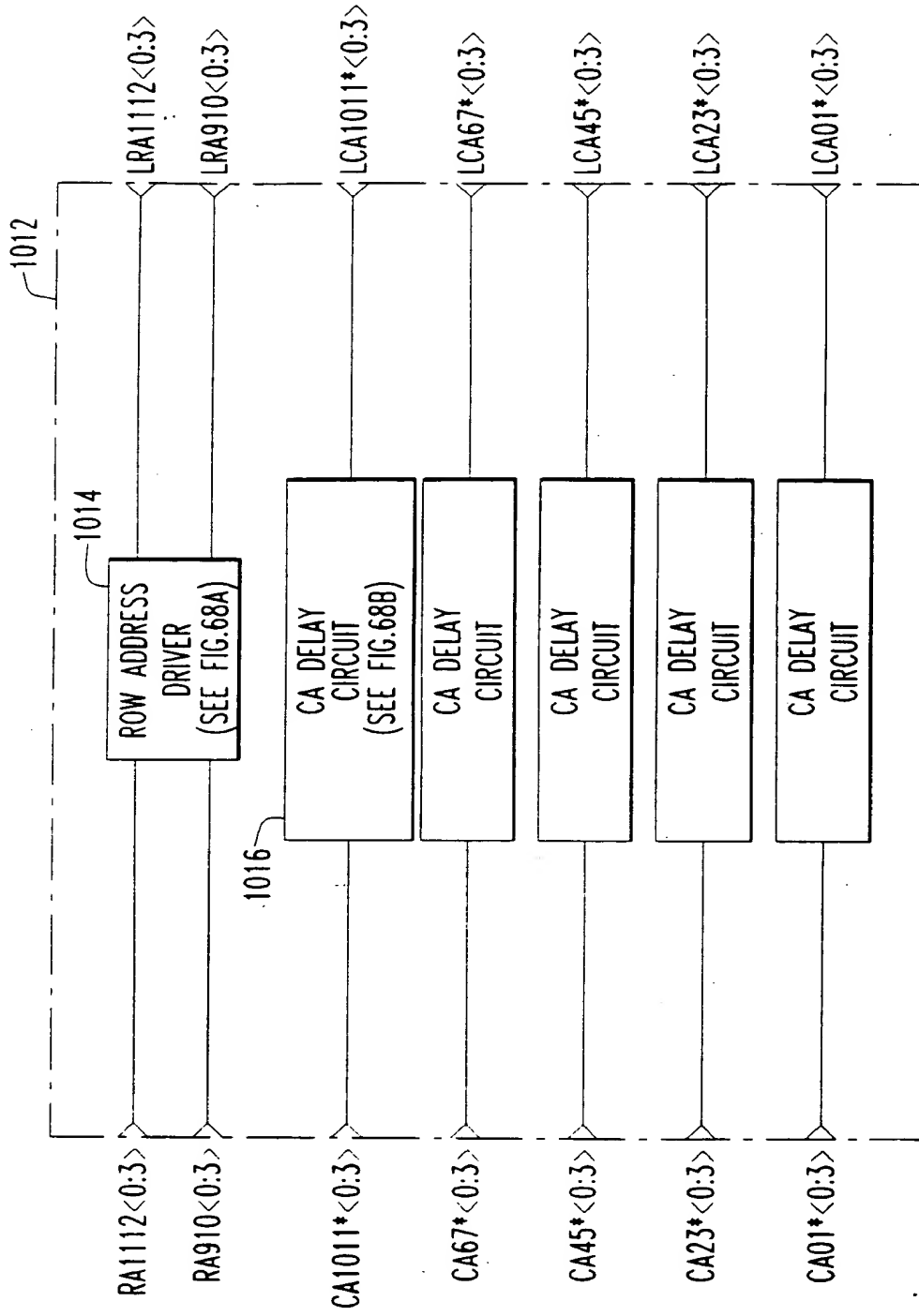
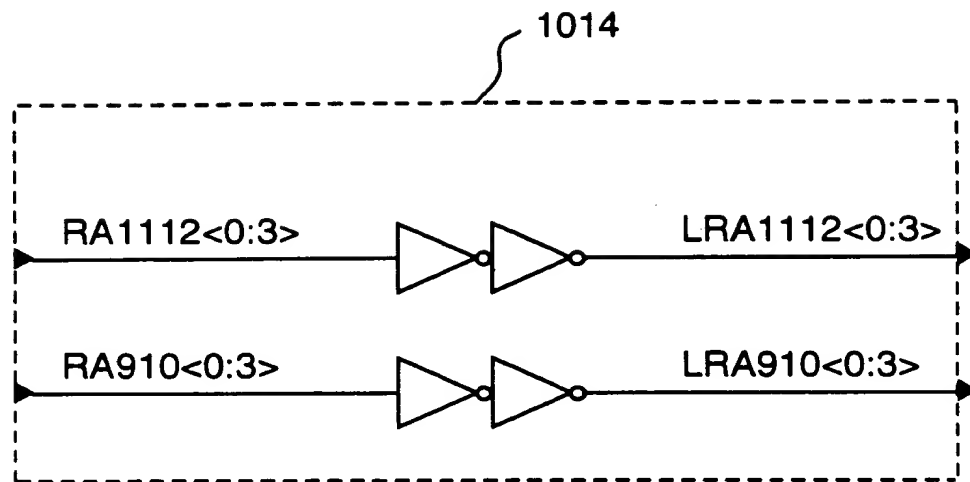
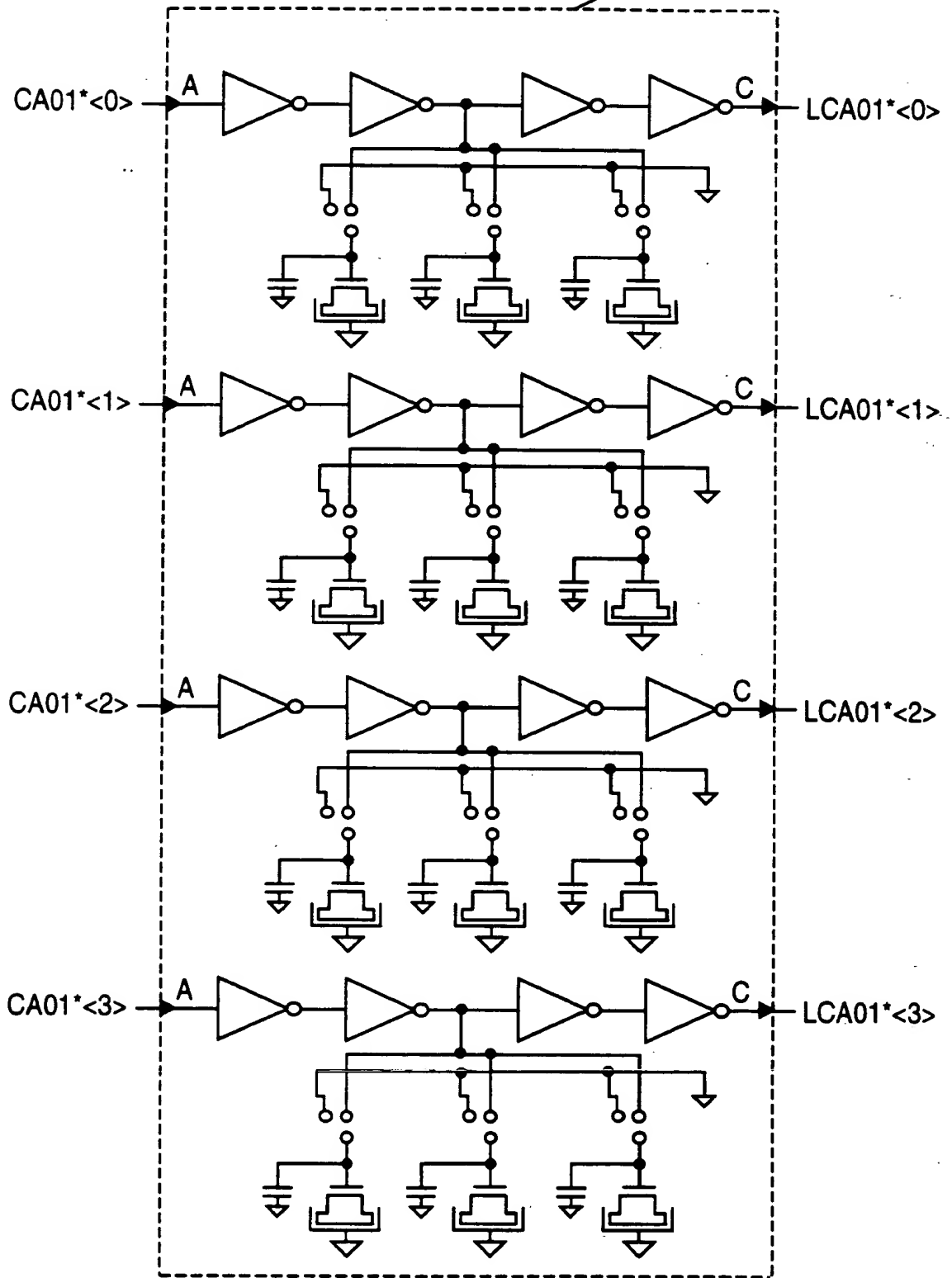


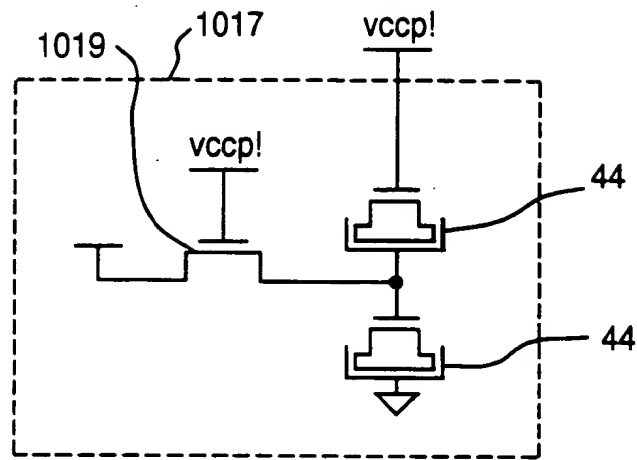
FIG. 67

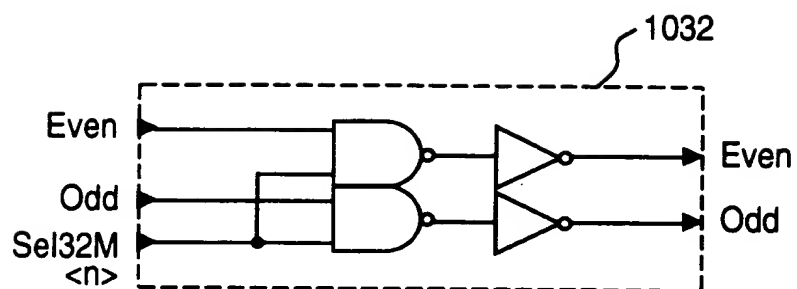
**FIG. 68A**

276/367

1016

**FIG. 68B**

**FIG. 69**

**FIG. 70**

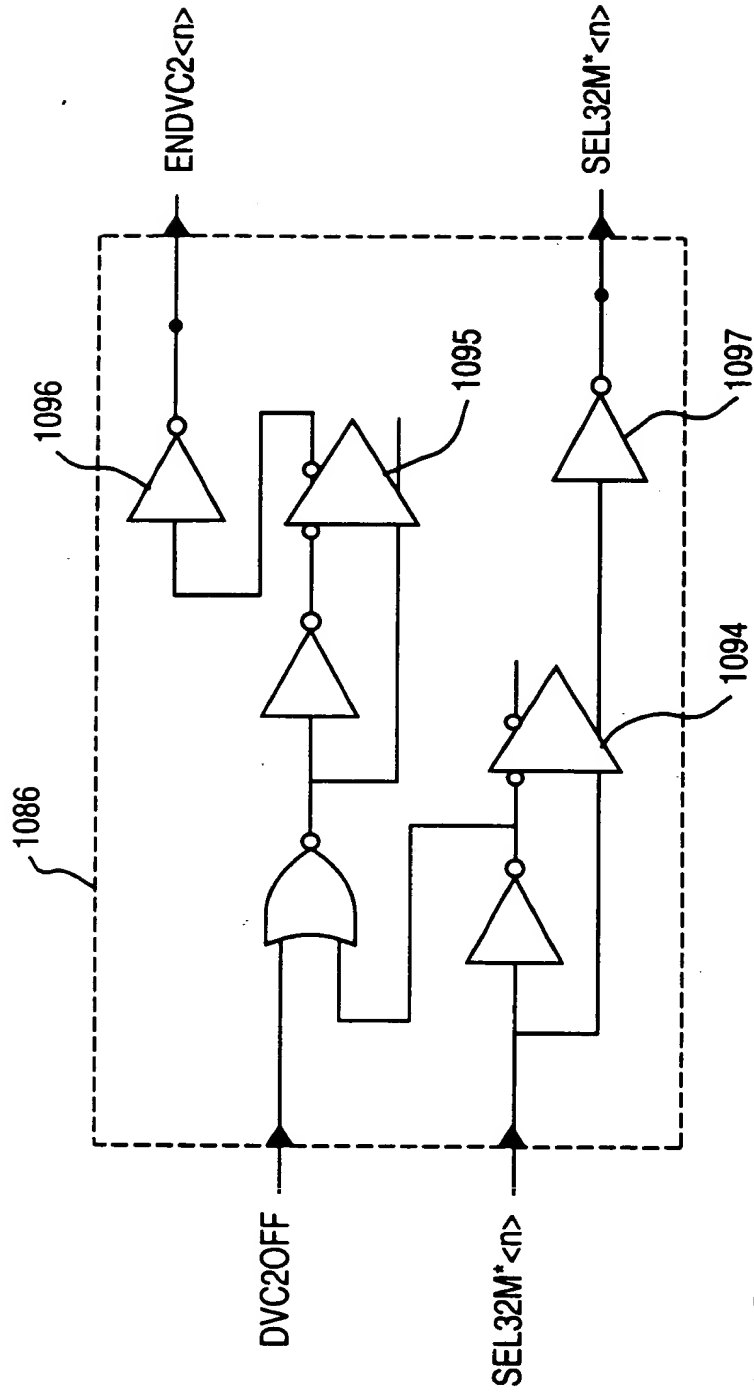
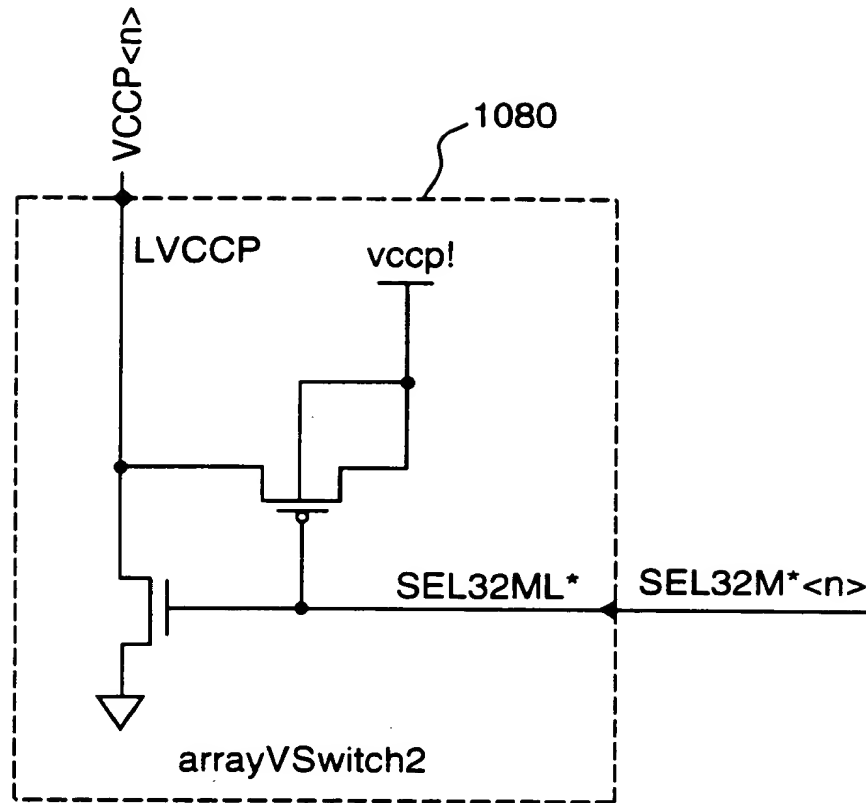


FIG. 71A

**FIG. 71B**

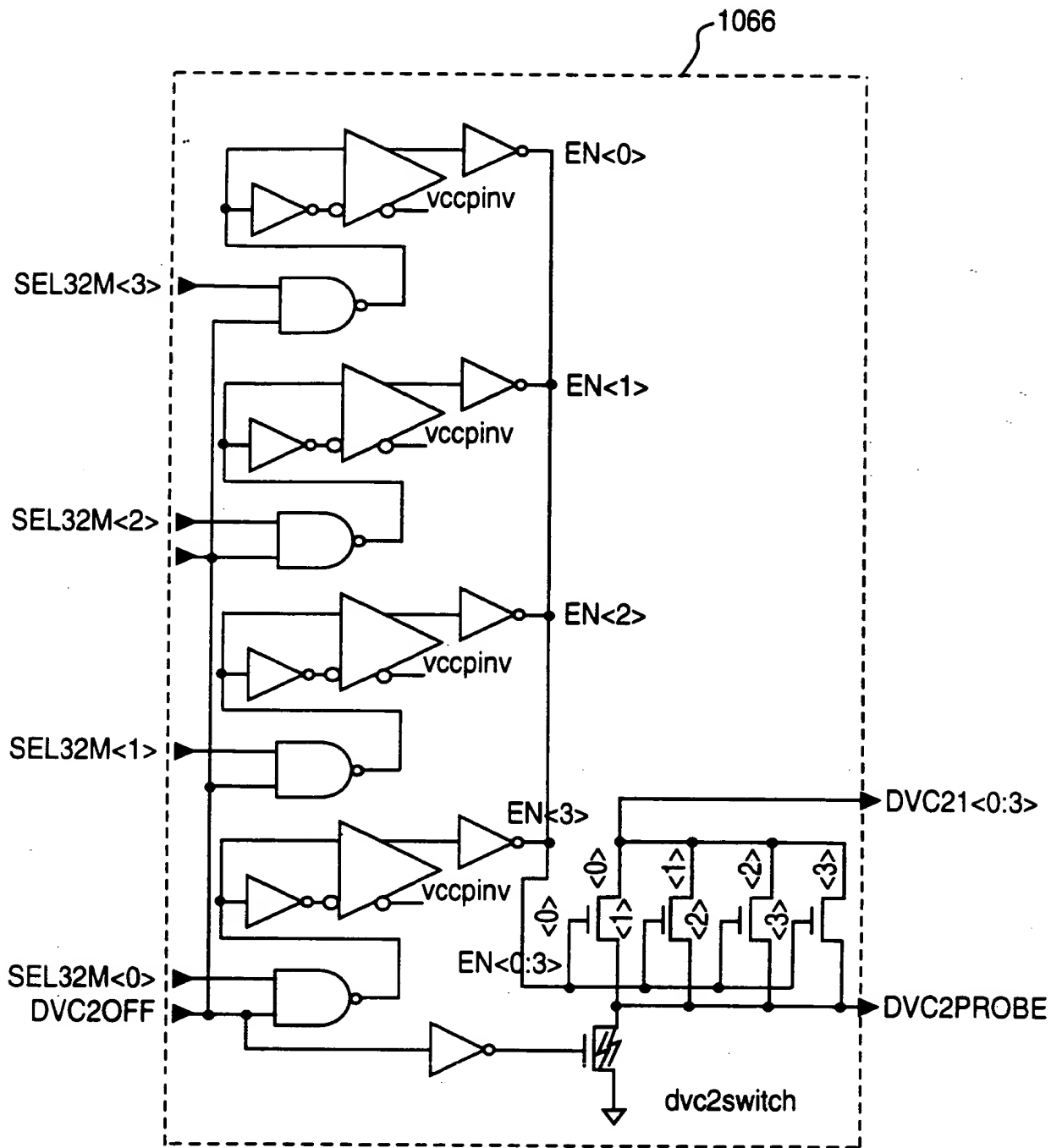
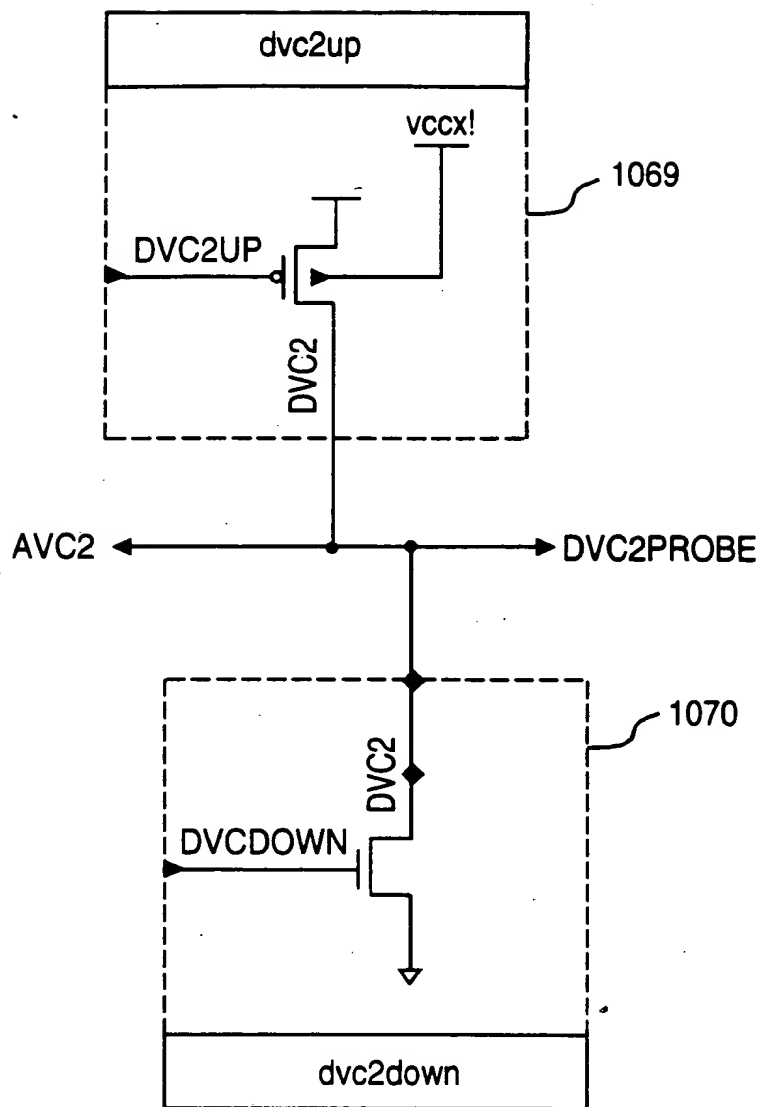


FIG. 72A

**FIG. 72B**

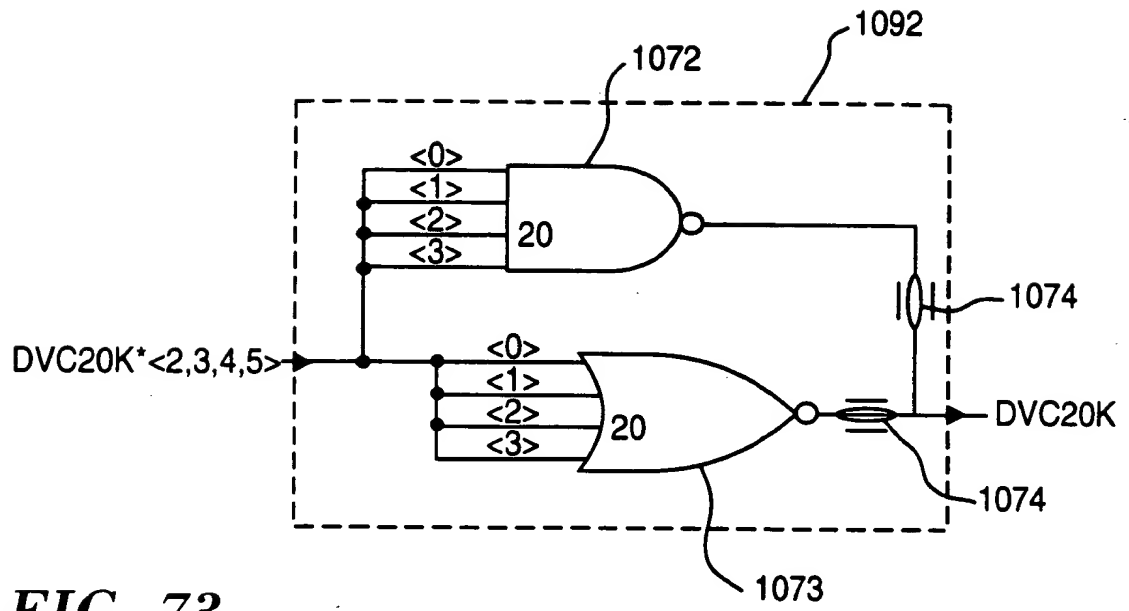


FIG. 73

FIG. 74

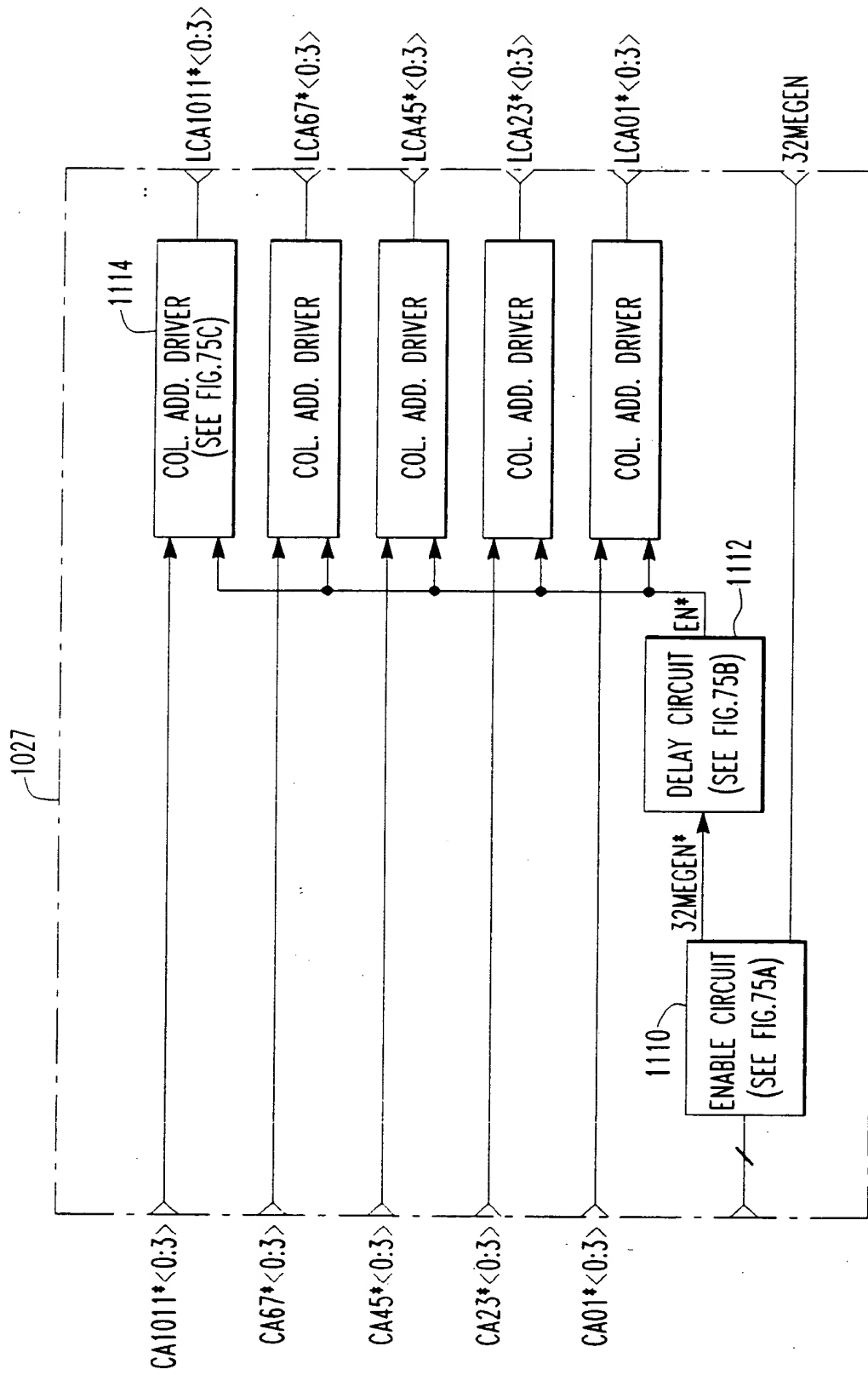


FIG. 74



FIG. 75A

FIG. 75B

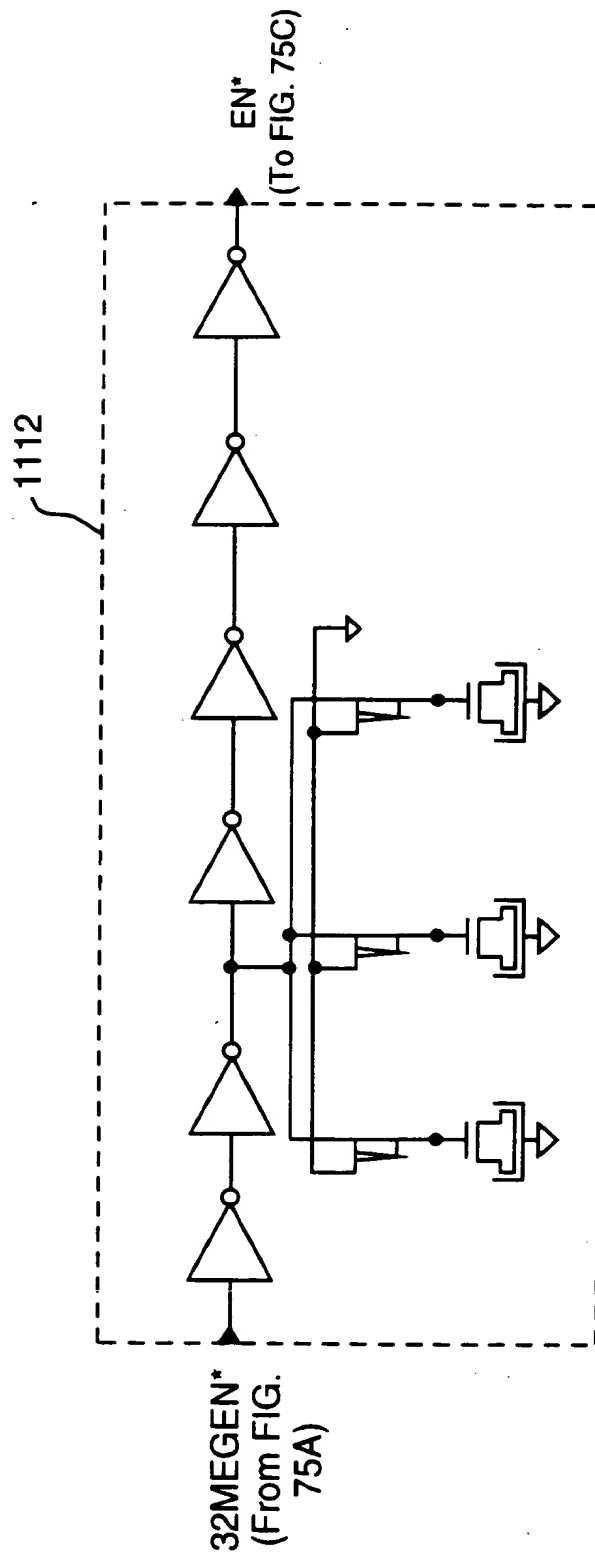


FIG. 75B

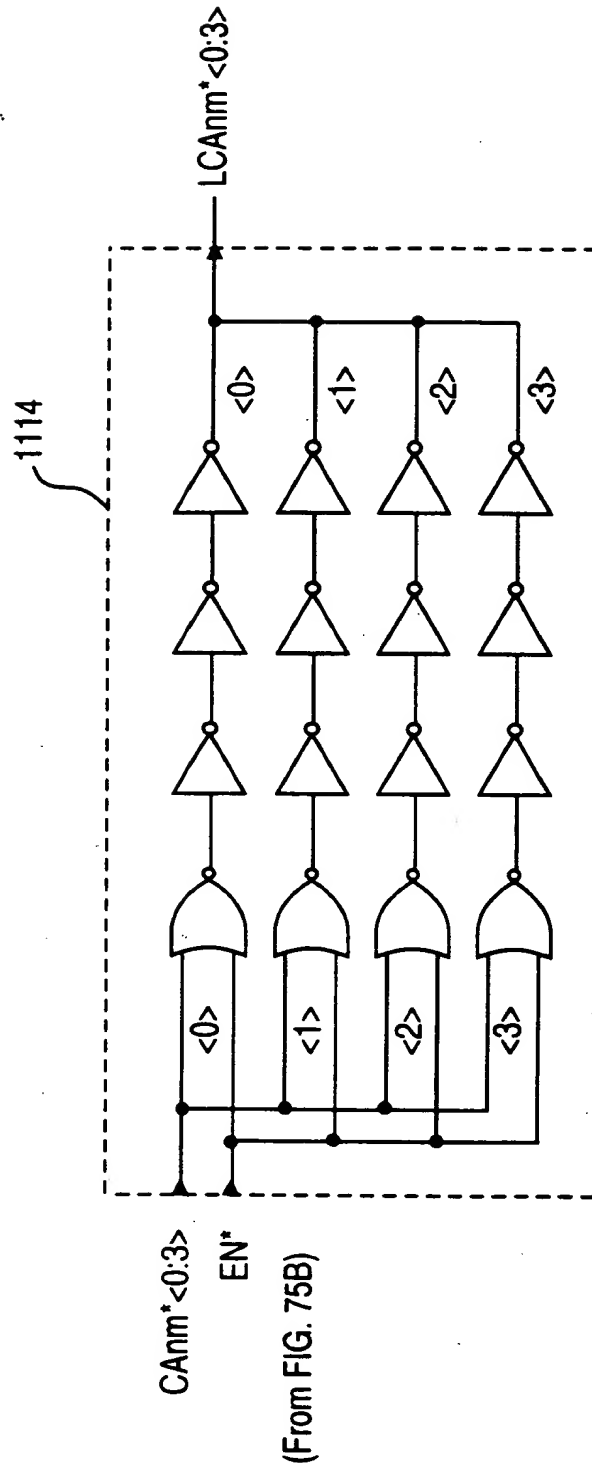


FIG. 75C

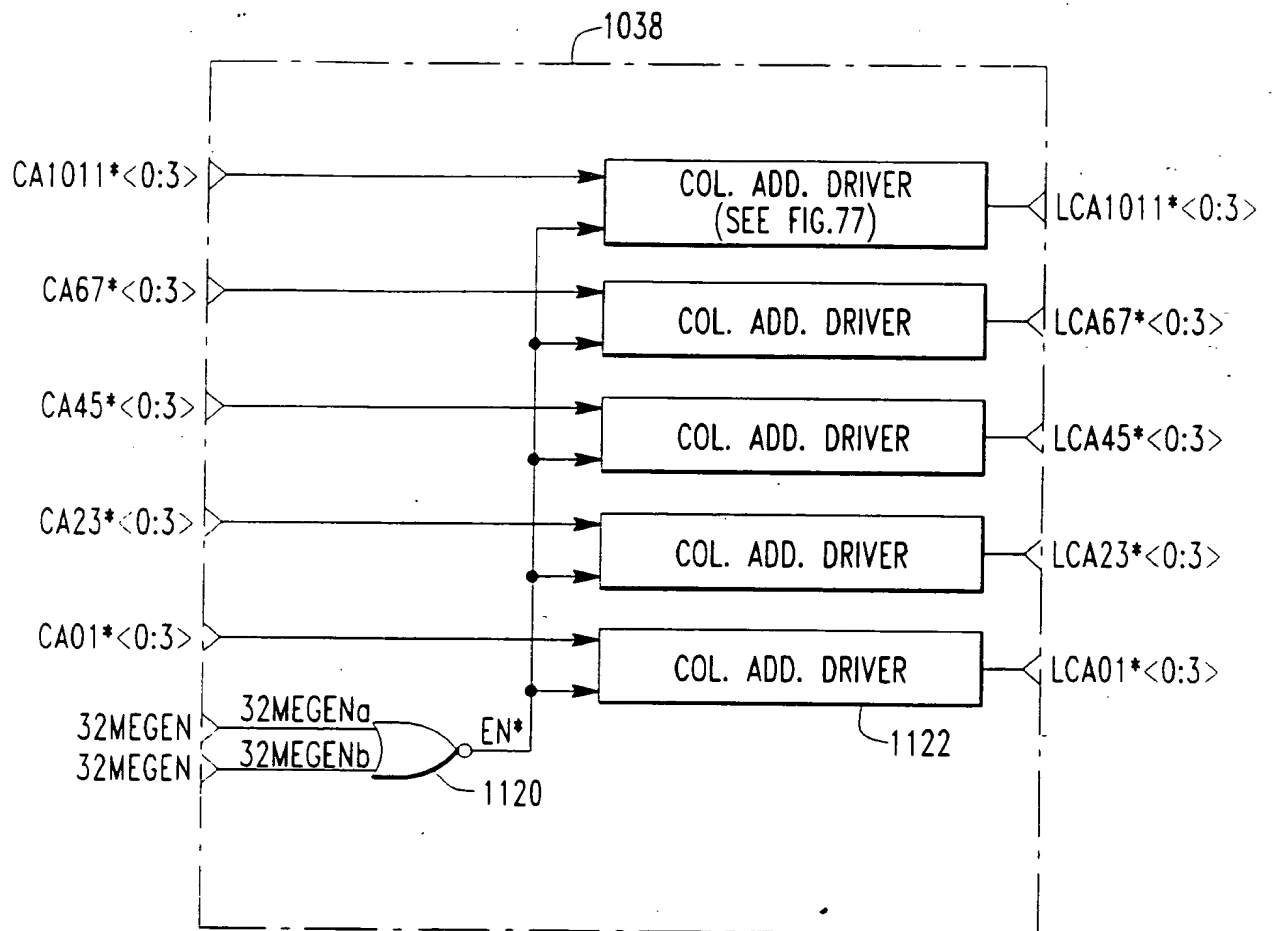


FIG. 76

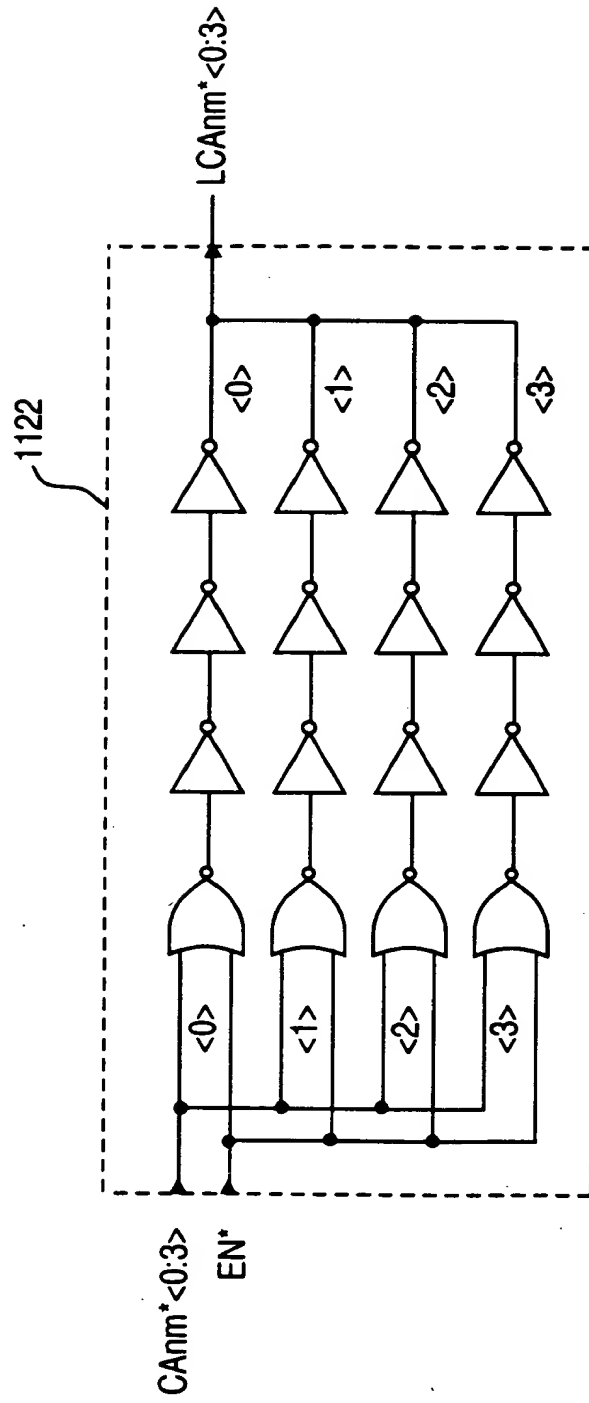


FIG. 77

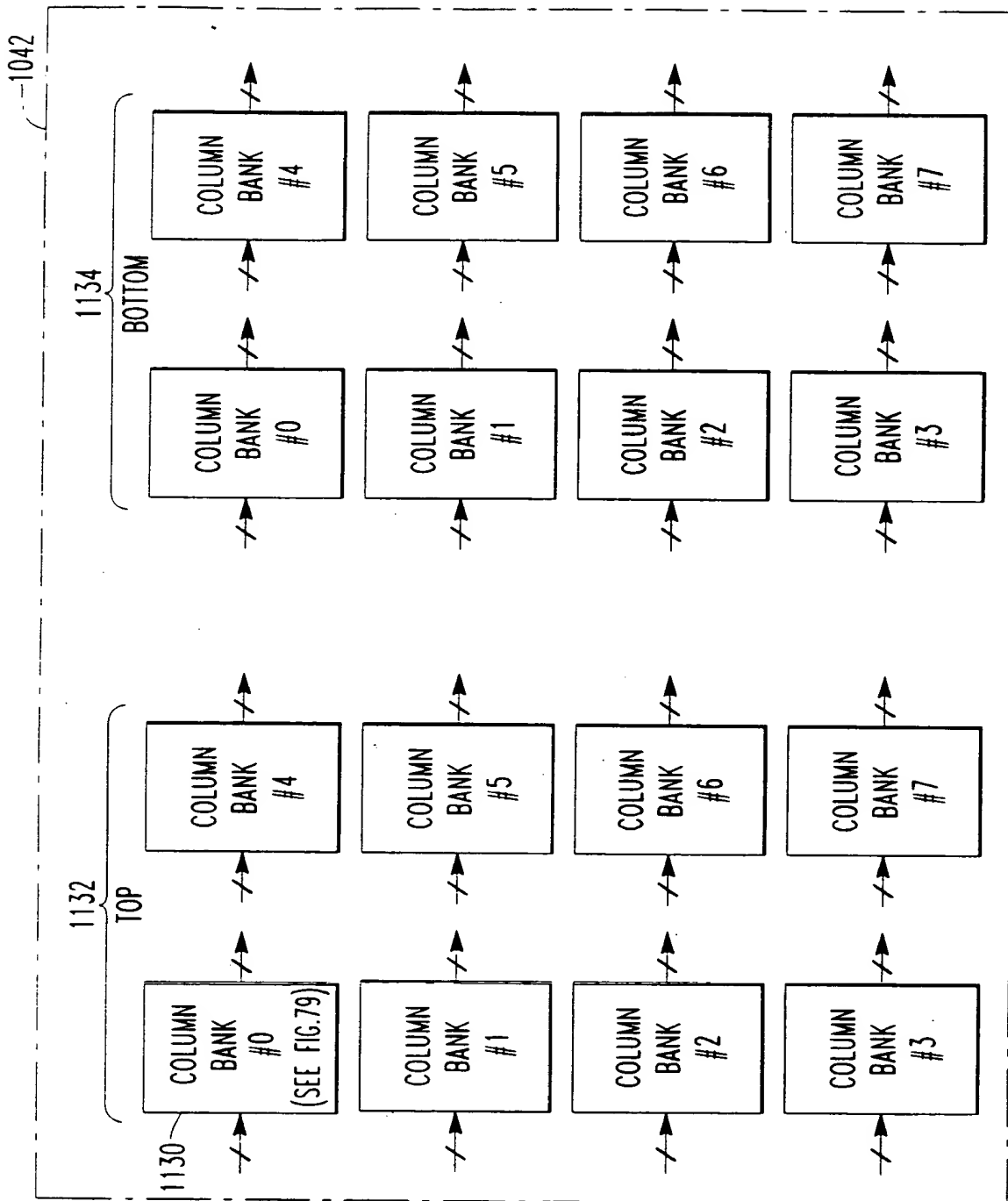
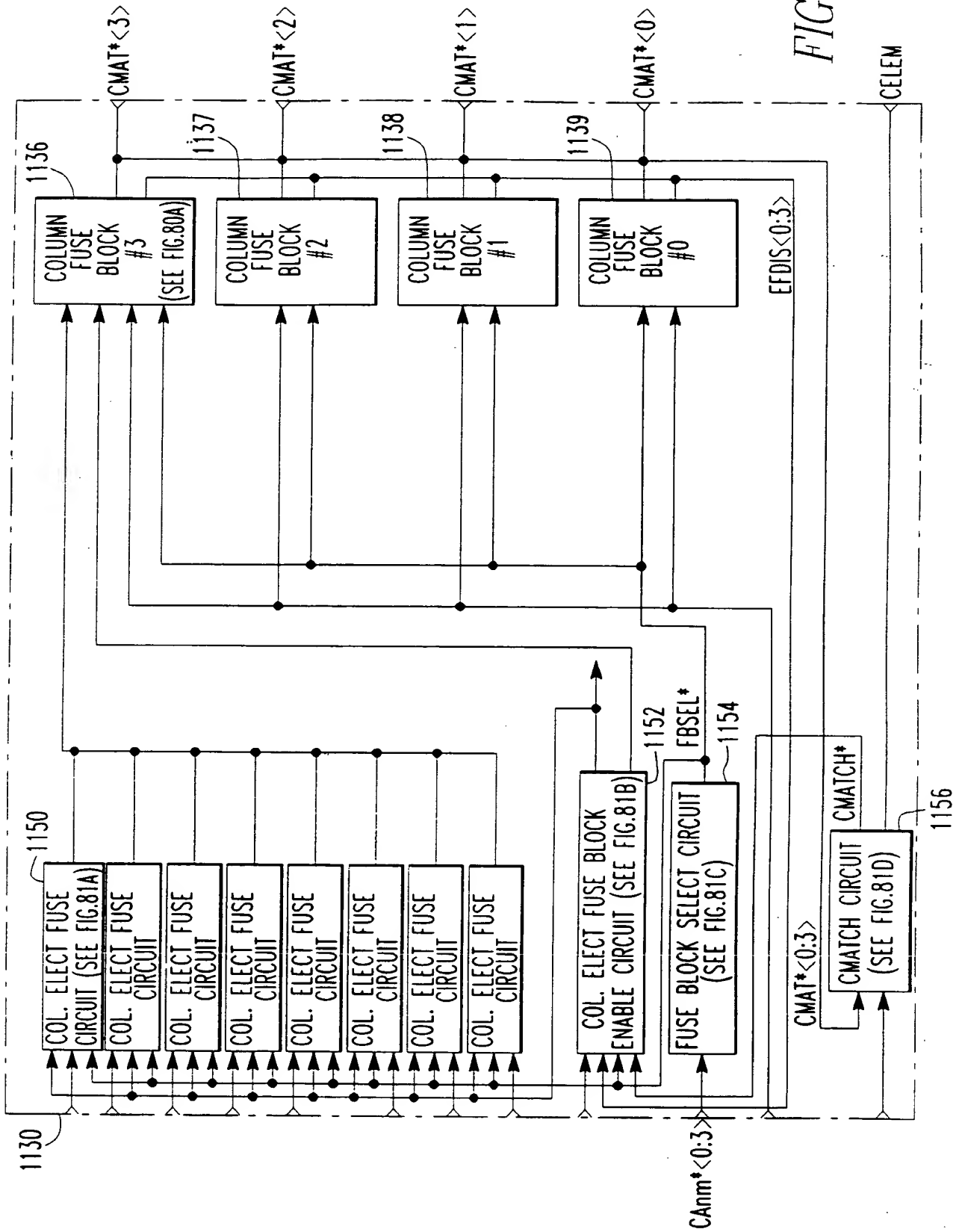


FIG. 78

FIG. 79



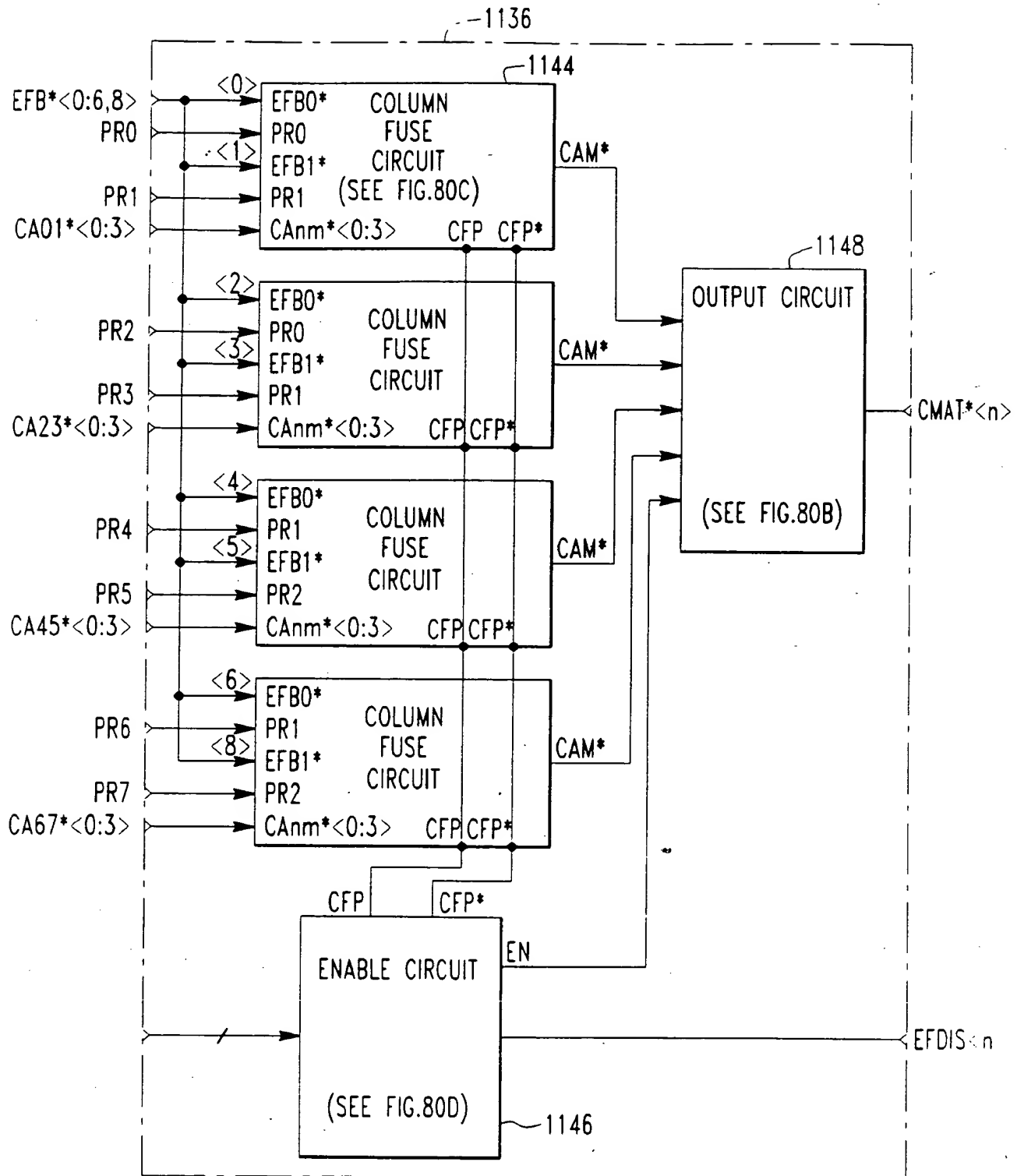
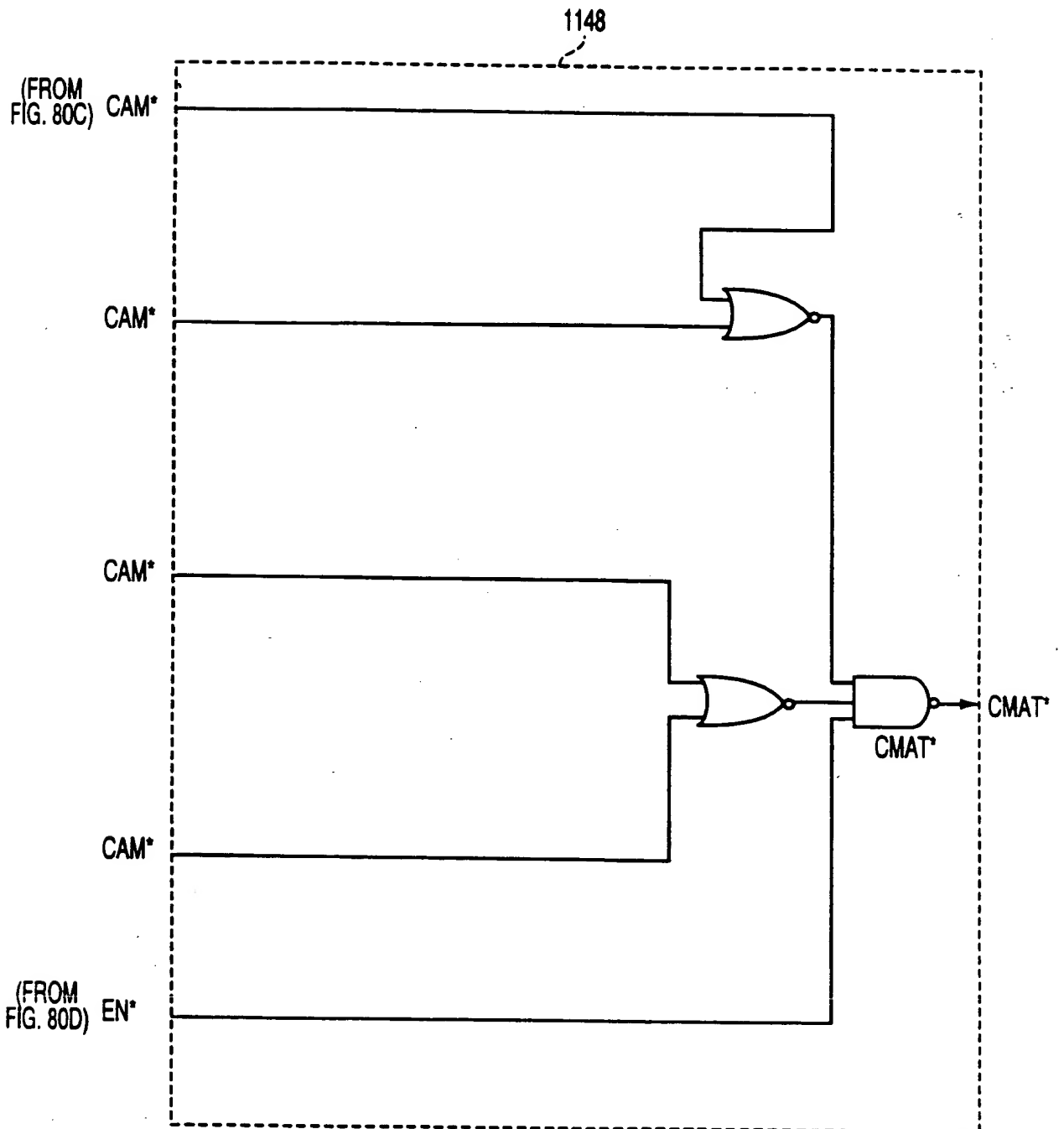


FIG. 80A

**FIG. 80B**

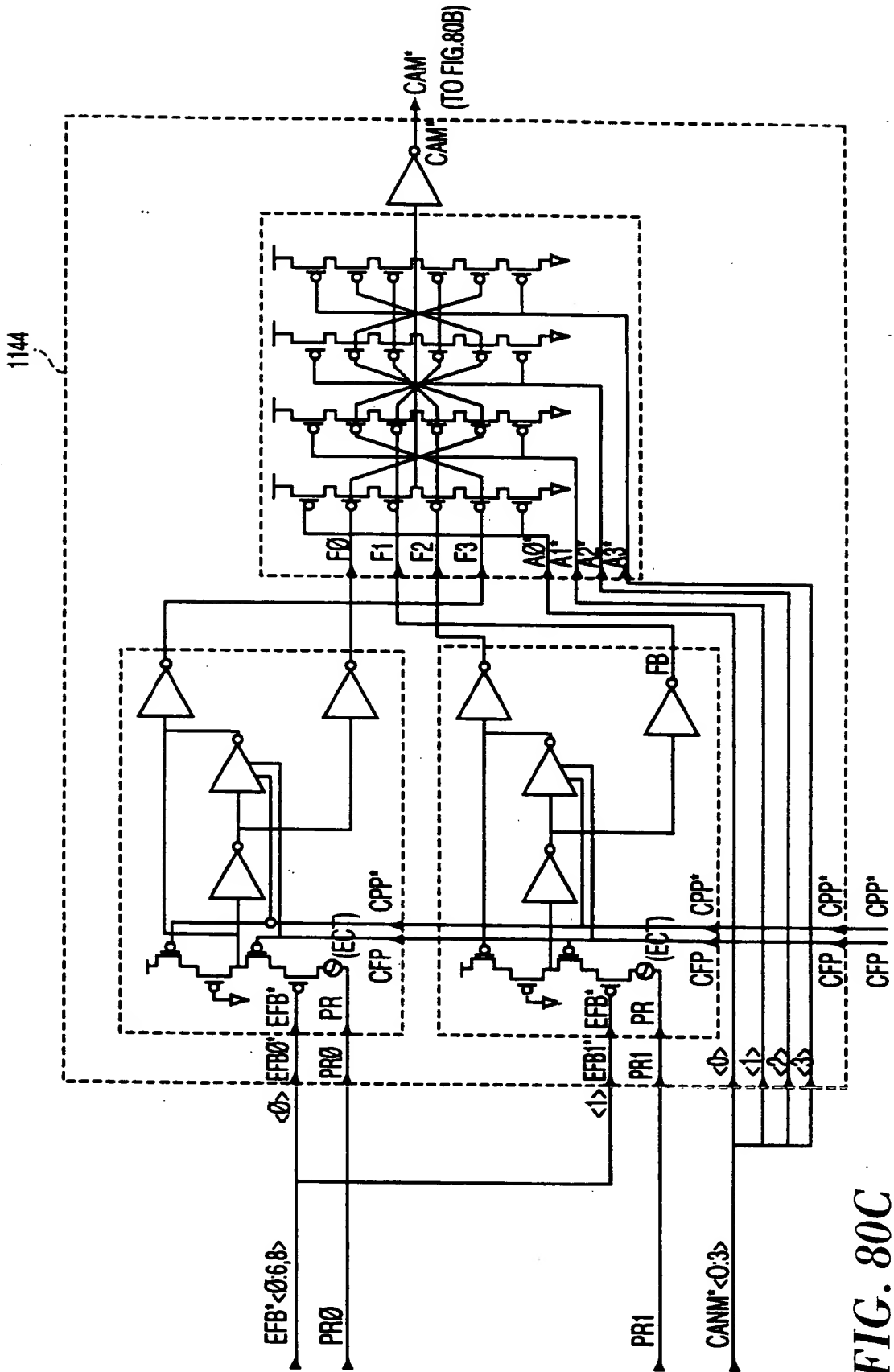


FIG. 80C

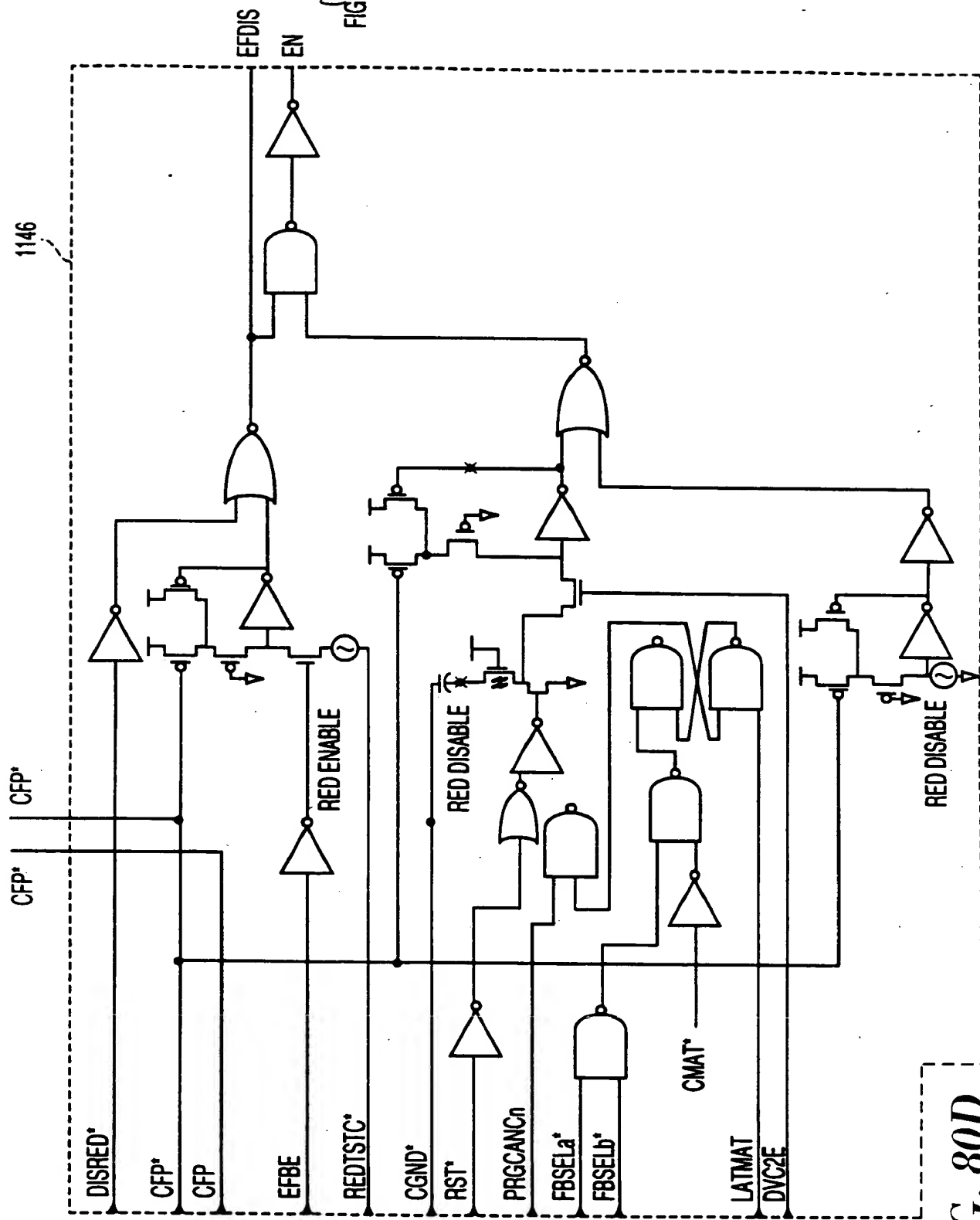


FIG. 80D

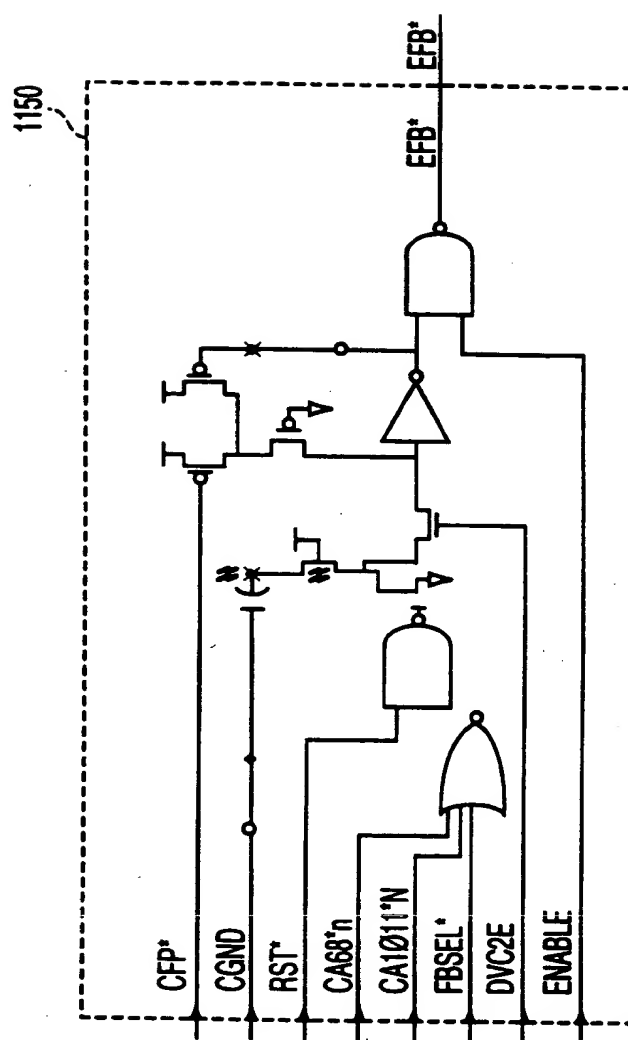


FIG. 81A

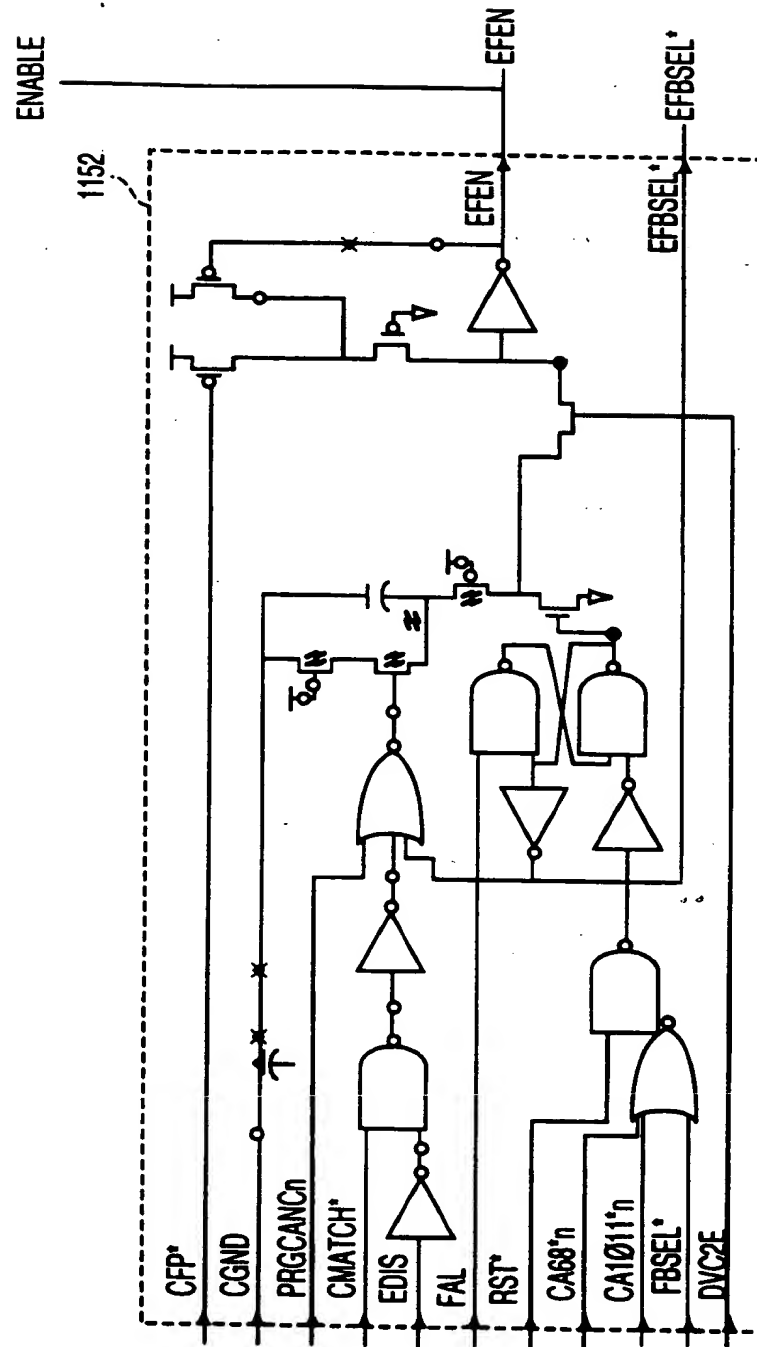


FIG. 81B

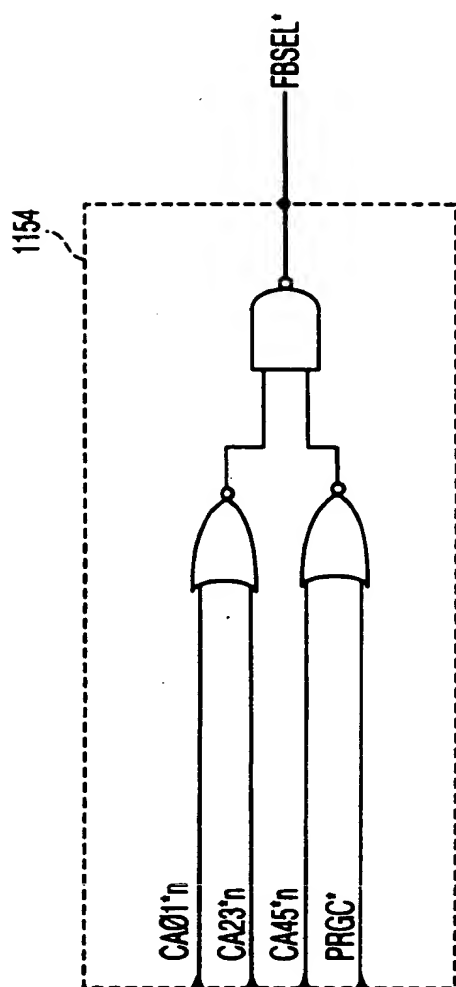


FIG. 81C

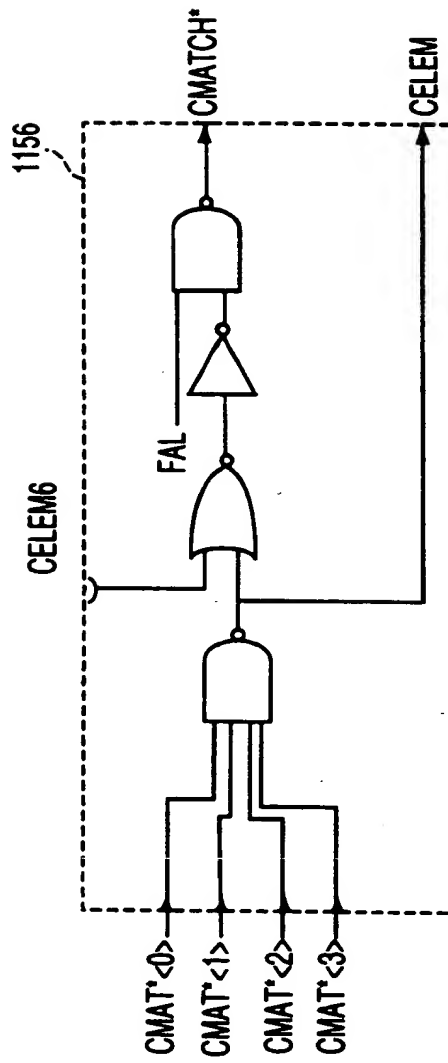
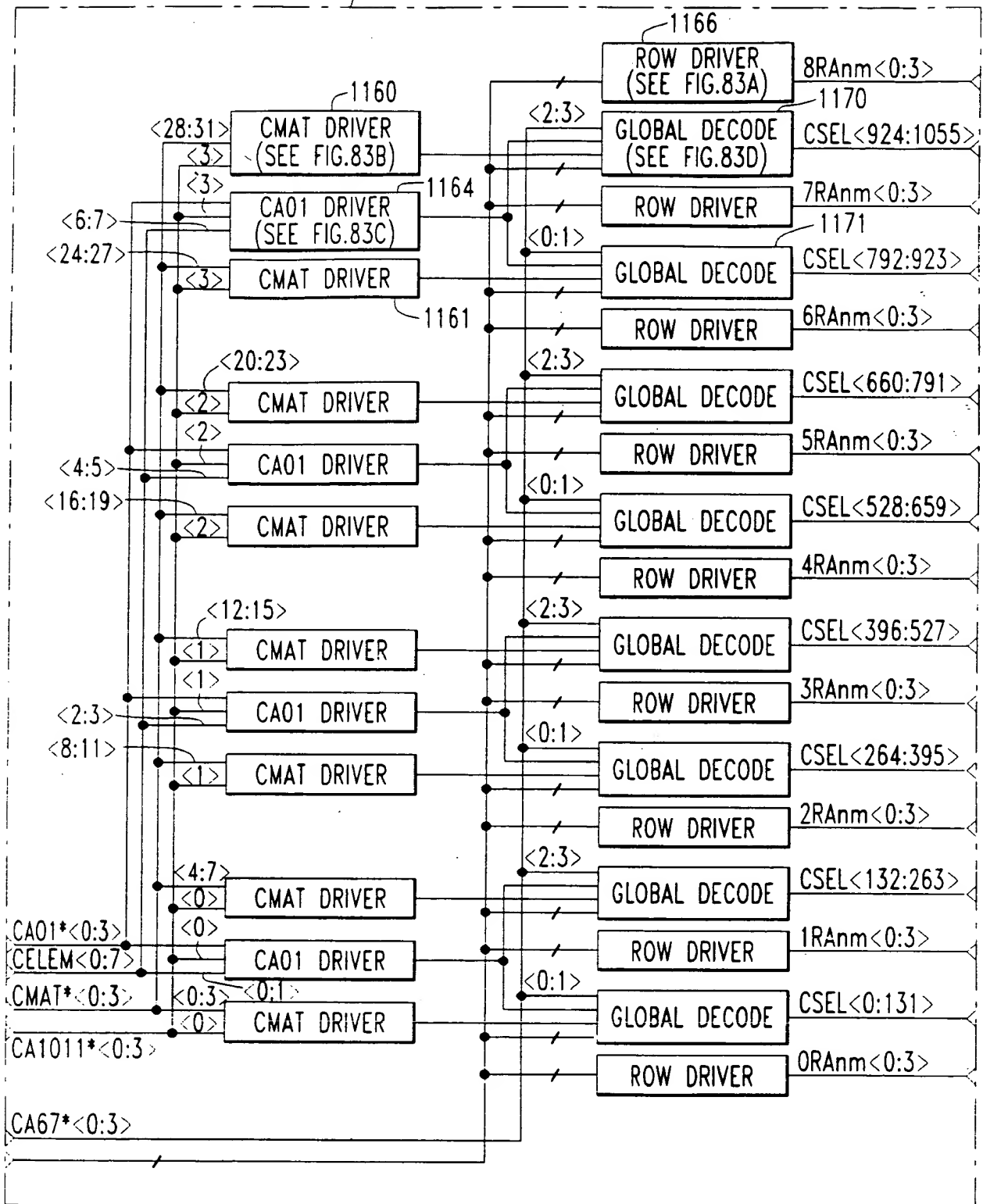


FIG. 81D



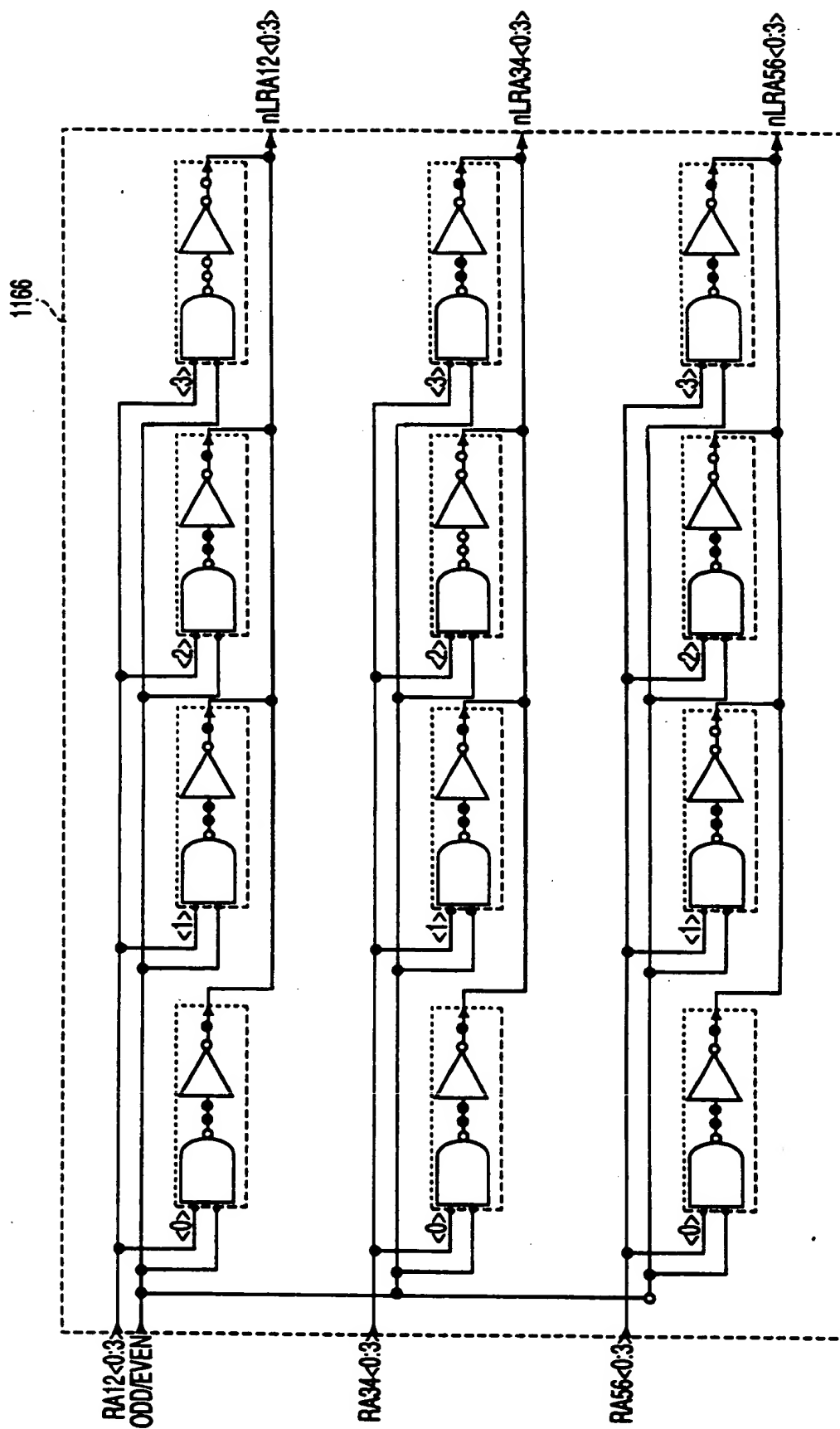


FIG. 83A

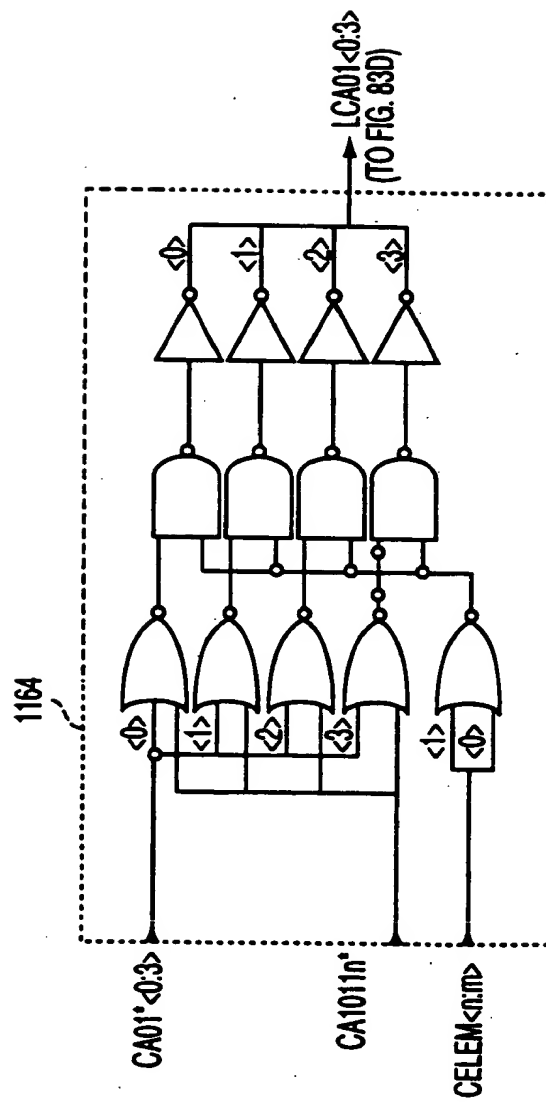
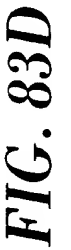


FIG. 83C



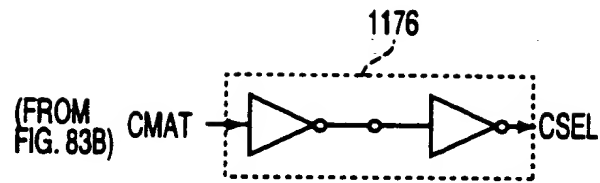


FIG. 84B

094474660 5624E660

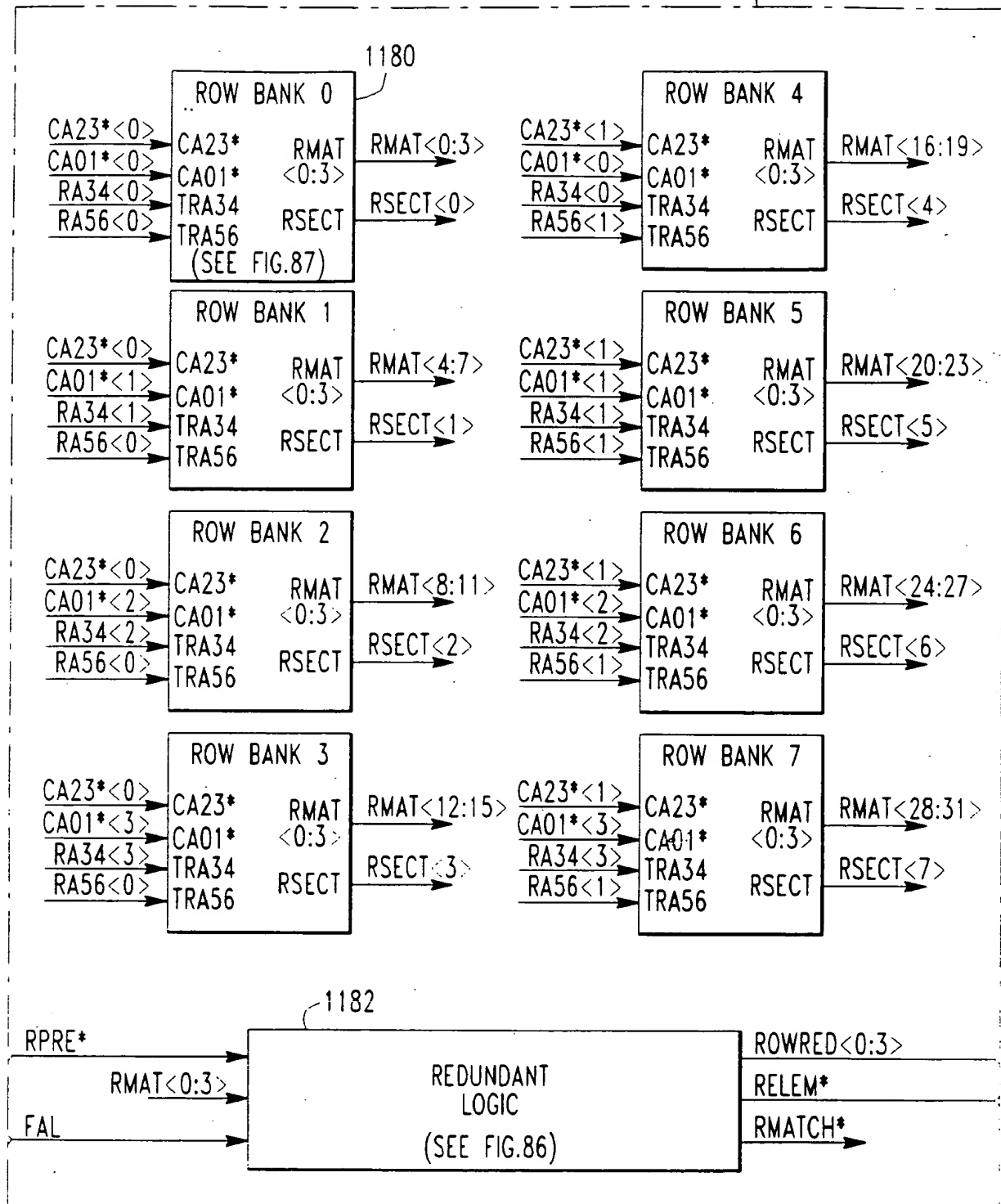


FIG. 85

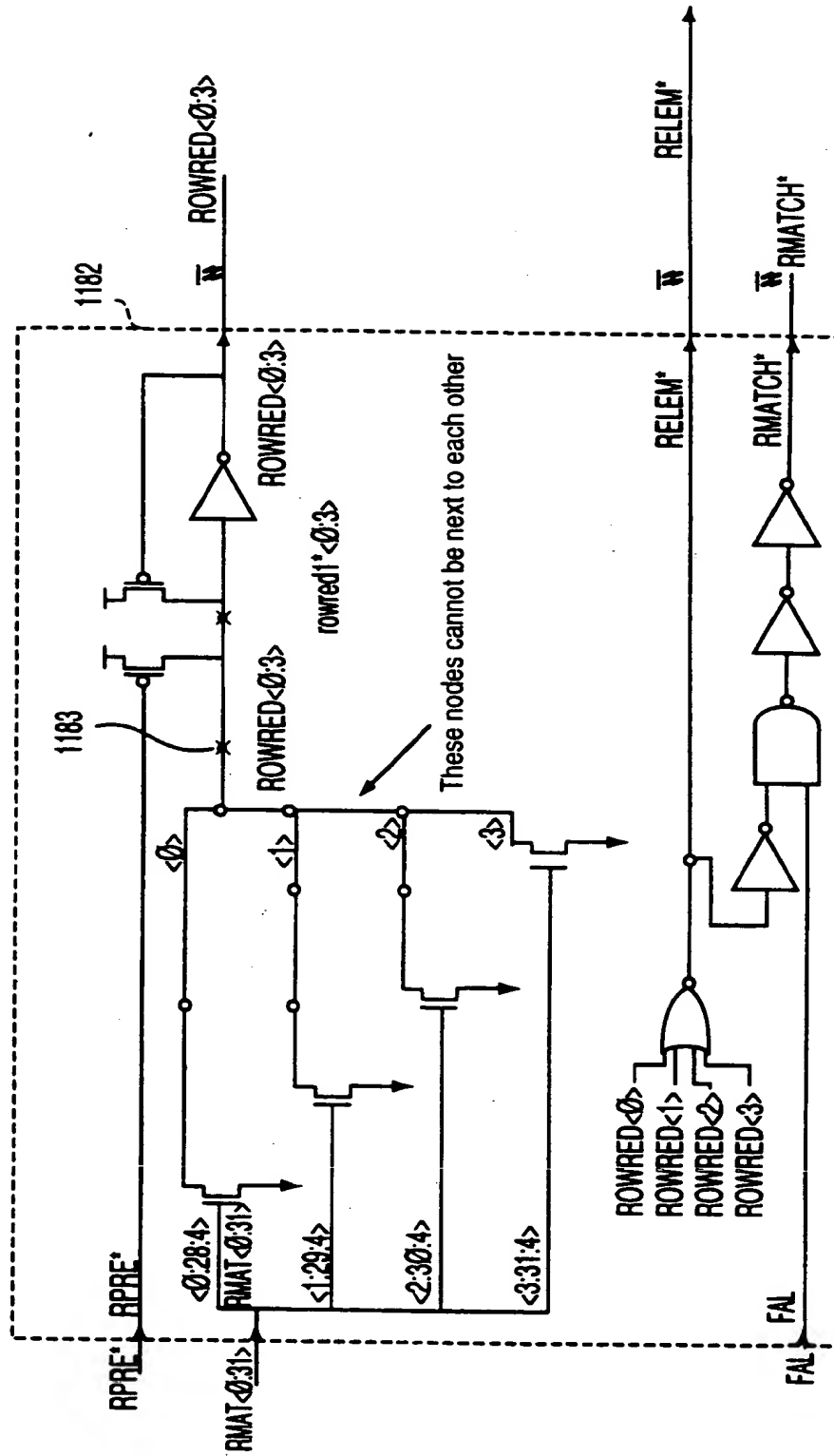


FIG. 86

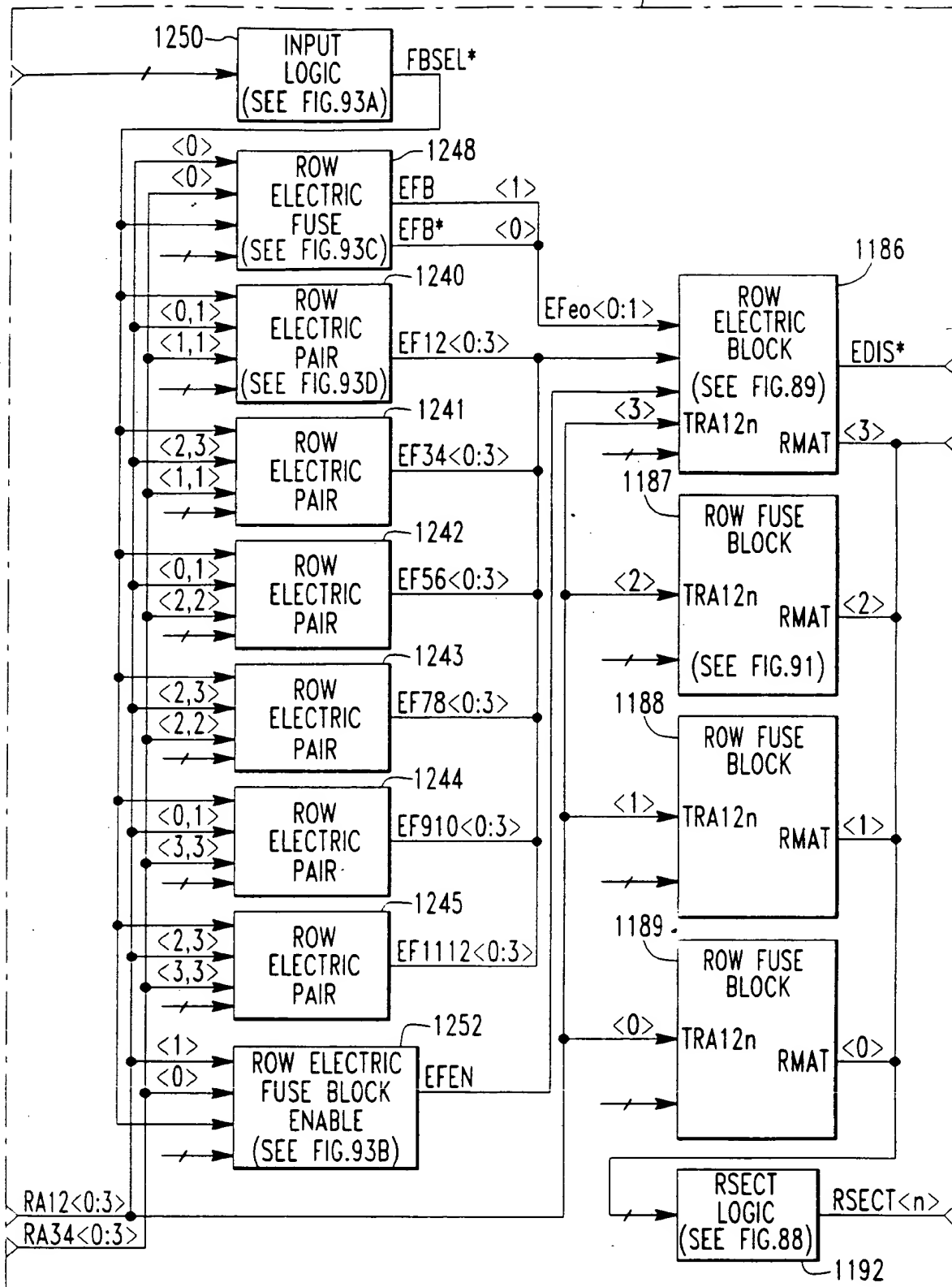
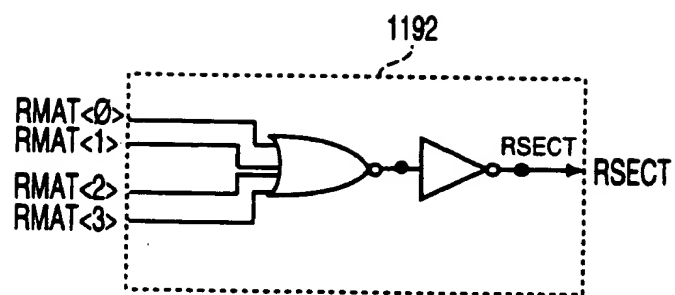


FIG. 87

**FIG. 88**

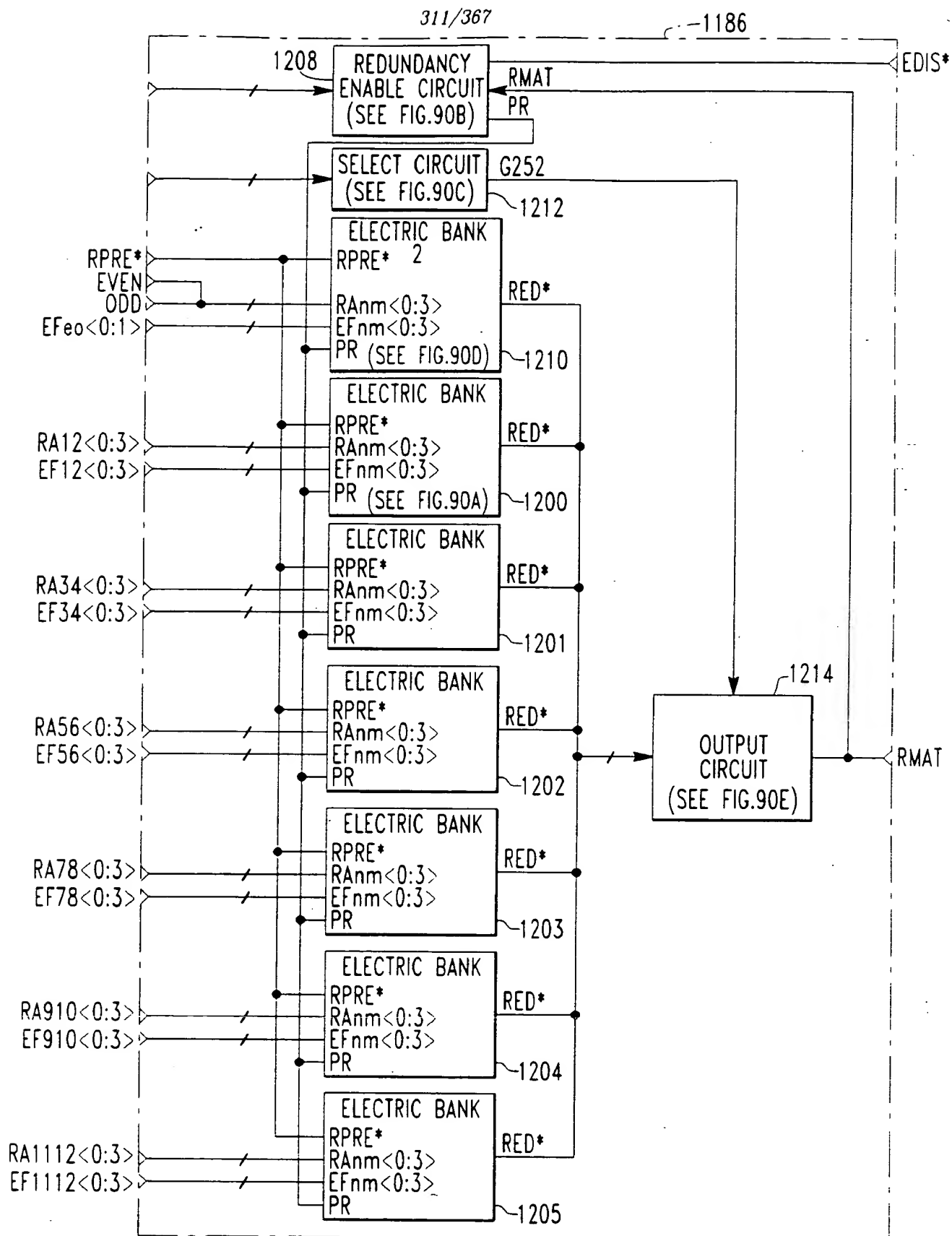


FIG. 89

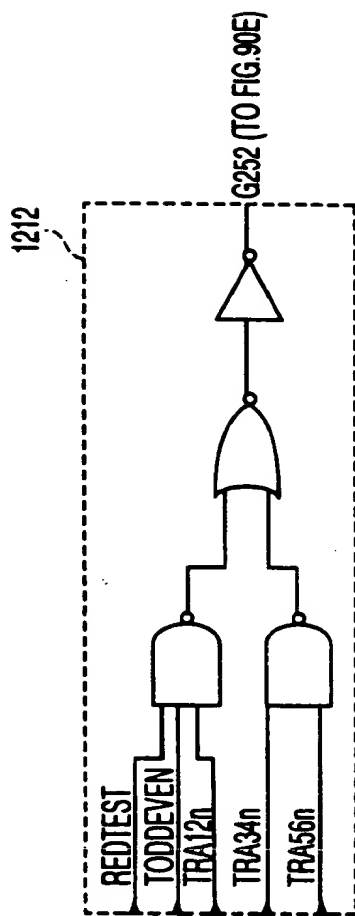


FIG. 90C

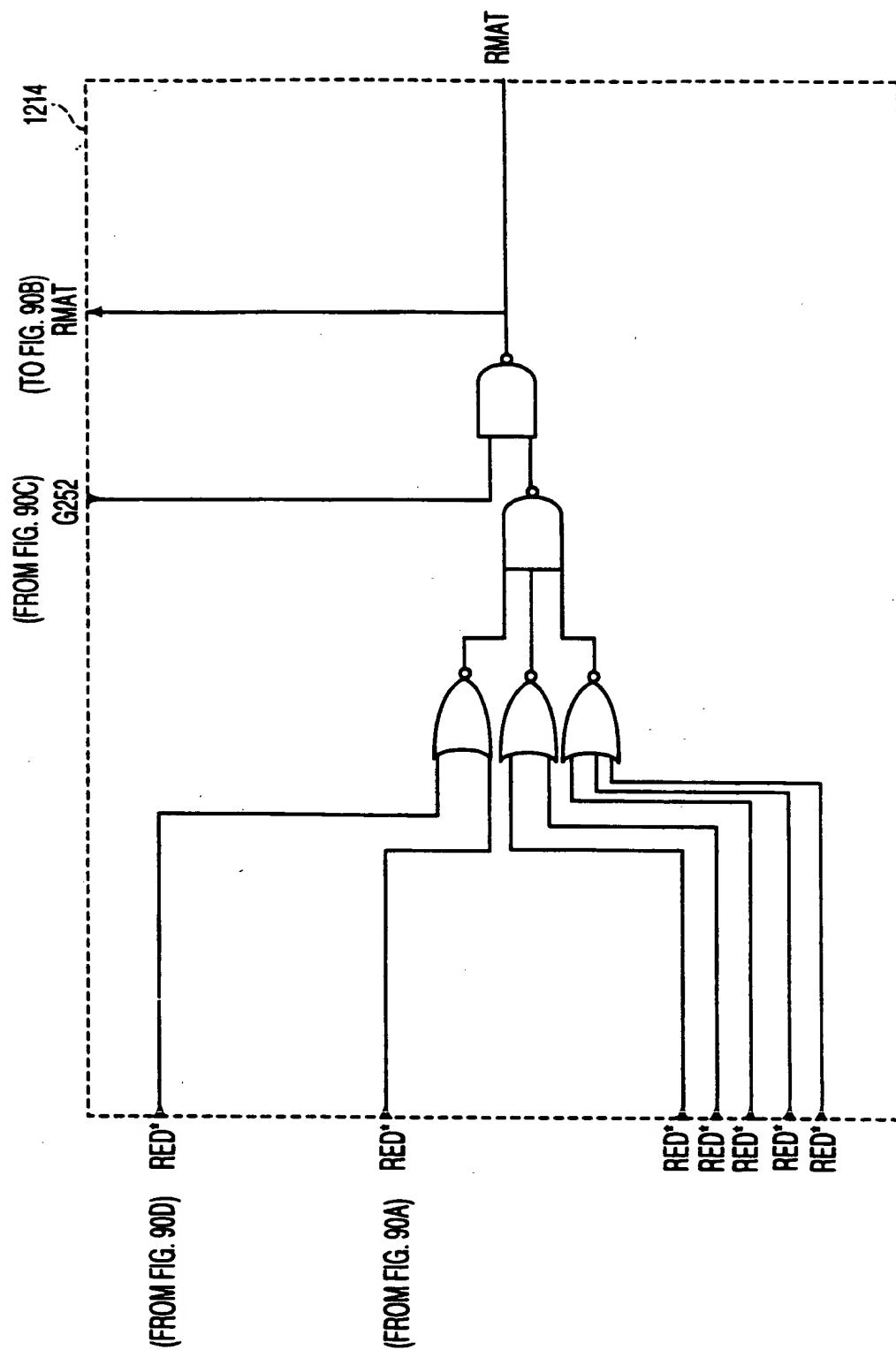


FIG. 90E

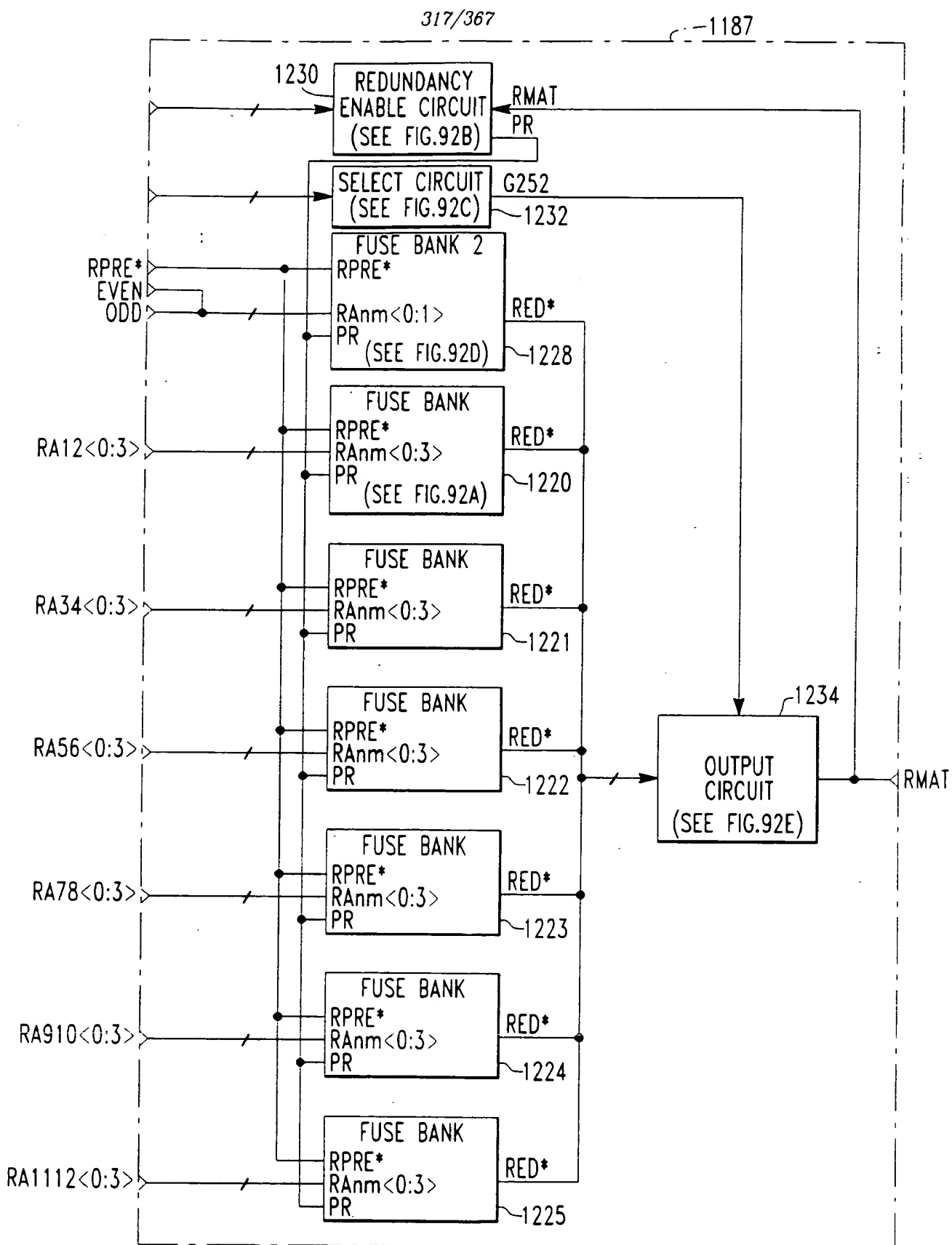


FIG. 91

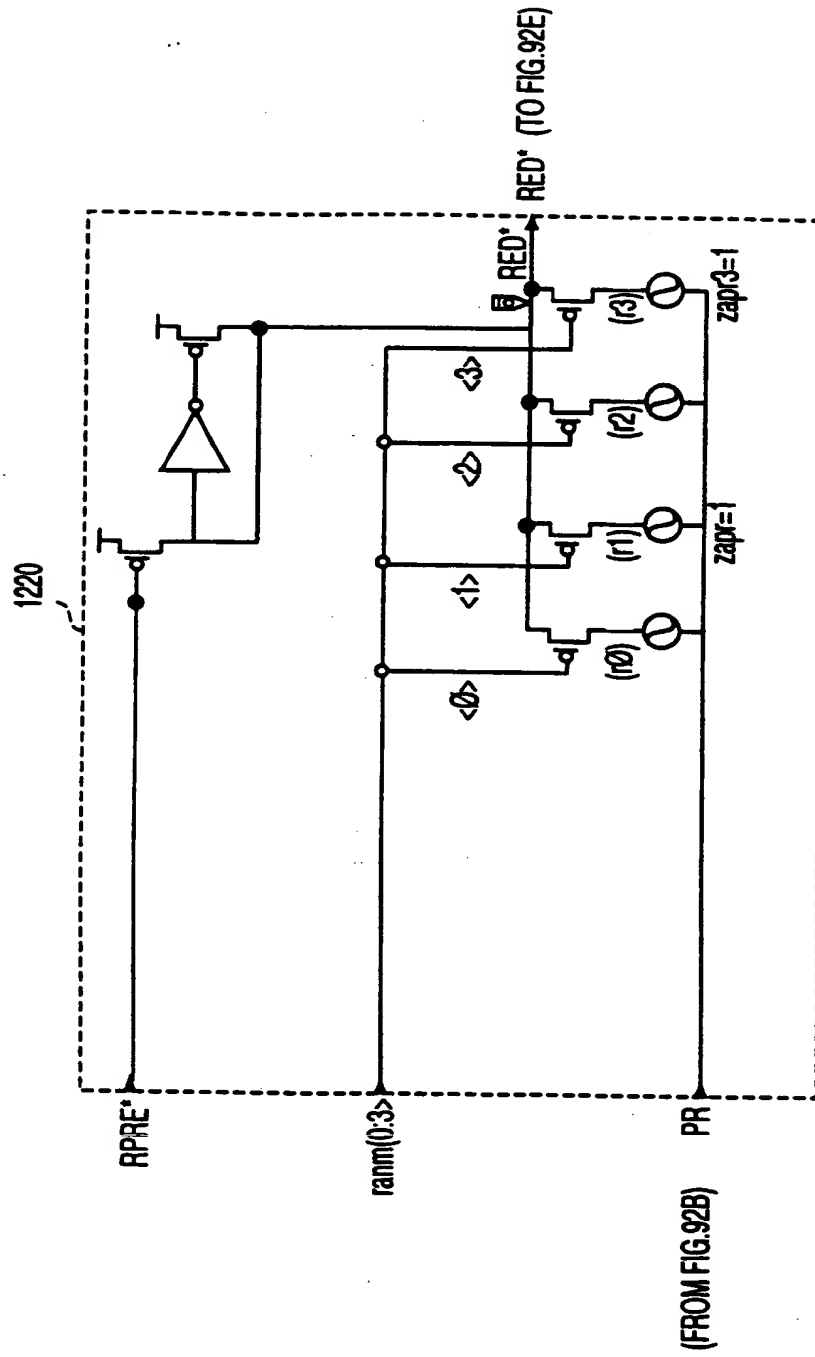


FIG. 92A

FIG. 92B

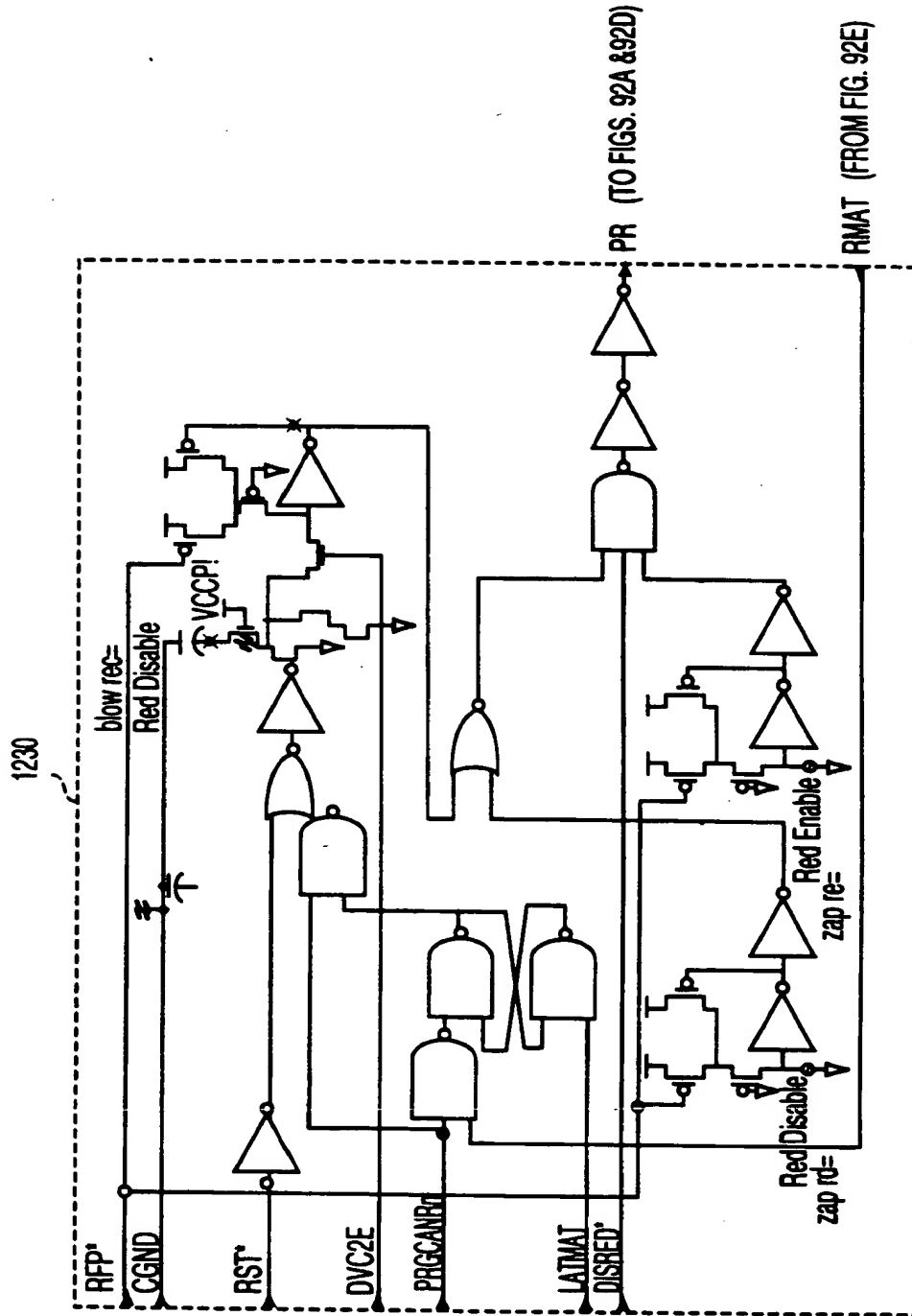


FIG. 92B

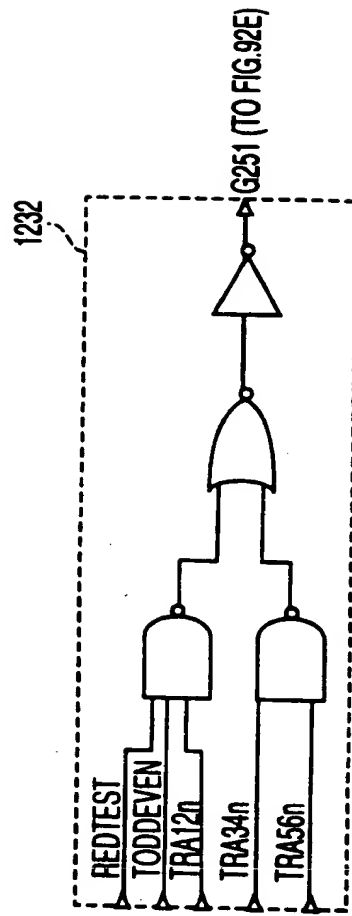


FIG. 92C

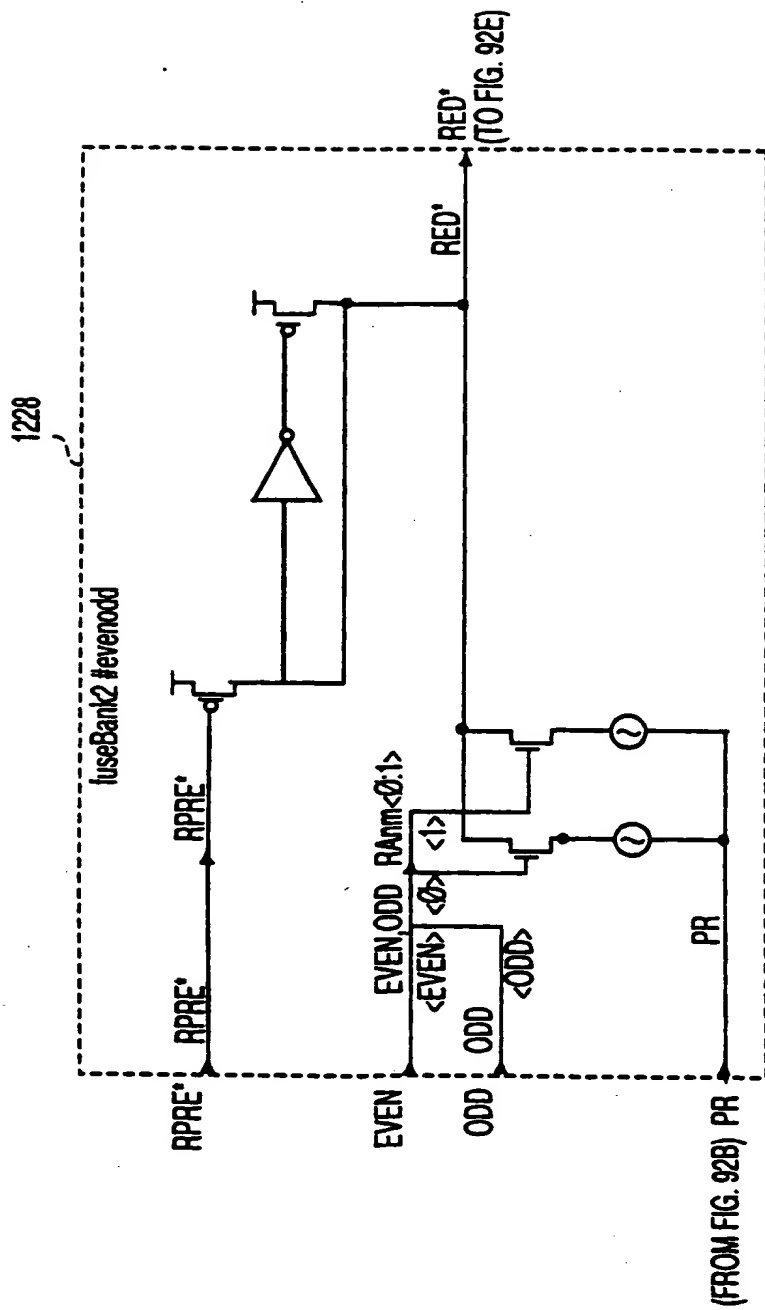


FIG. 92D

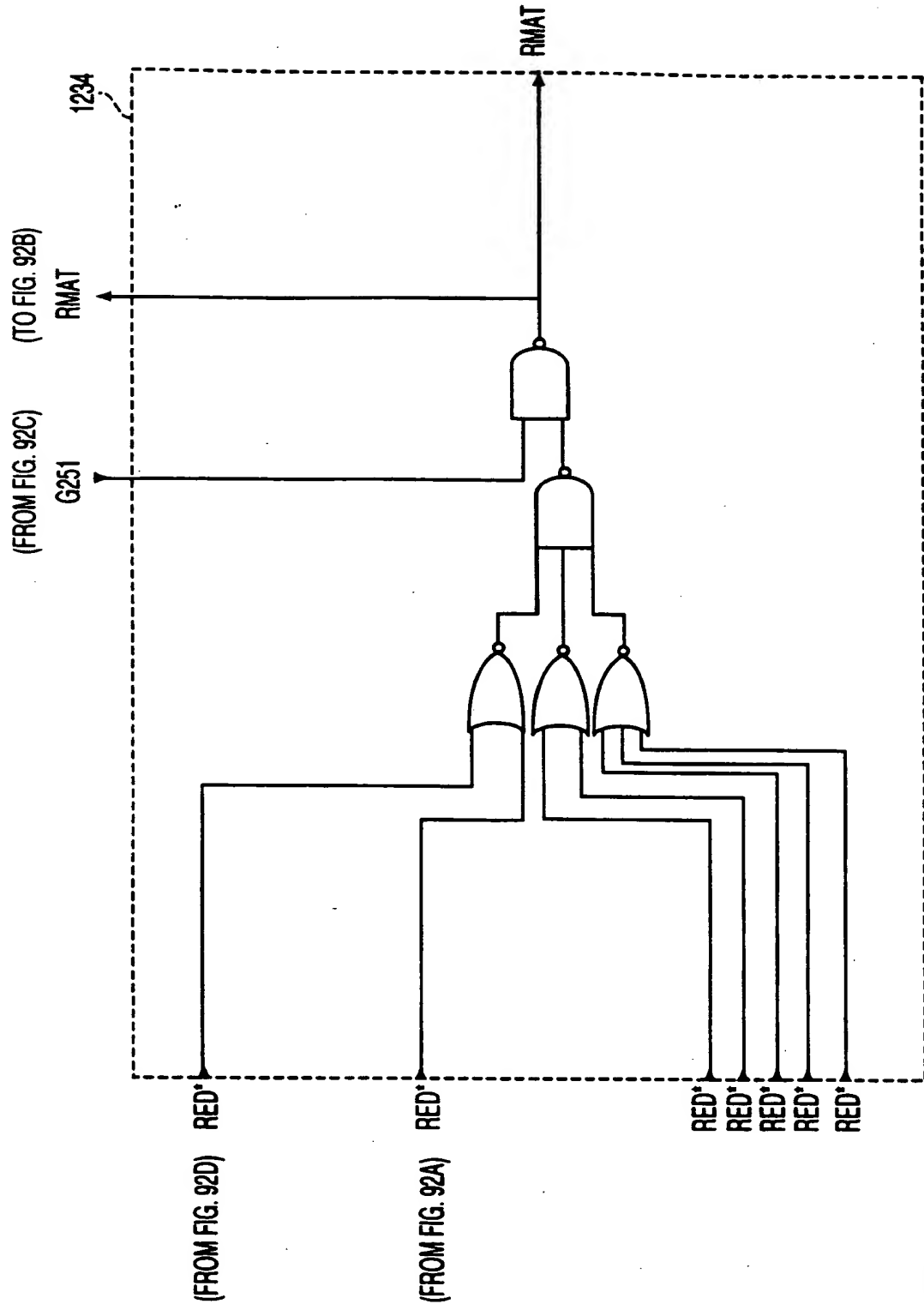
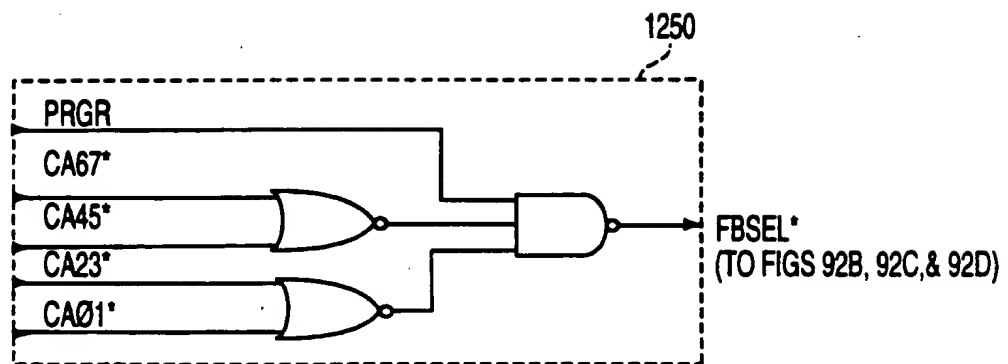
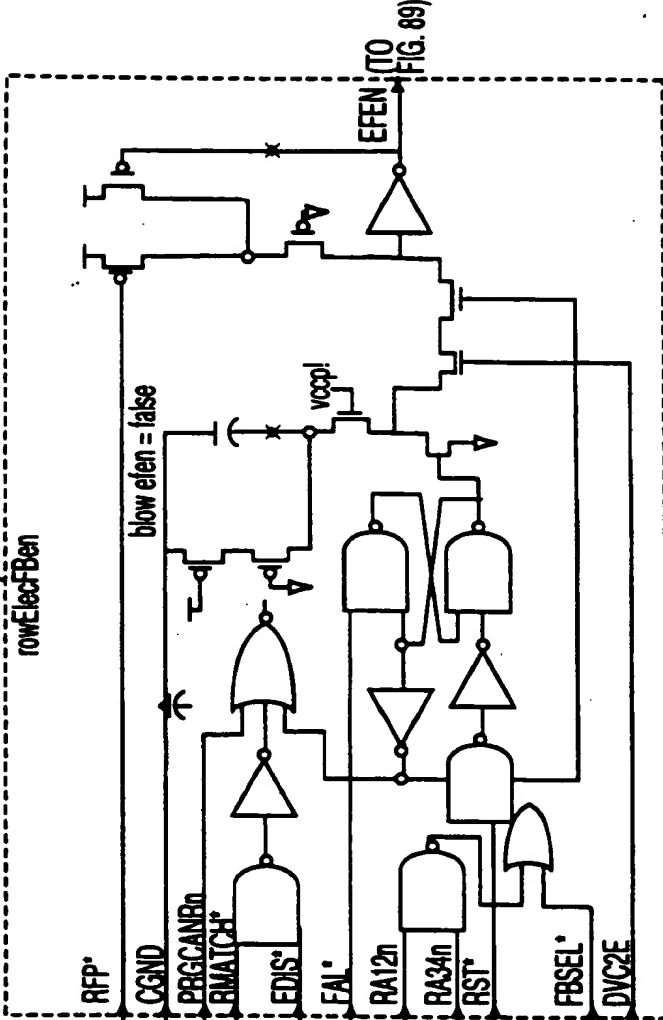


FIG. 92E

**FIG. 93A**

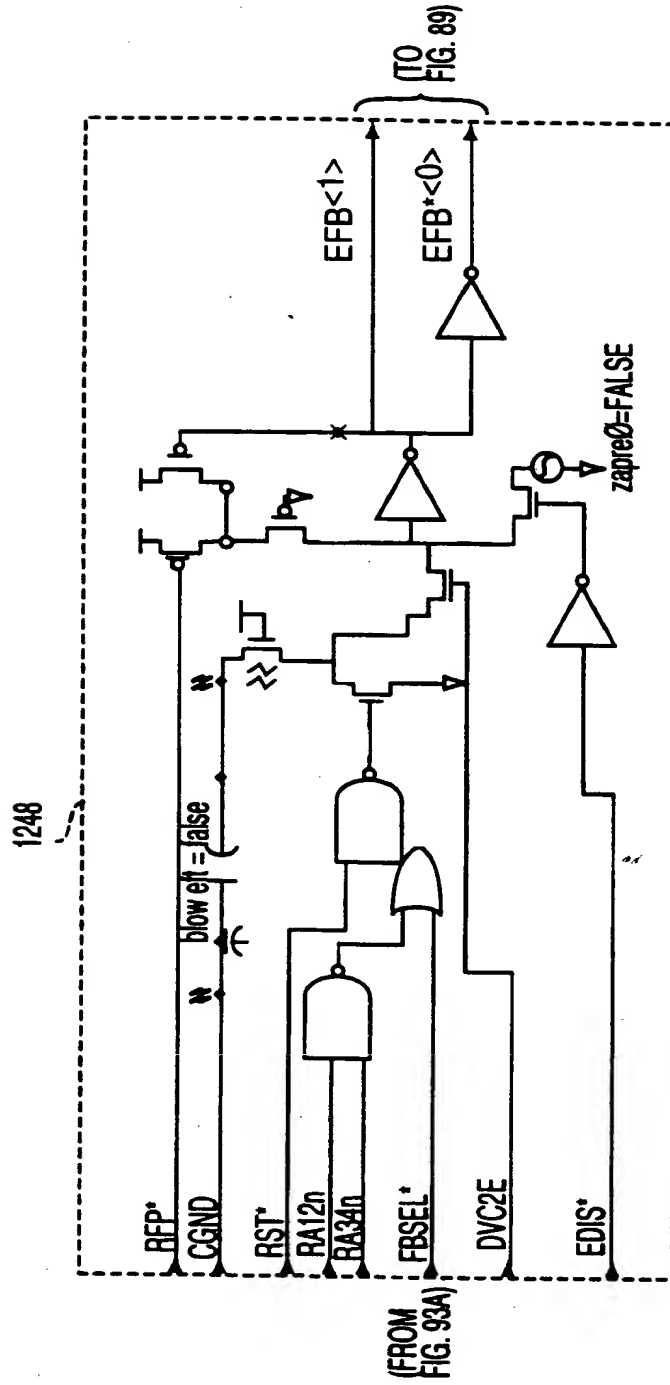
1252



(FROM
FIG. 93A)

$RA12<0:3>$
 $RA34<0:3>$

FIG. 93B



1240

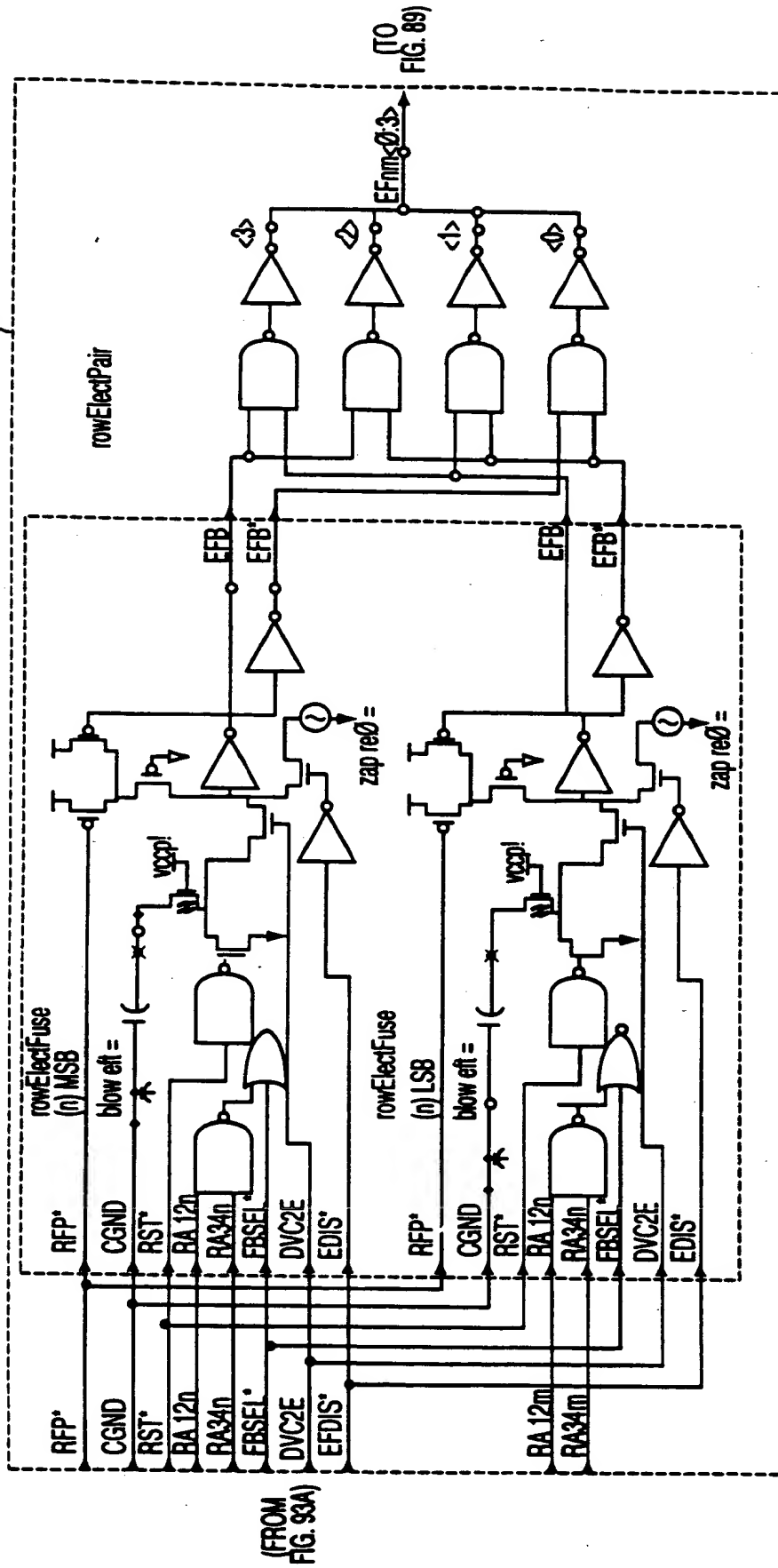
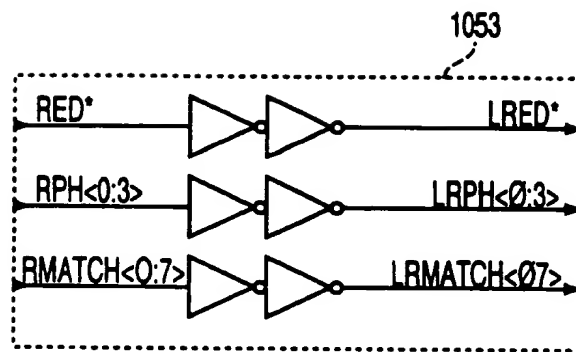


FIG. 93D

**FIG. 94**

1059

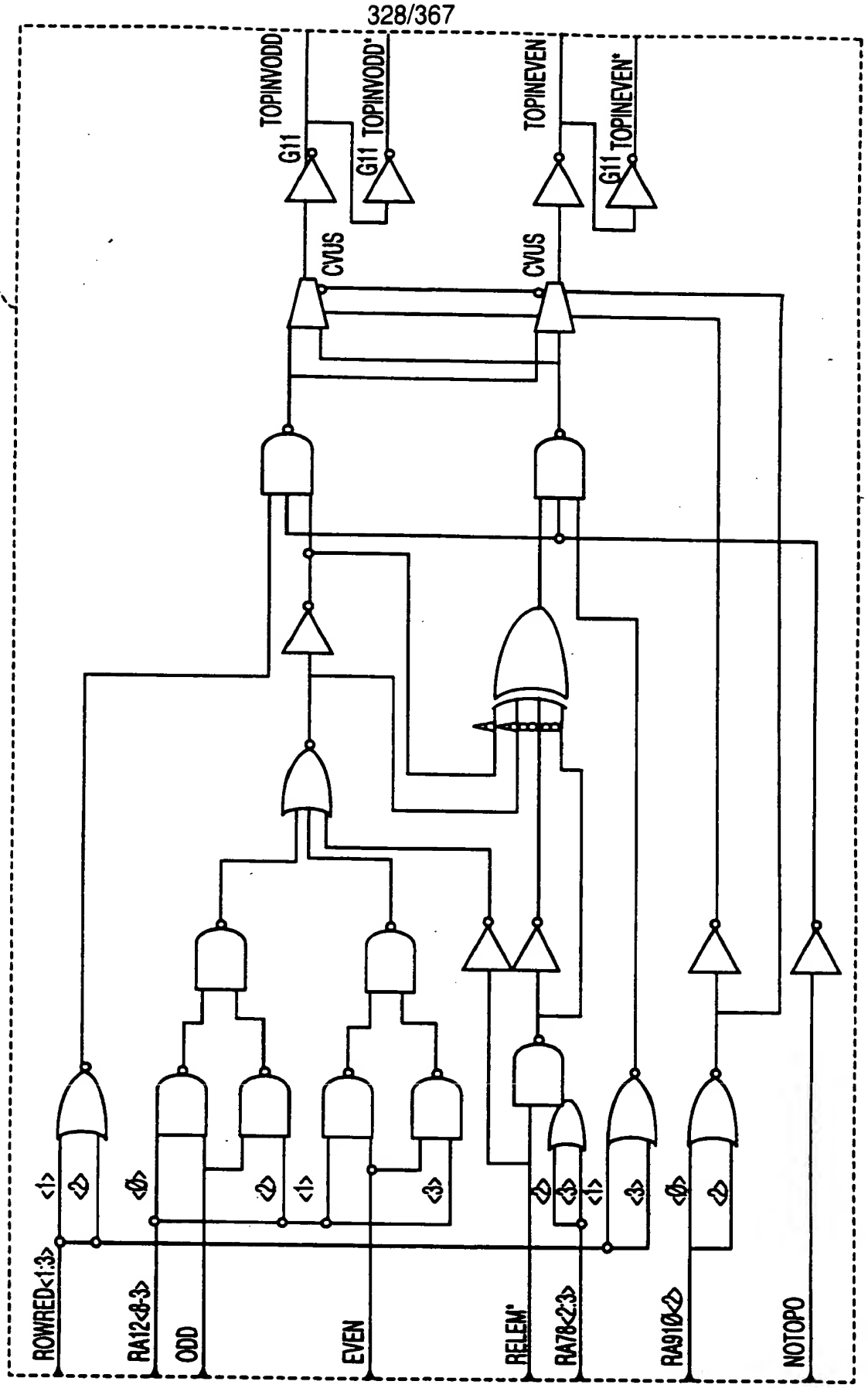


FIG. 95

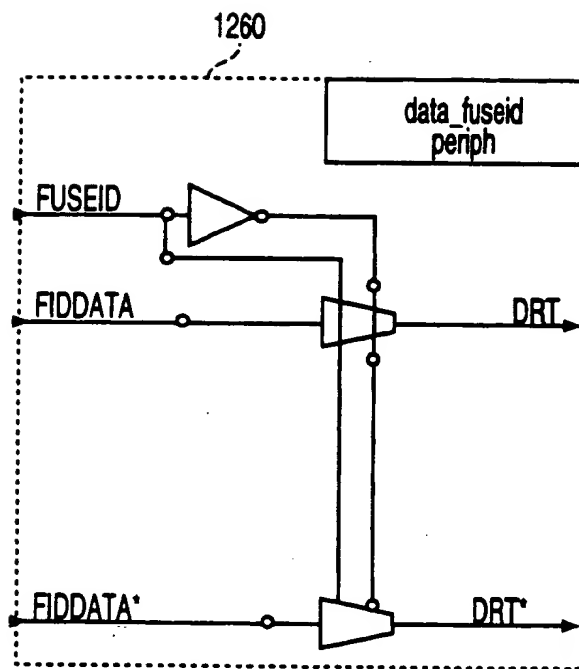
**FIG. 96**



FIG. 97-1 | FIG. 97-2

FIG. 97-1

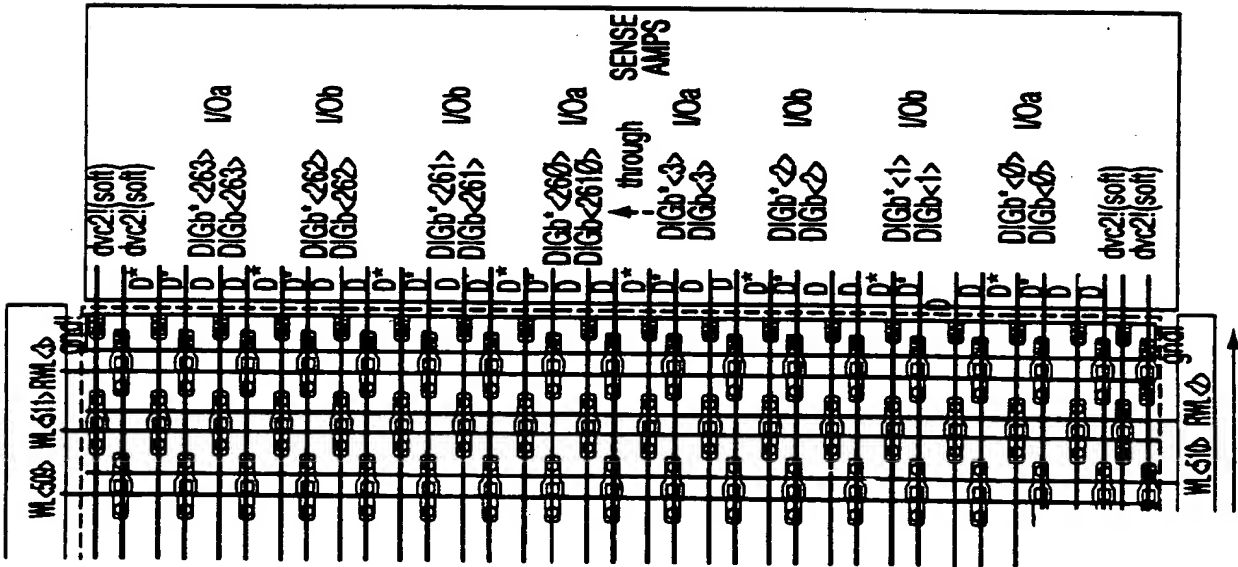
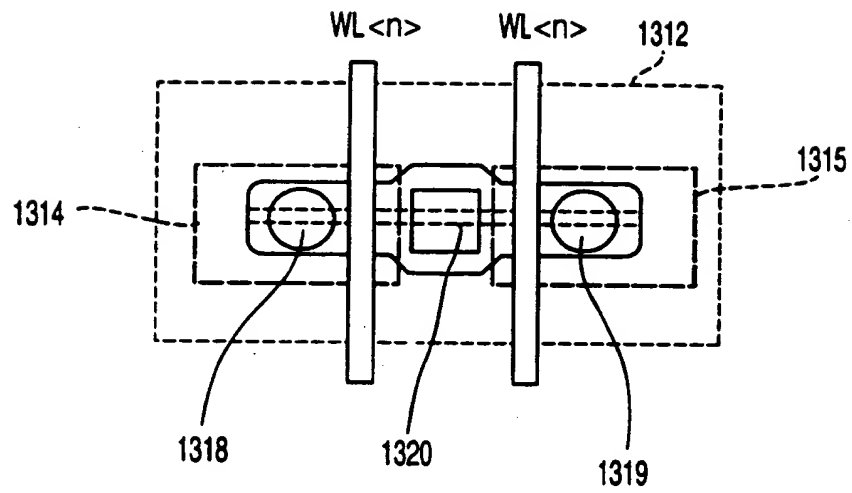


FIG. 97-1 FIG. 97-2

FIG. 97-2

**FIG. 98**

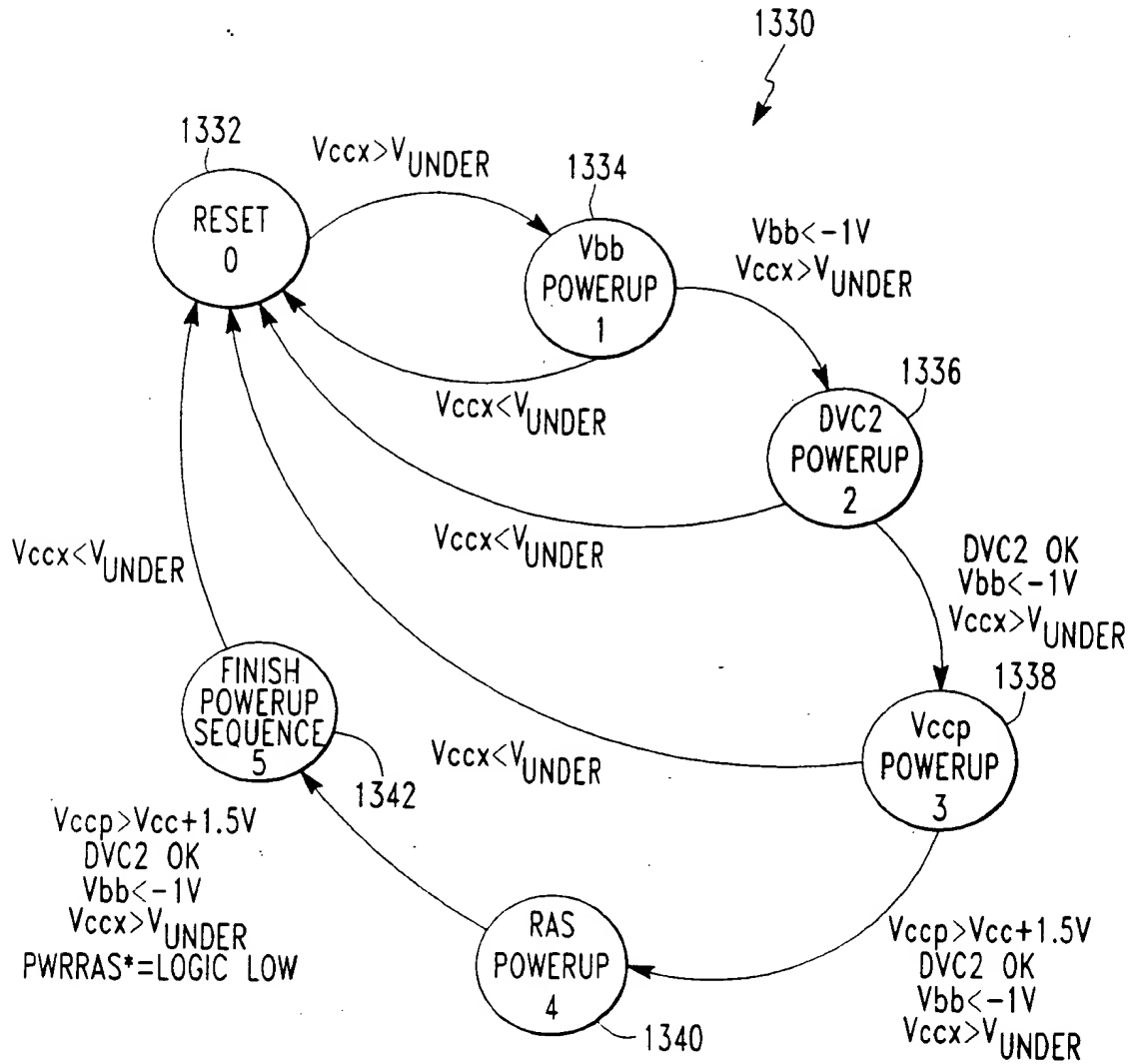


FIG. 99

1348

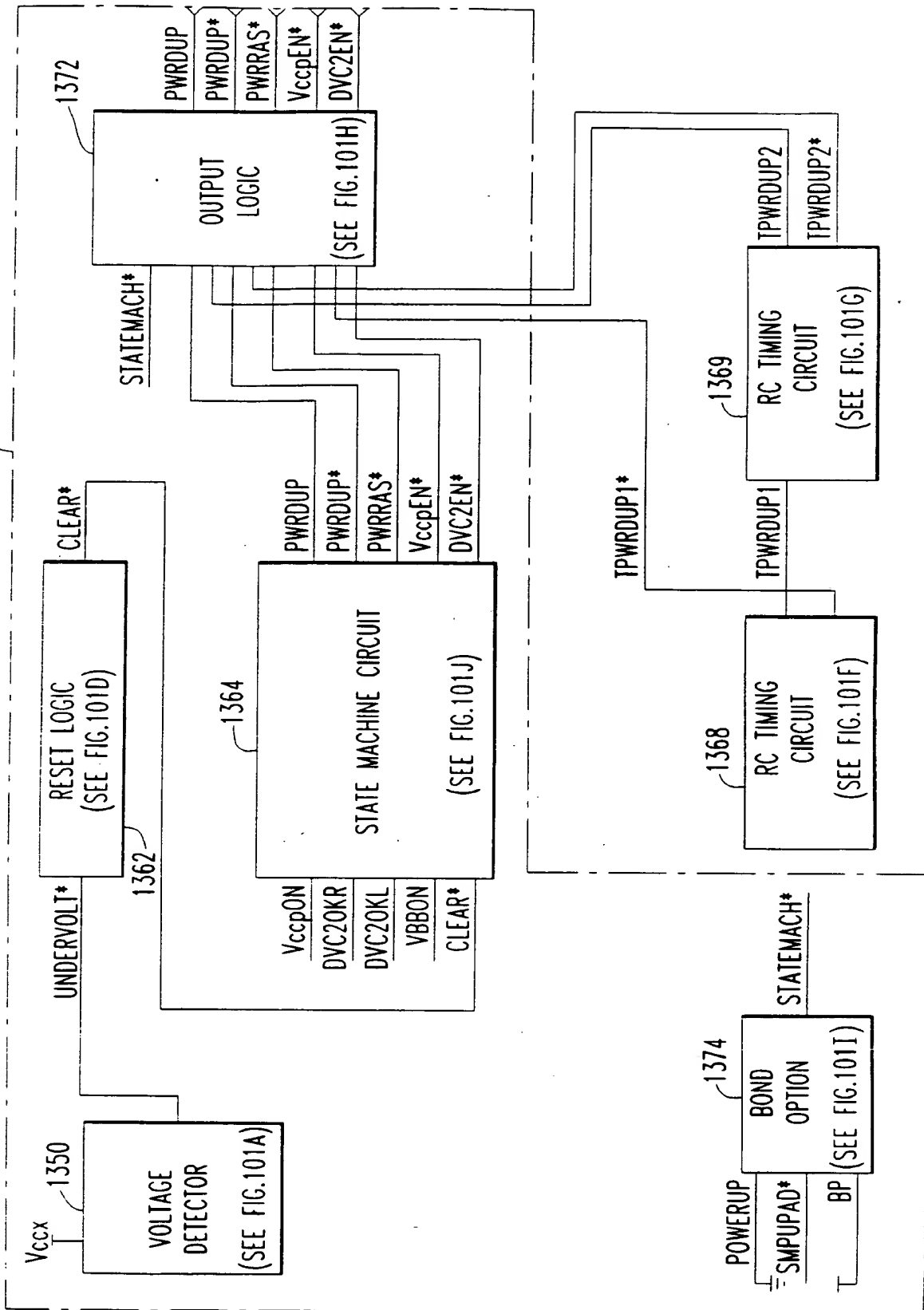


FIG. 100

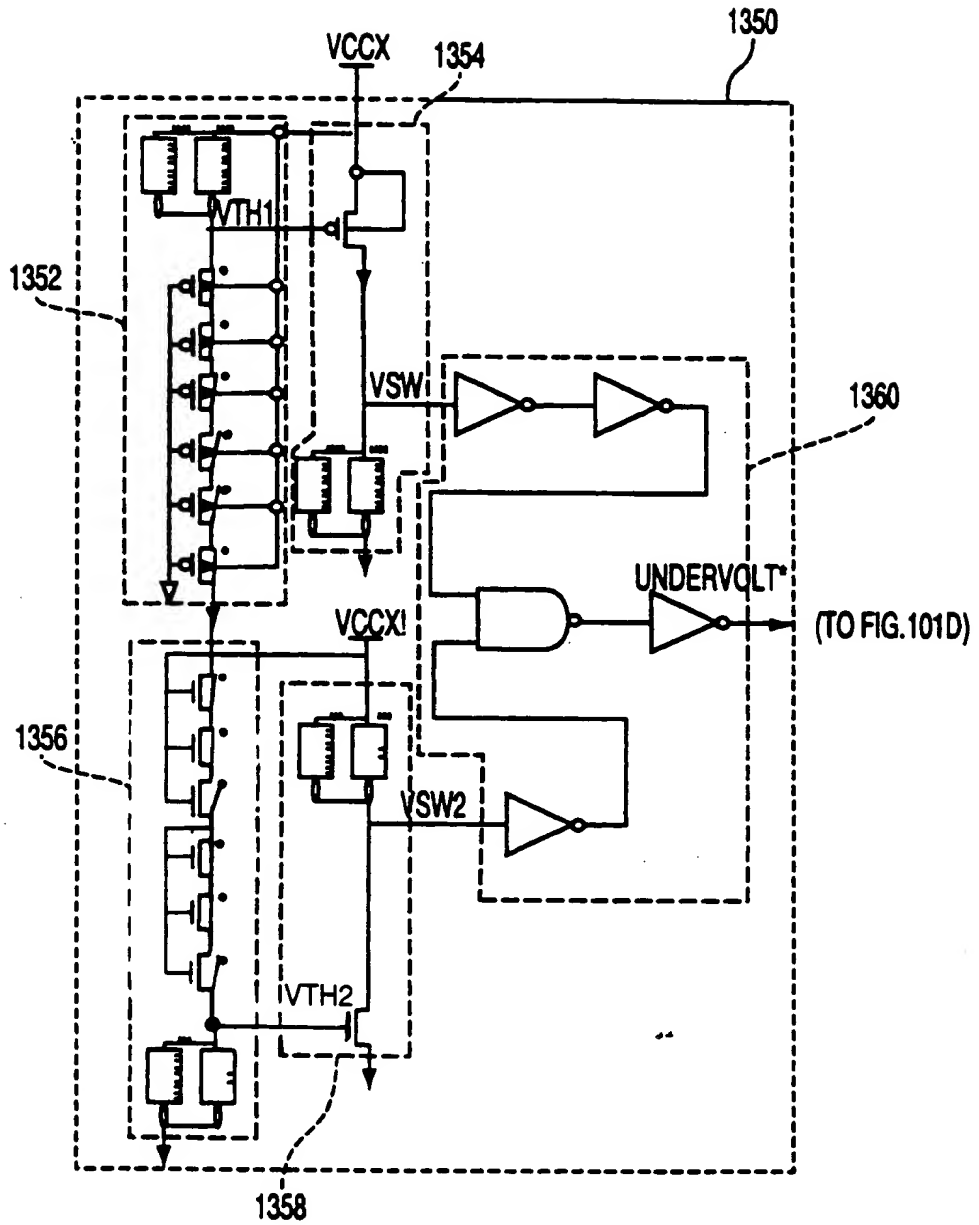
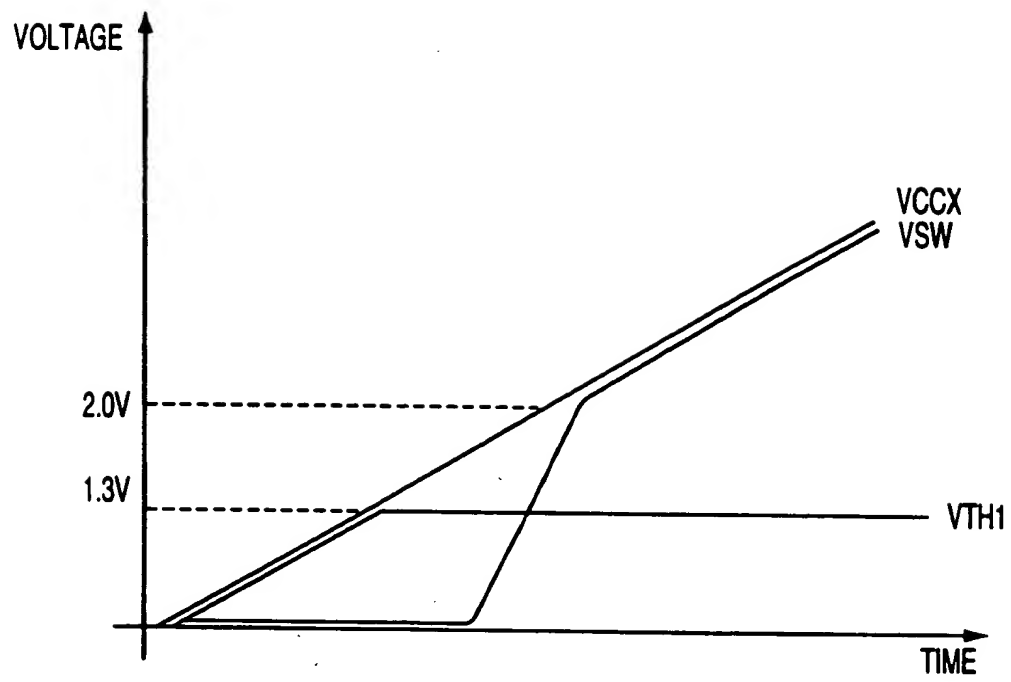
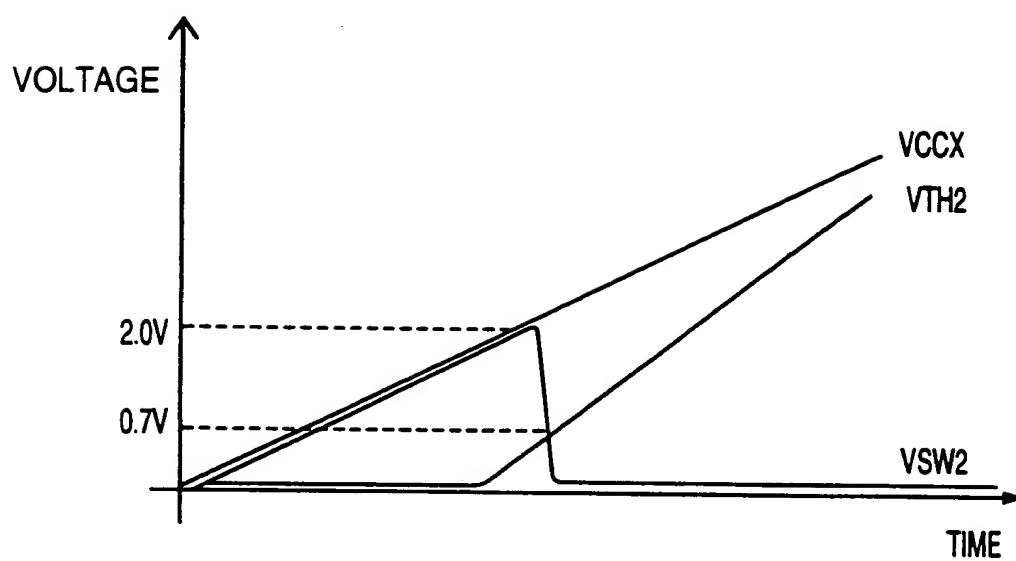


FIG. 101A

**FIG. 101B**

**FIG. 101C**

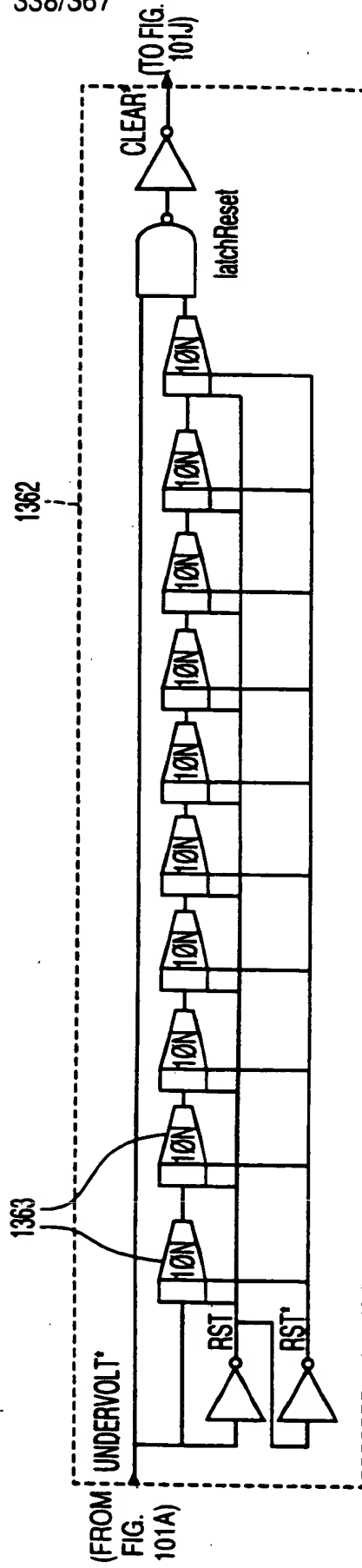


FIG. 101D

FIG. 101E

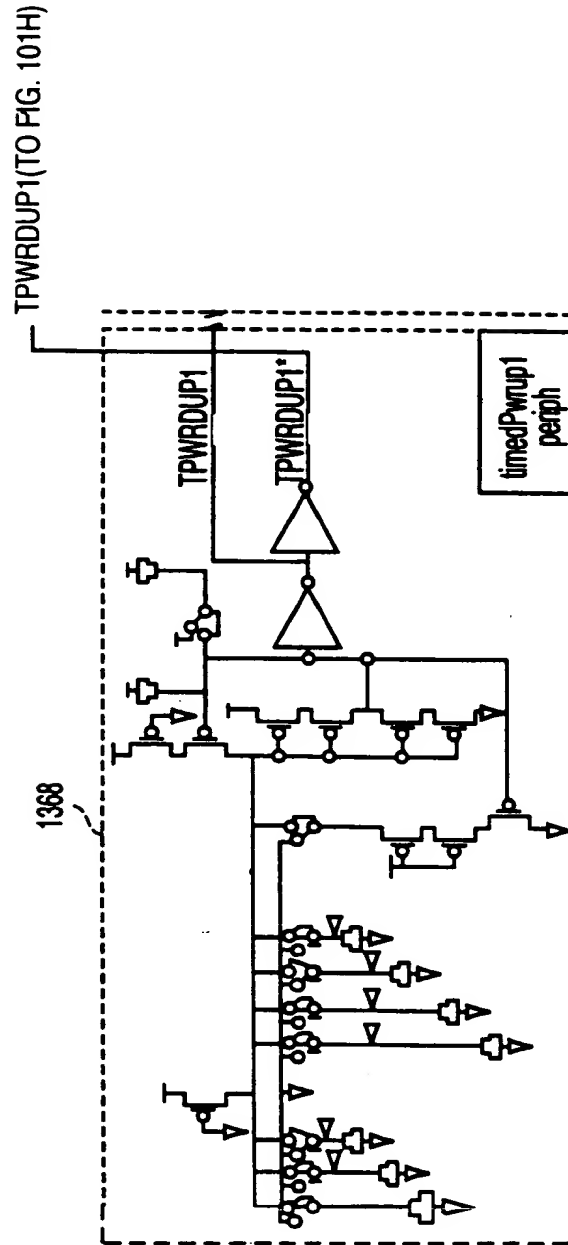


FIG. 101F

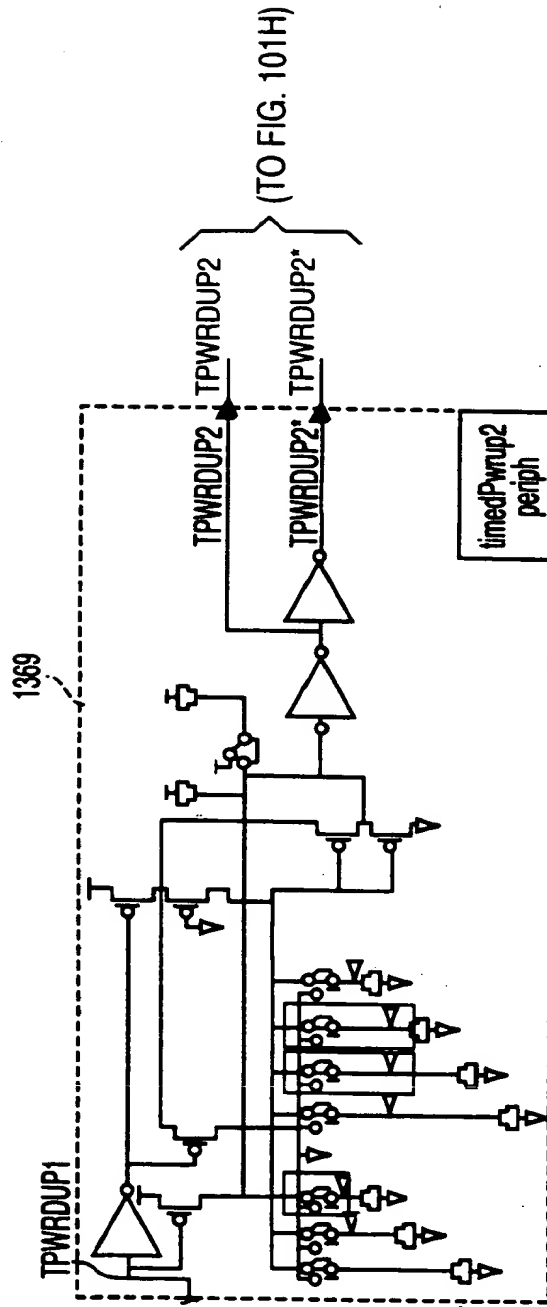
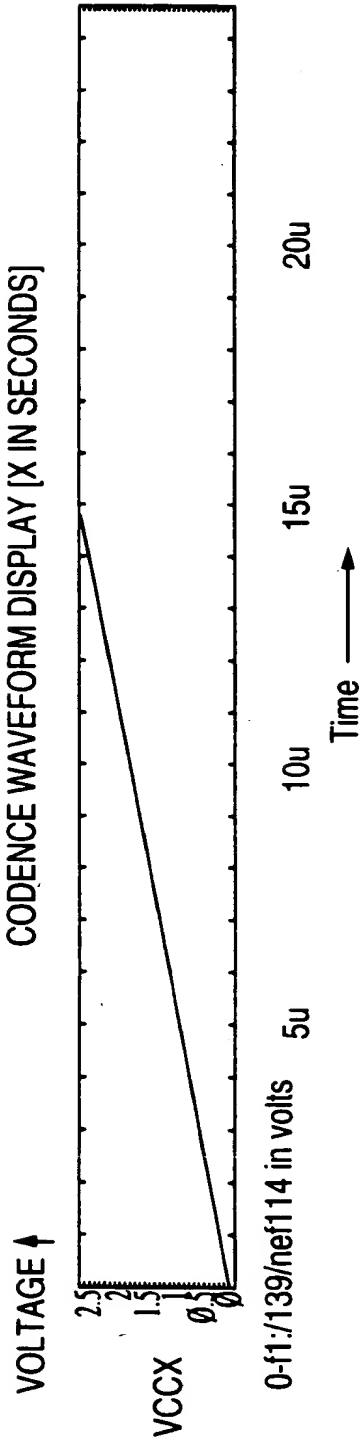
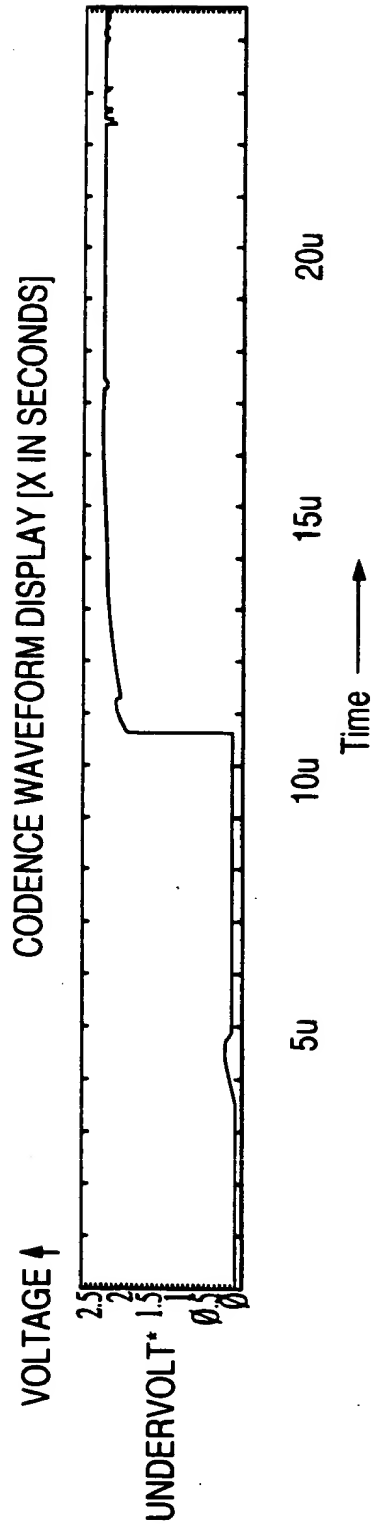


FIG. 101G



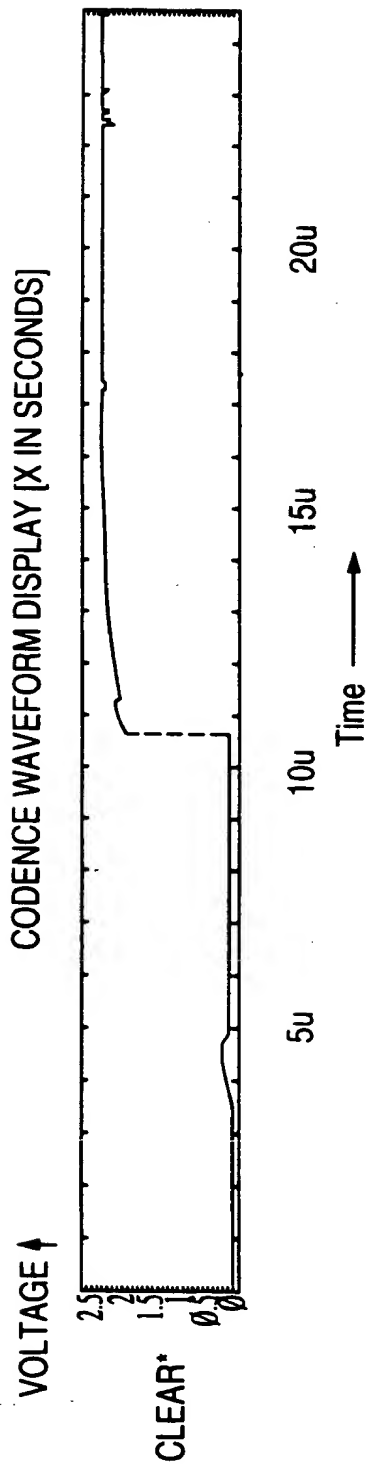
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102A



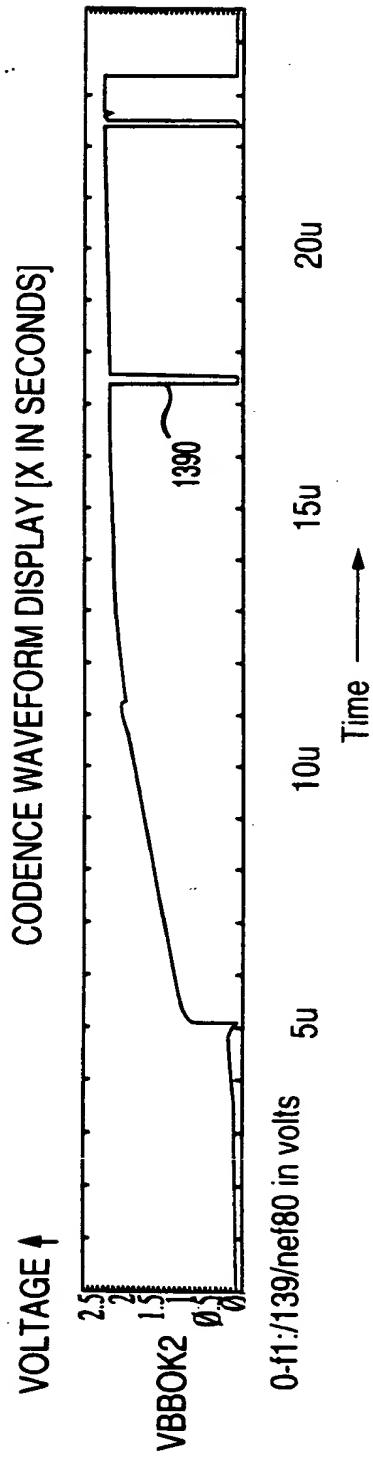
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102B



NOTE THAT PWRRAS* WAITED FOR DVC20KR

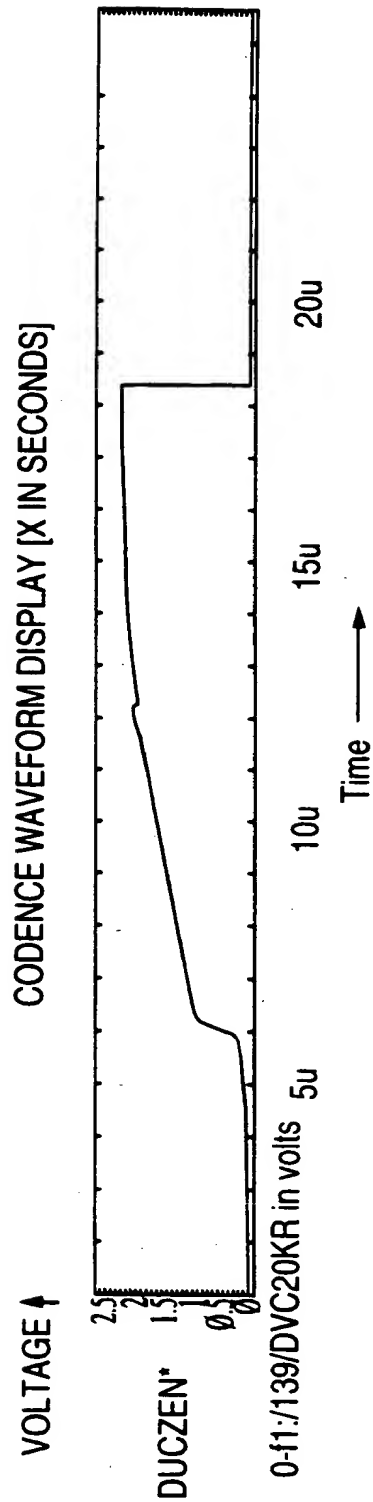
FIG. 102C



NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102D

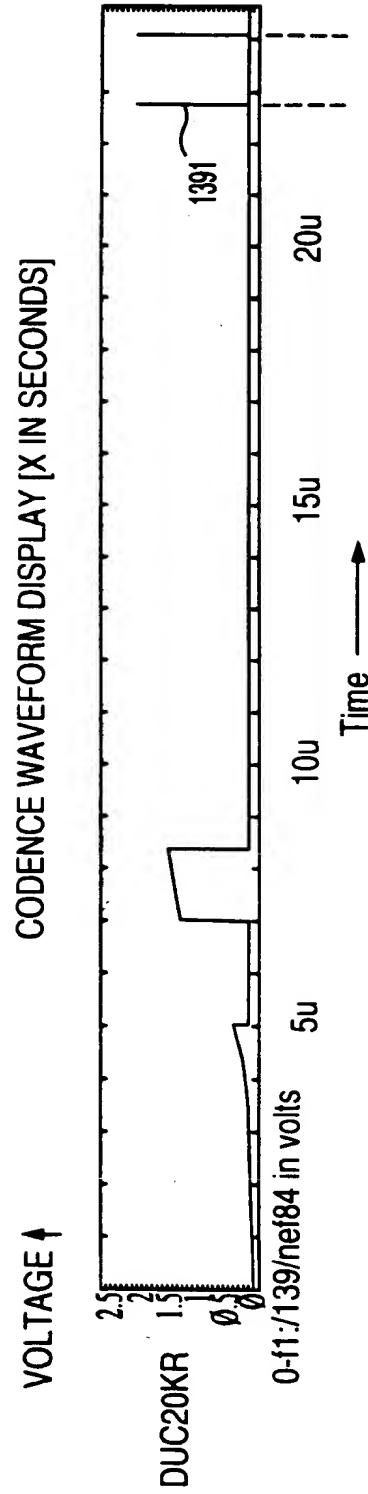
FO2280-56/4E66G



NOTE THAT PWRRAS* WAITED FOR DVC20KR

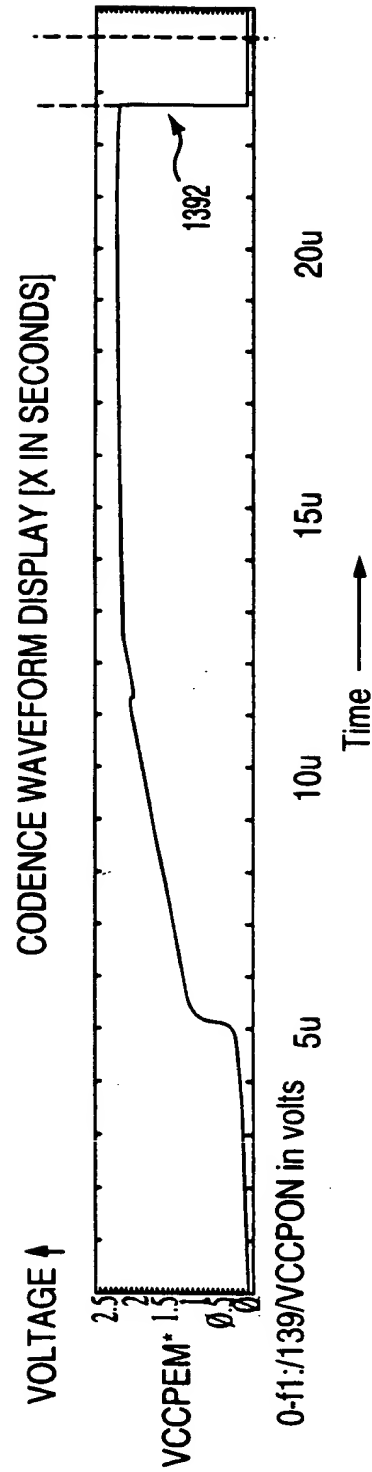
FIG. 102E

FIG. 102F



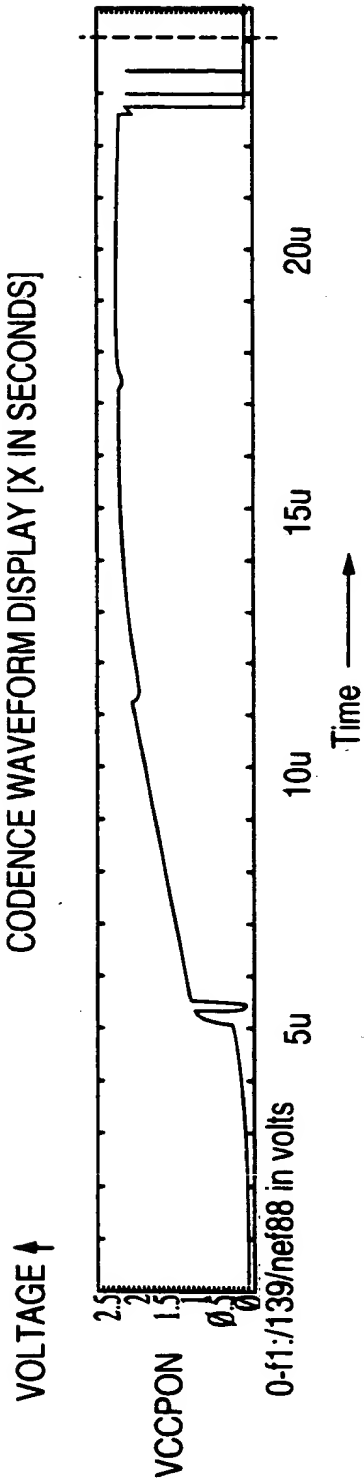
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102F



NOTE THAT PWRRAS* WAITED FOR DVC20KR

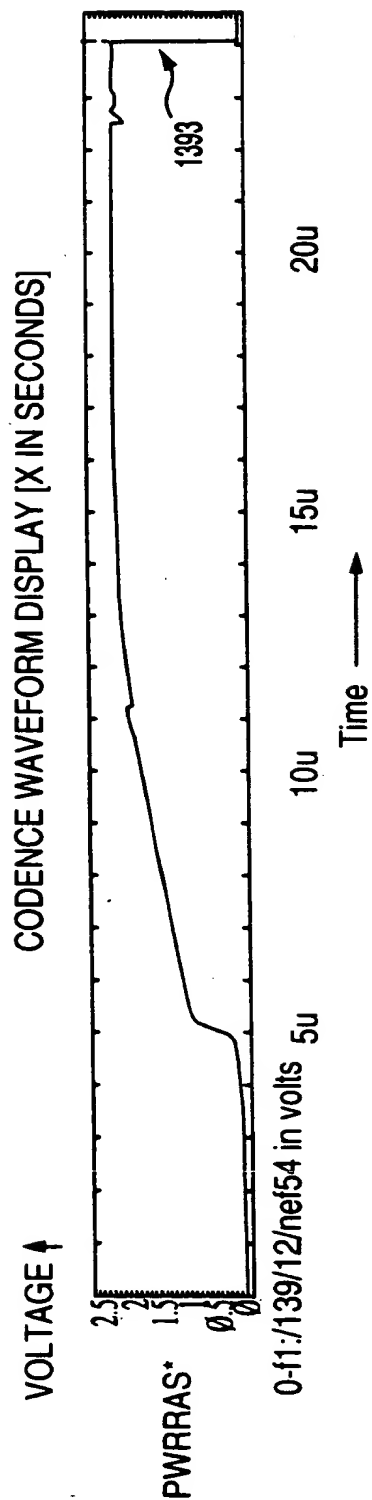
FIG. 102G



NOTE THAT PWRRAS* WAITED FOR DVC20KR

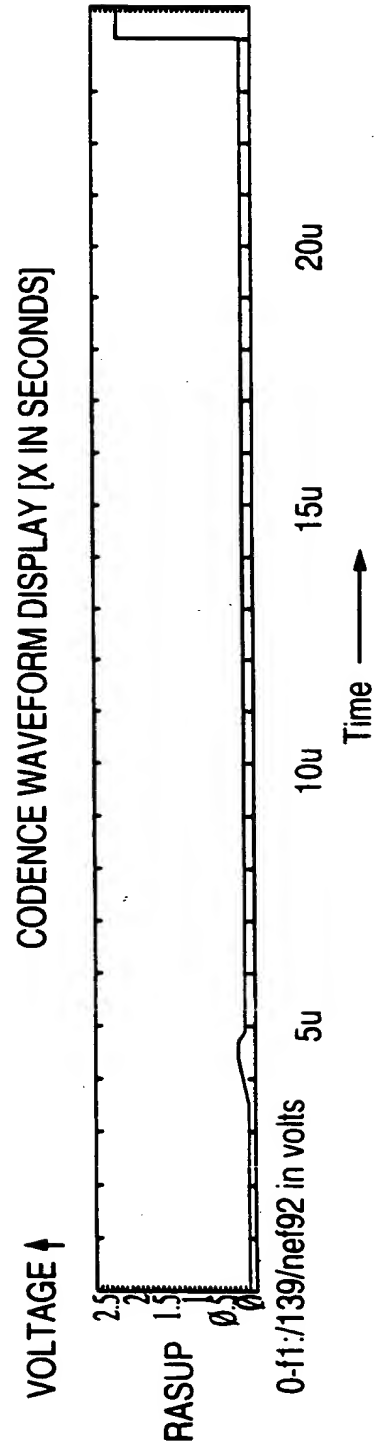
FIG. 102H

FORM 564E660



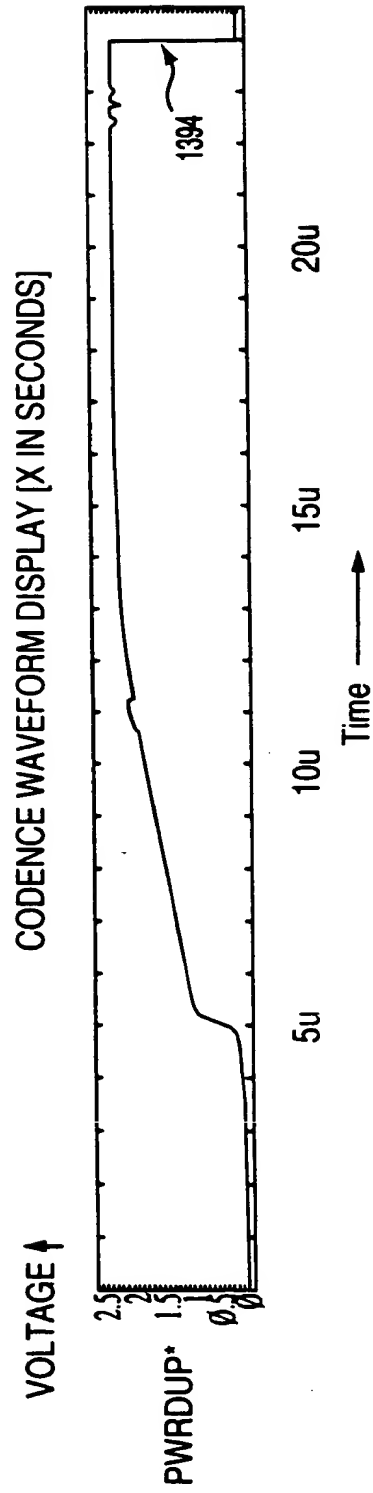
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102I



NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102J



NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102K

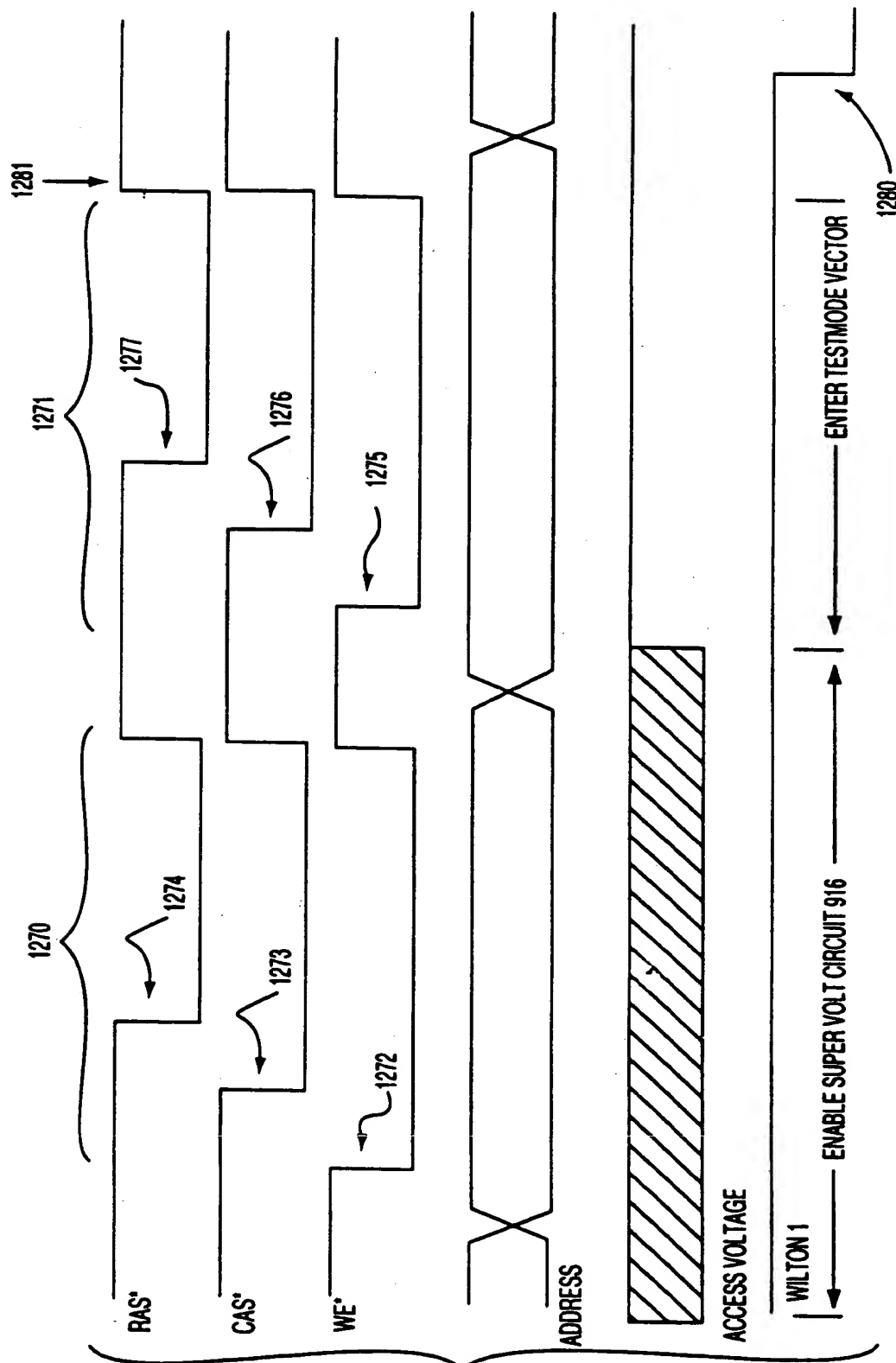
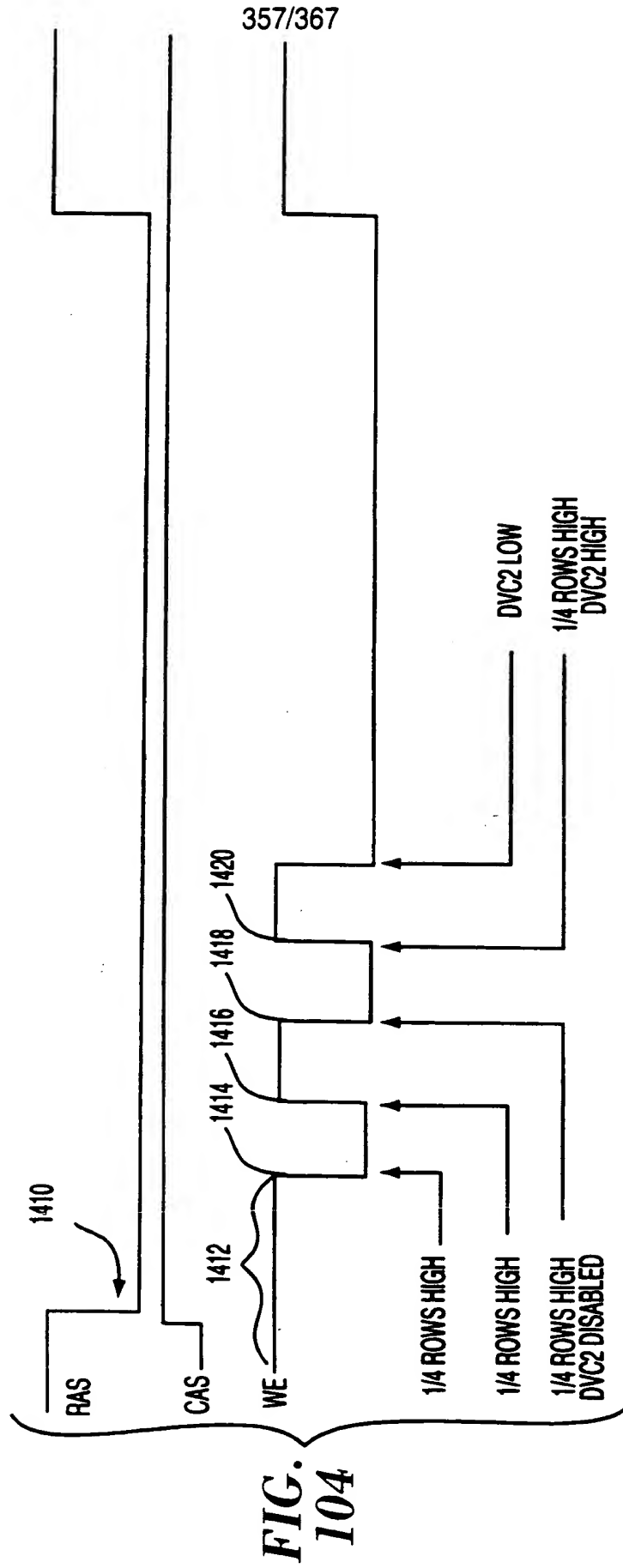


FIG.
103



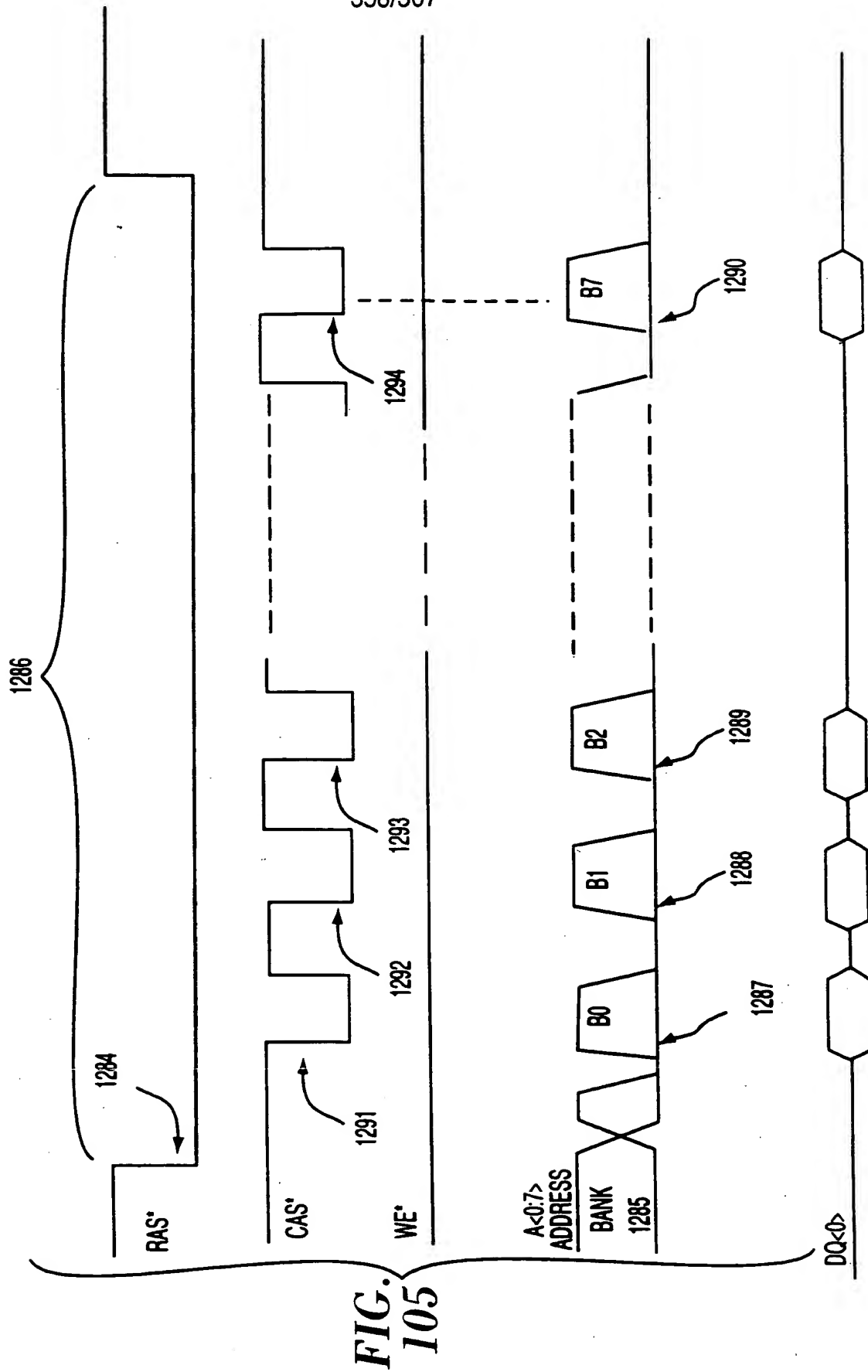


FIG. 106

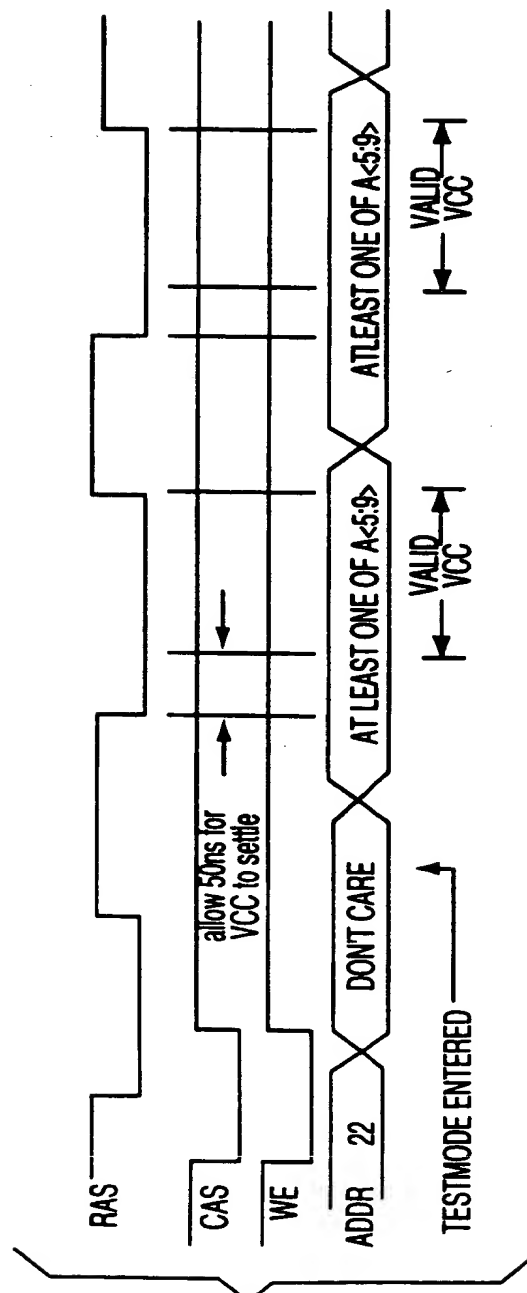
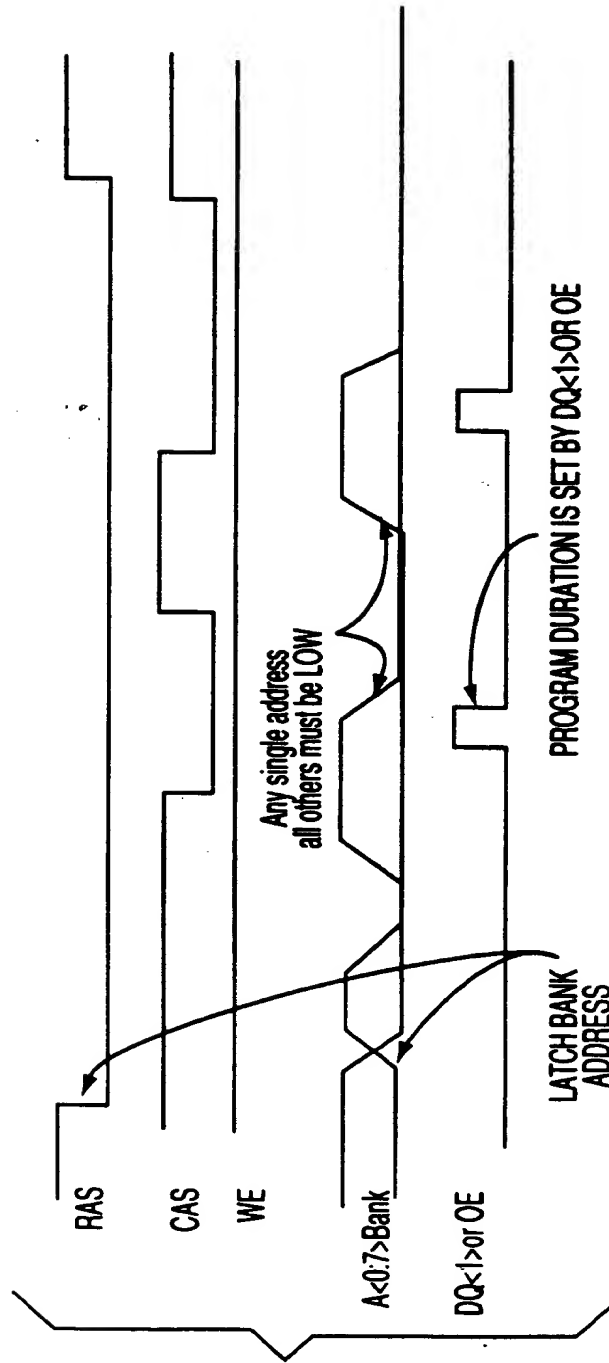


FIG. 106

FIG. 107



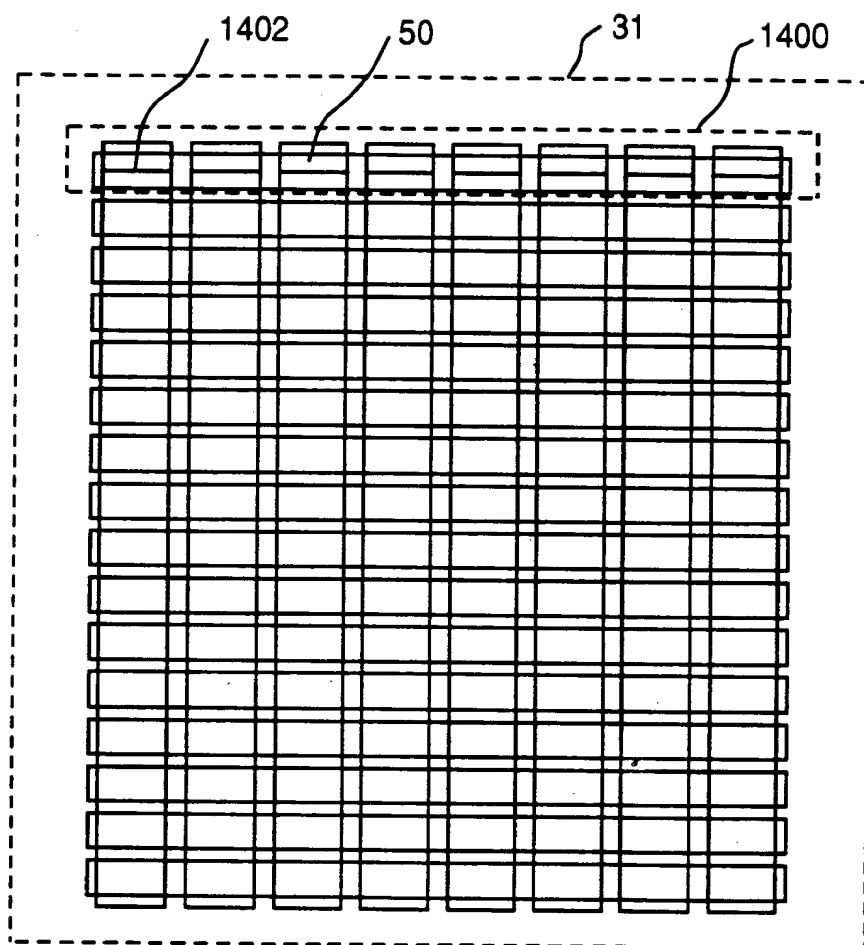
**FIG. 108**

FIG. 109-1

FIG. 109-1

FIG. 109-2

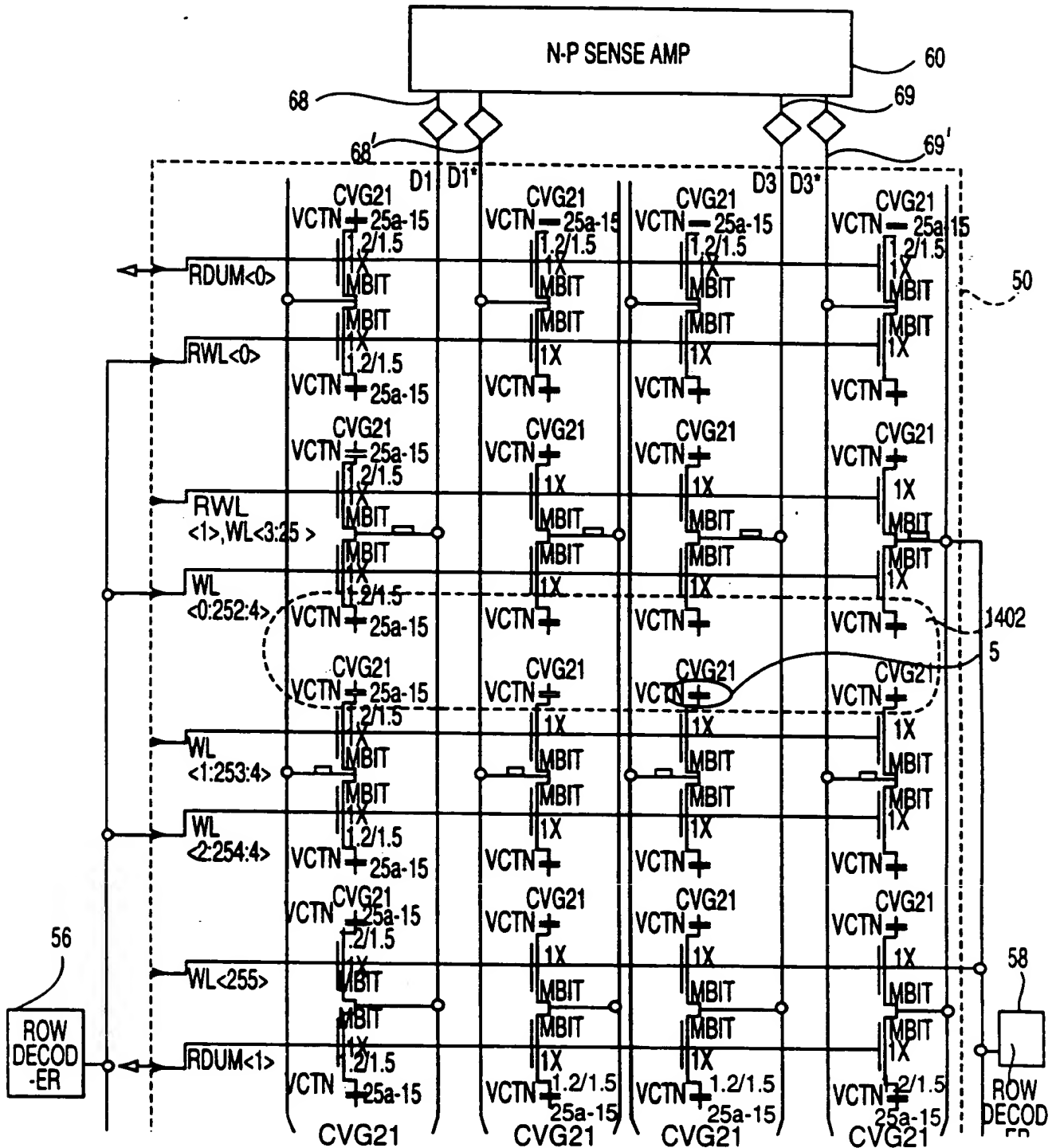


FIG. 109-2

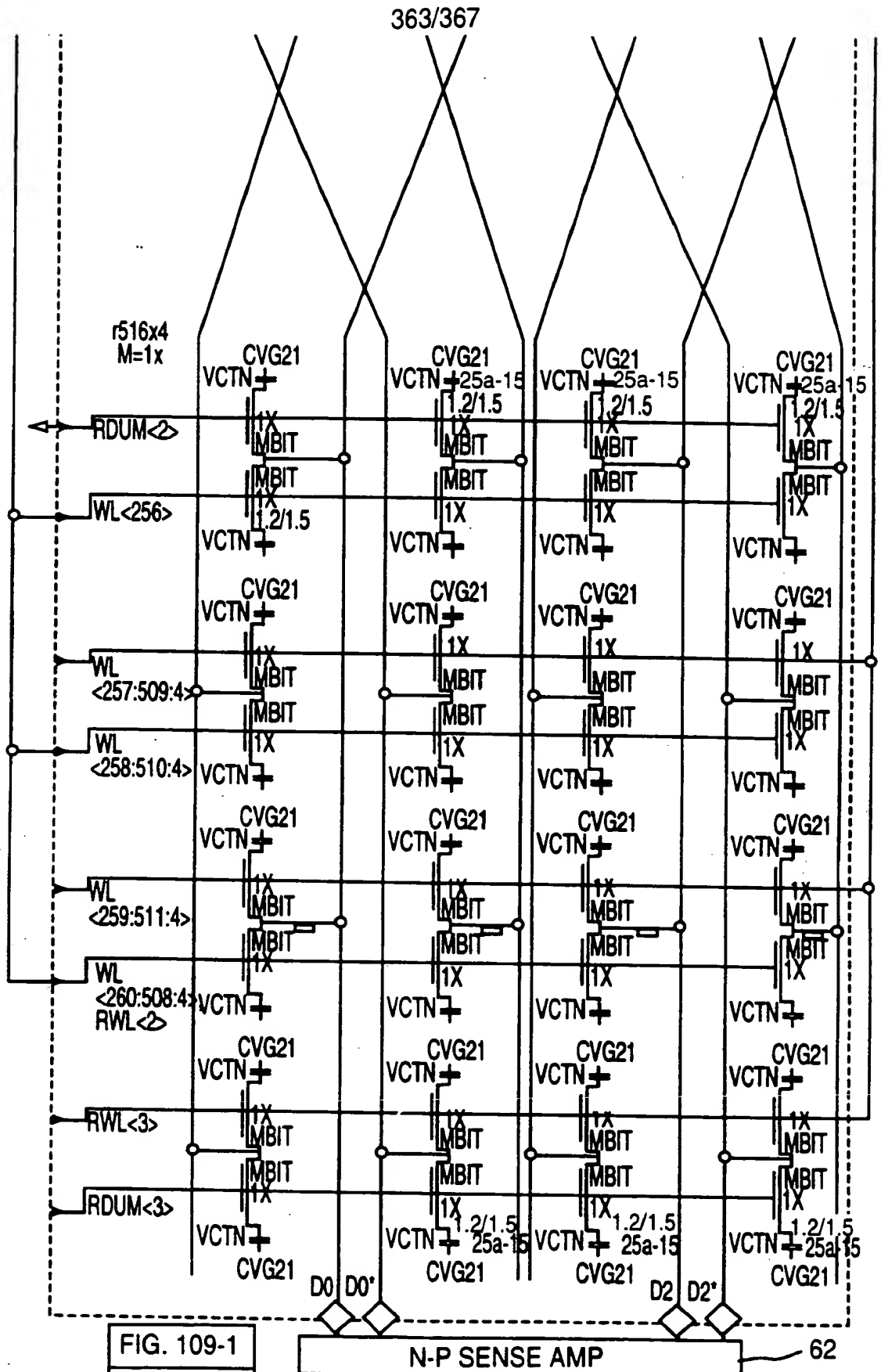


FIG. 109-1

FIG. 109-2

FIG. 109-2

1024 Actual Digits

Shrink Factor = 0.24
Mbit = 2.5 x 5.4 um (drawn)
0.600um Column Pitch
0.648 um Row Pitch

10

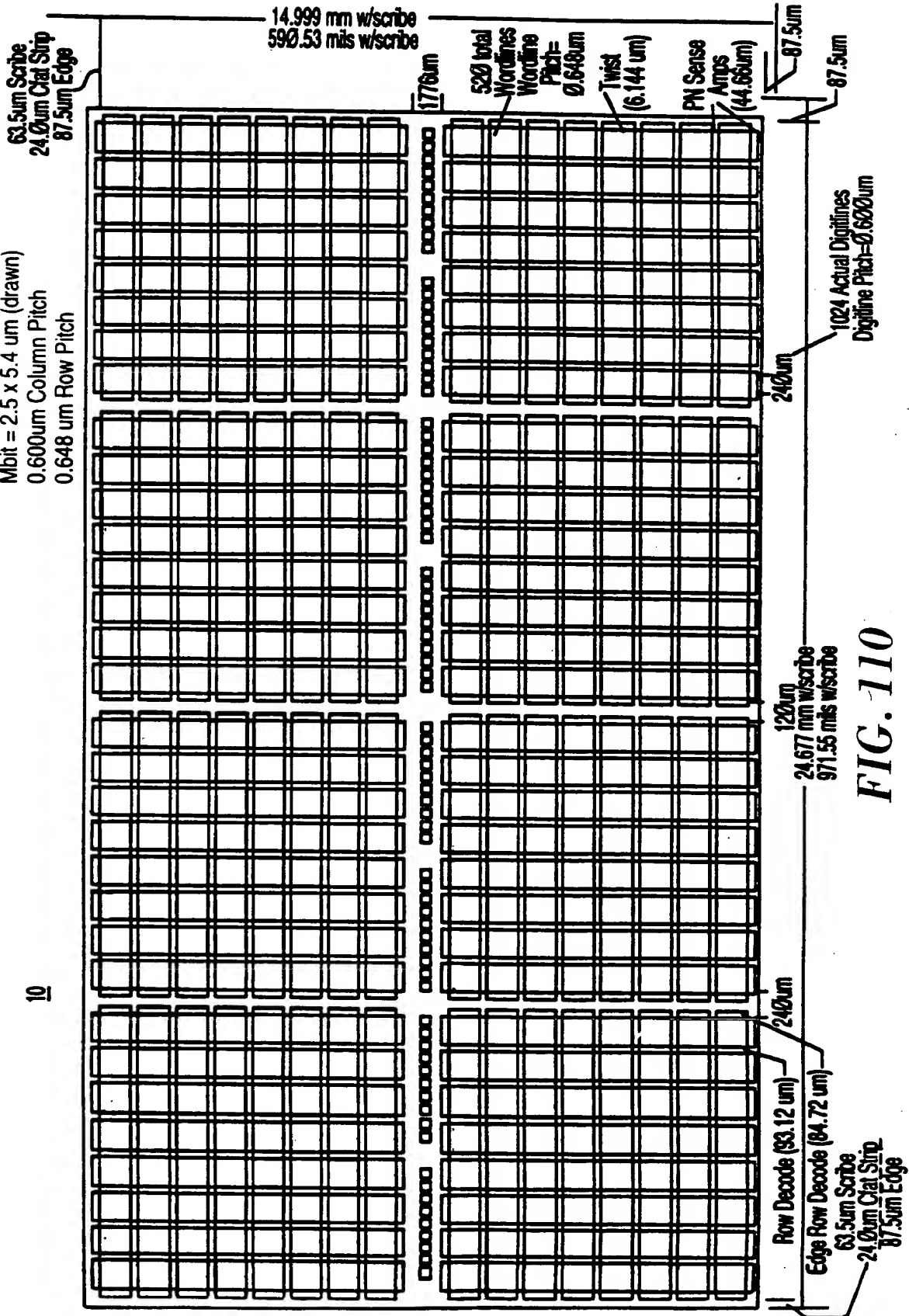


FIG. 110

364/367

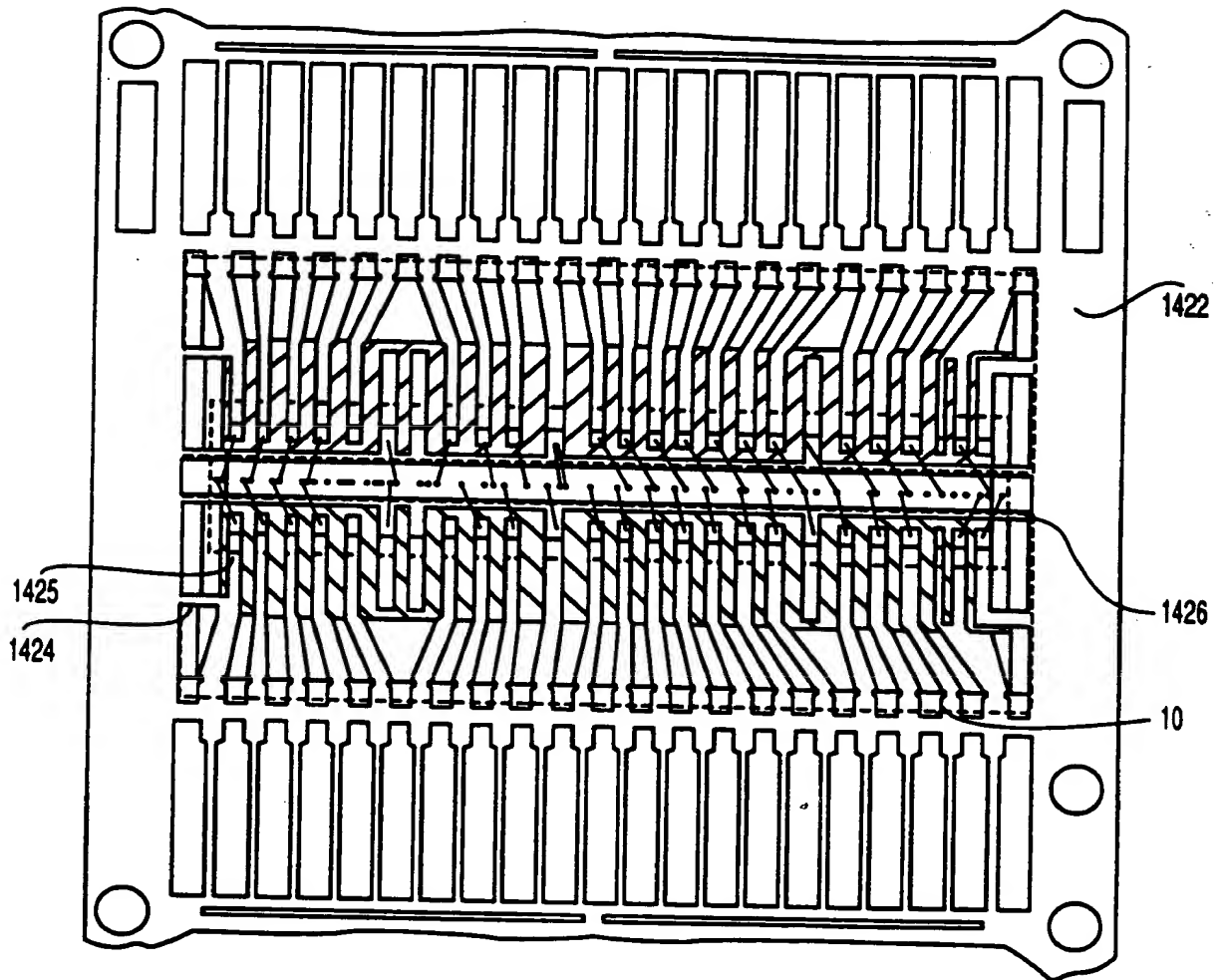


FIG. 111

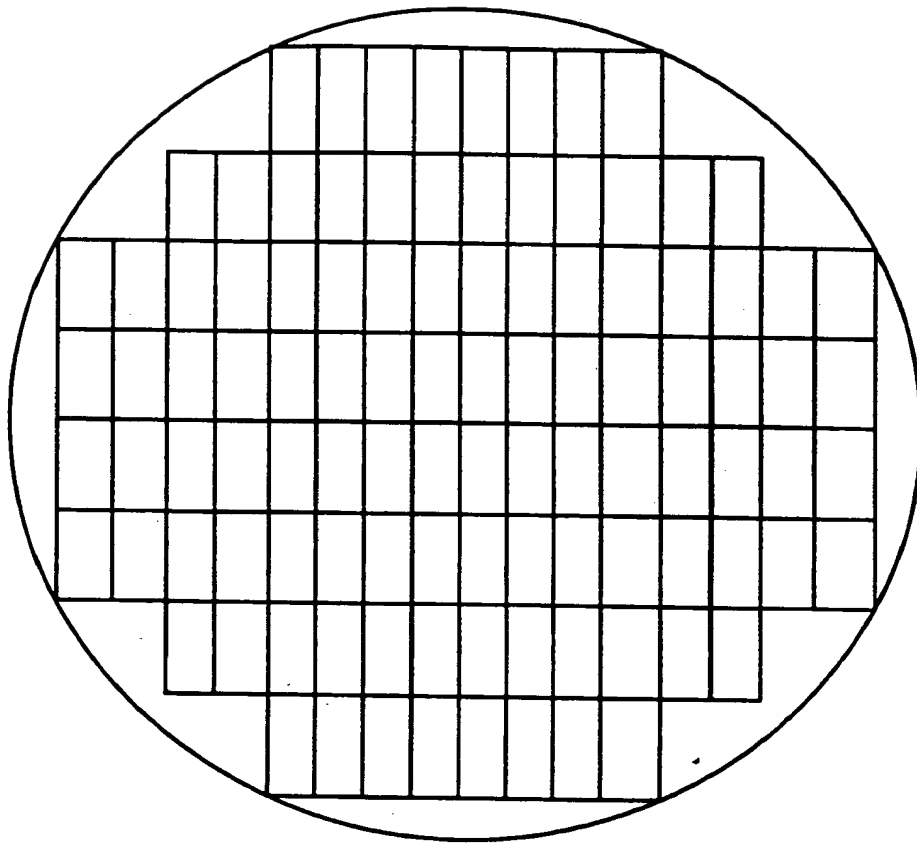


FIG. 112

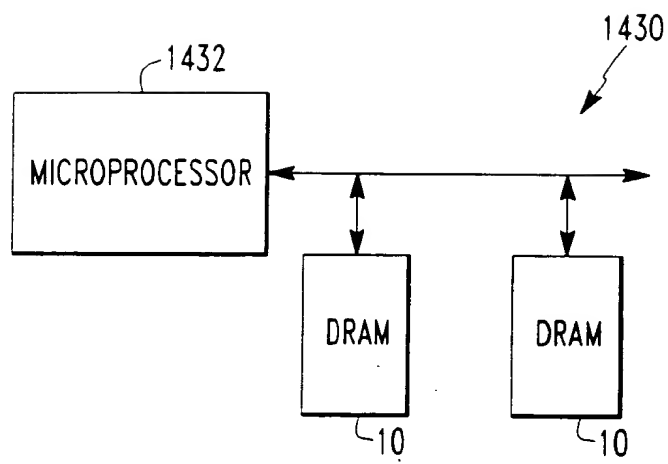


FIG. 113

FOR 2005-06-06